

Leakage Resilient Computation

Yevgeniy Vahlis

joint work with Ali Juma, Charles Rackoff



Crypto

Encryption
Box



Crypto

Encryption
Box

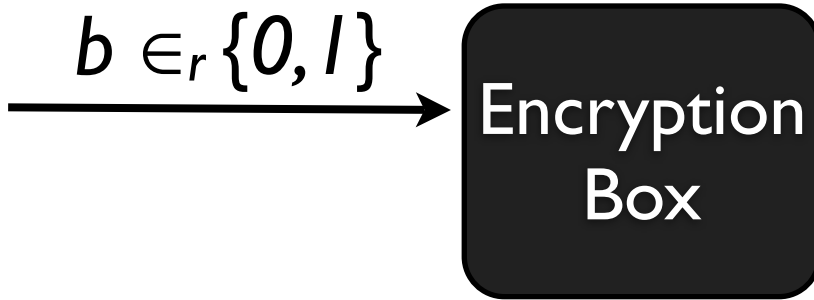
Message M_0
Message M_1



Crypto

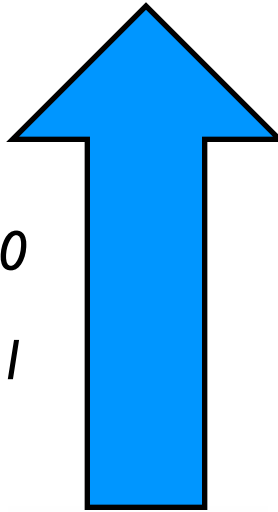


$b \in_r \{0, 1\}$



Encryption
Box

Message M_0
Message M_1



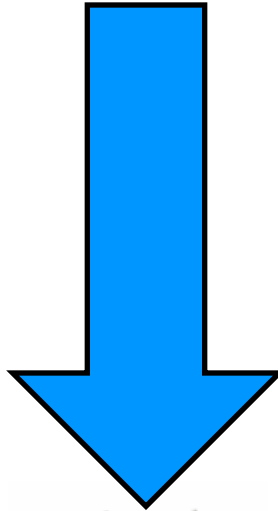
Crypto



$b \in_r \{0, 1\}$



Encryption
Box



Encryption of M_b



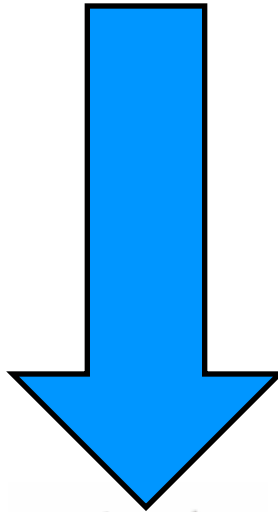
Crypto



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Encryption
Box

Encryption of M_b



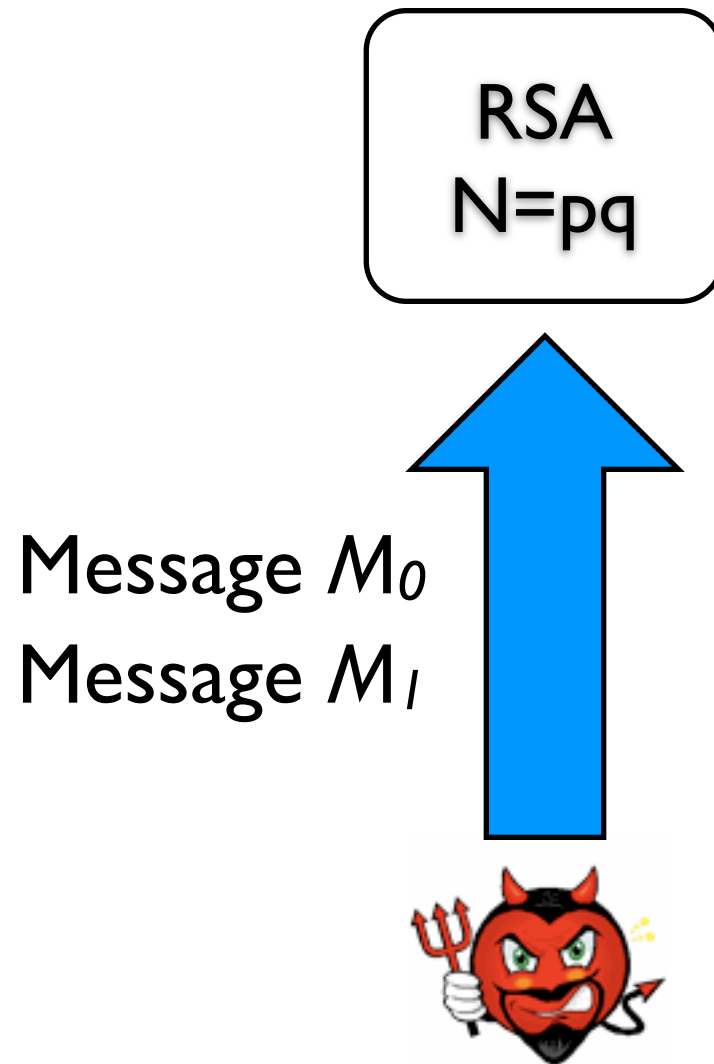
$b=?$

Crypto in Real Life

RSA
 $N=pq$



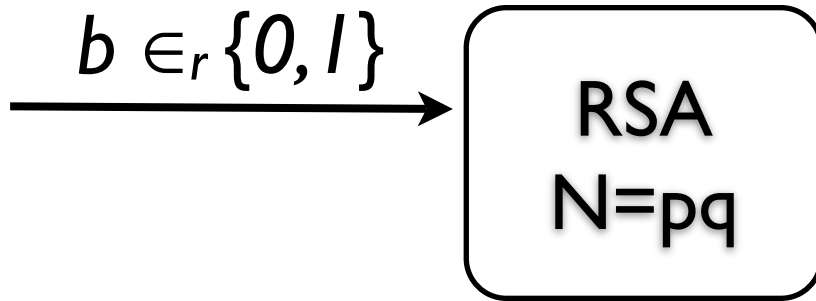
Crypto in Real Life



Crypto in Real Life

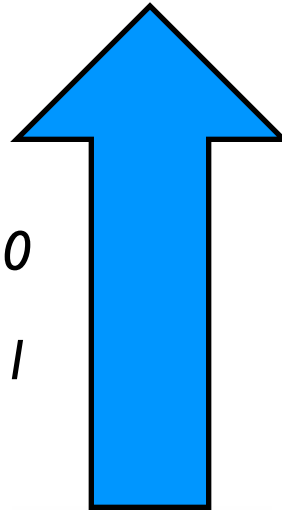


$b \in_r \{0, 1\}$



RSA
 $N=pq$

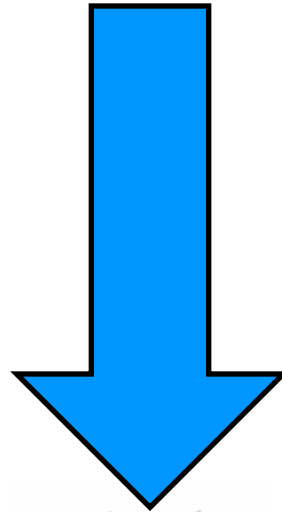
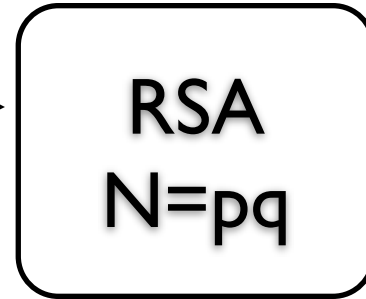
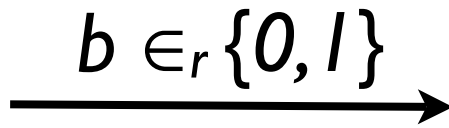
Message M_0
Message M_1



Crypto in Real Life



$b \in_r \{0, 1\}$



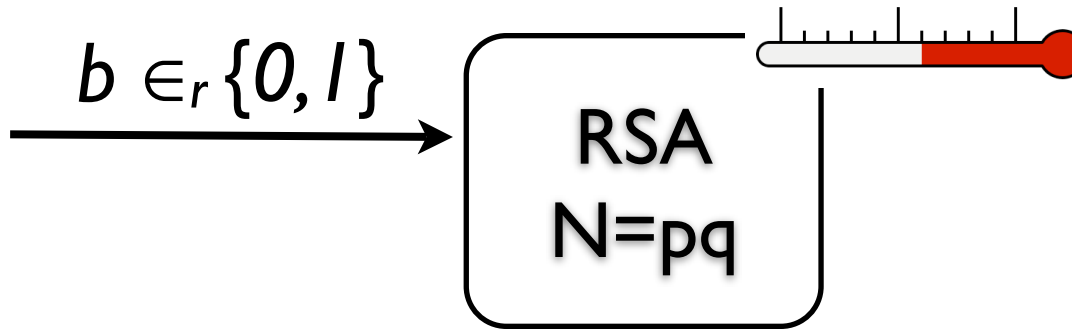
Encryption of M_b



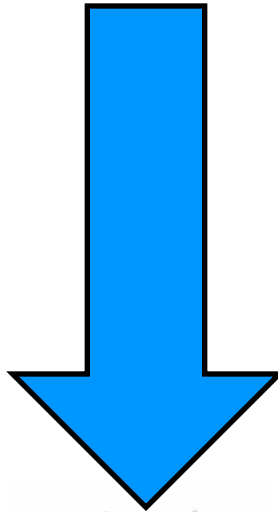
Crypto in Real Life



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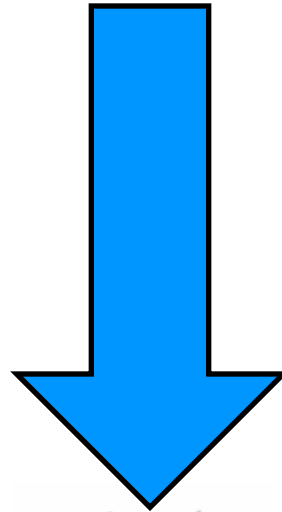
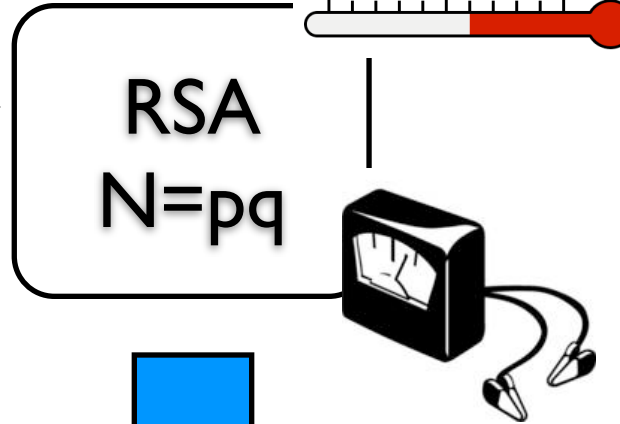
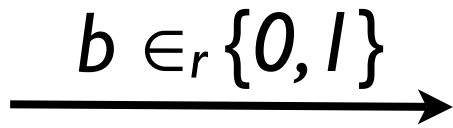
Encryption of M_b



Crypto in Real Life



$b \in_r \{0, 1\}$



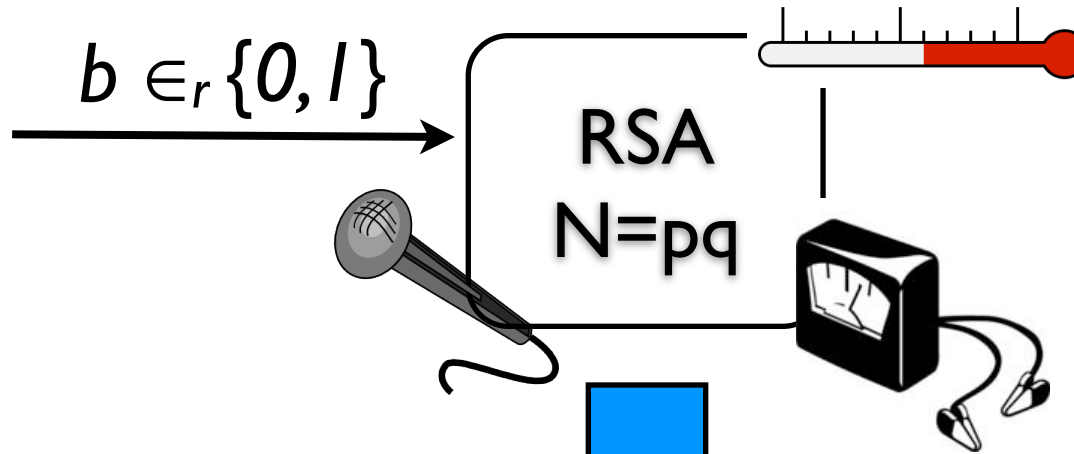
Encryption of M_b



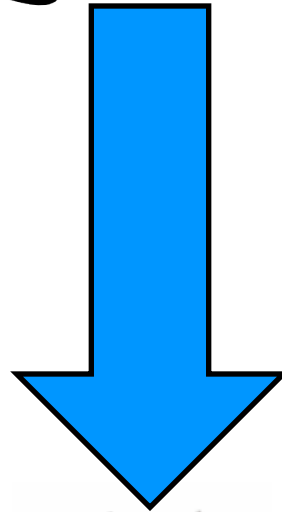
Crypto in Real Life



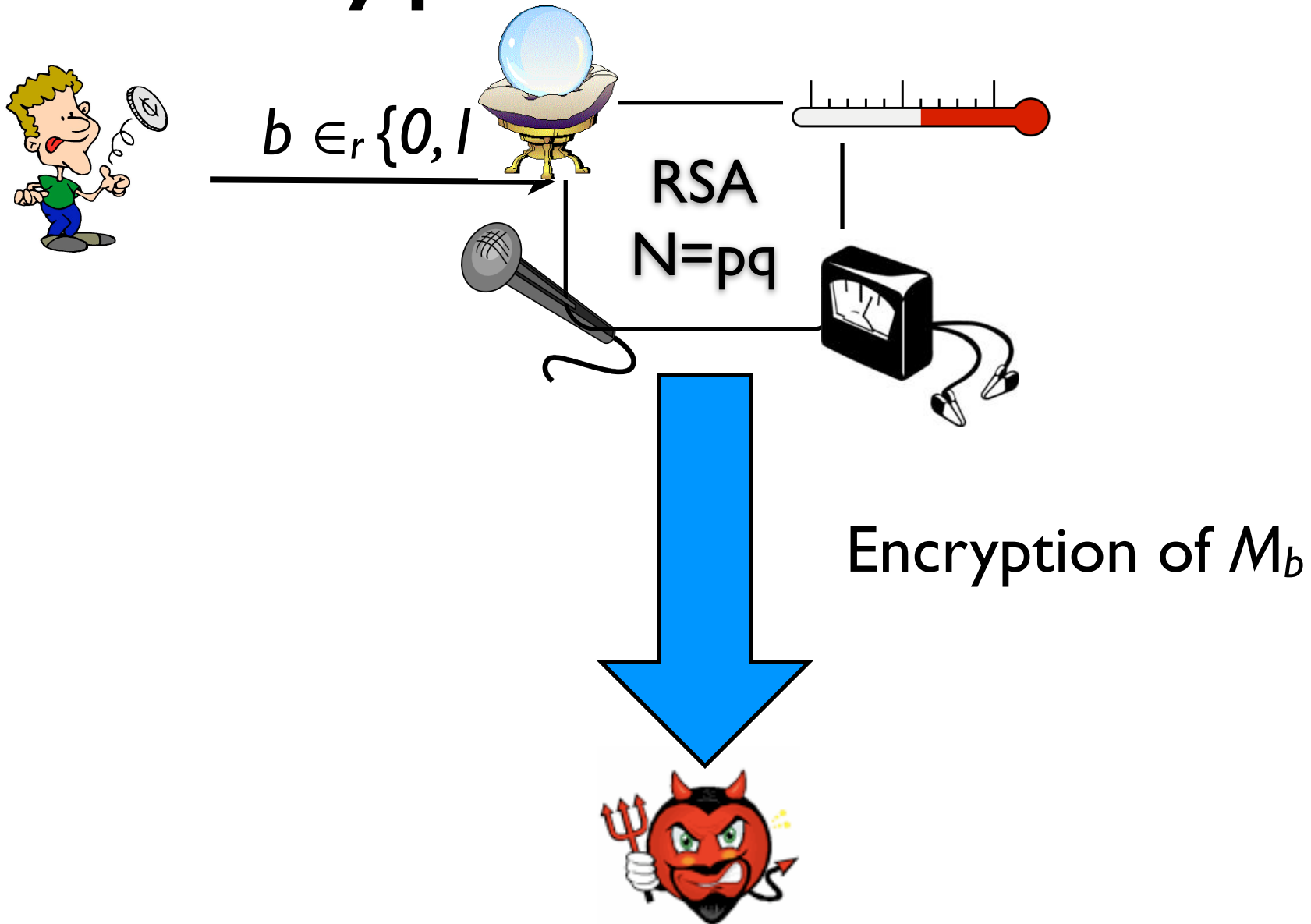
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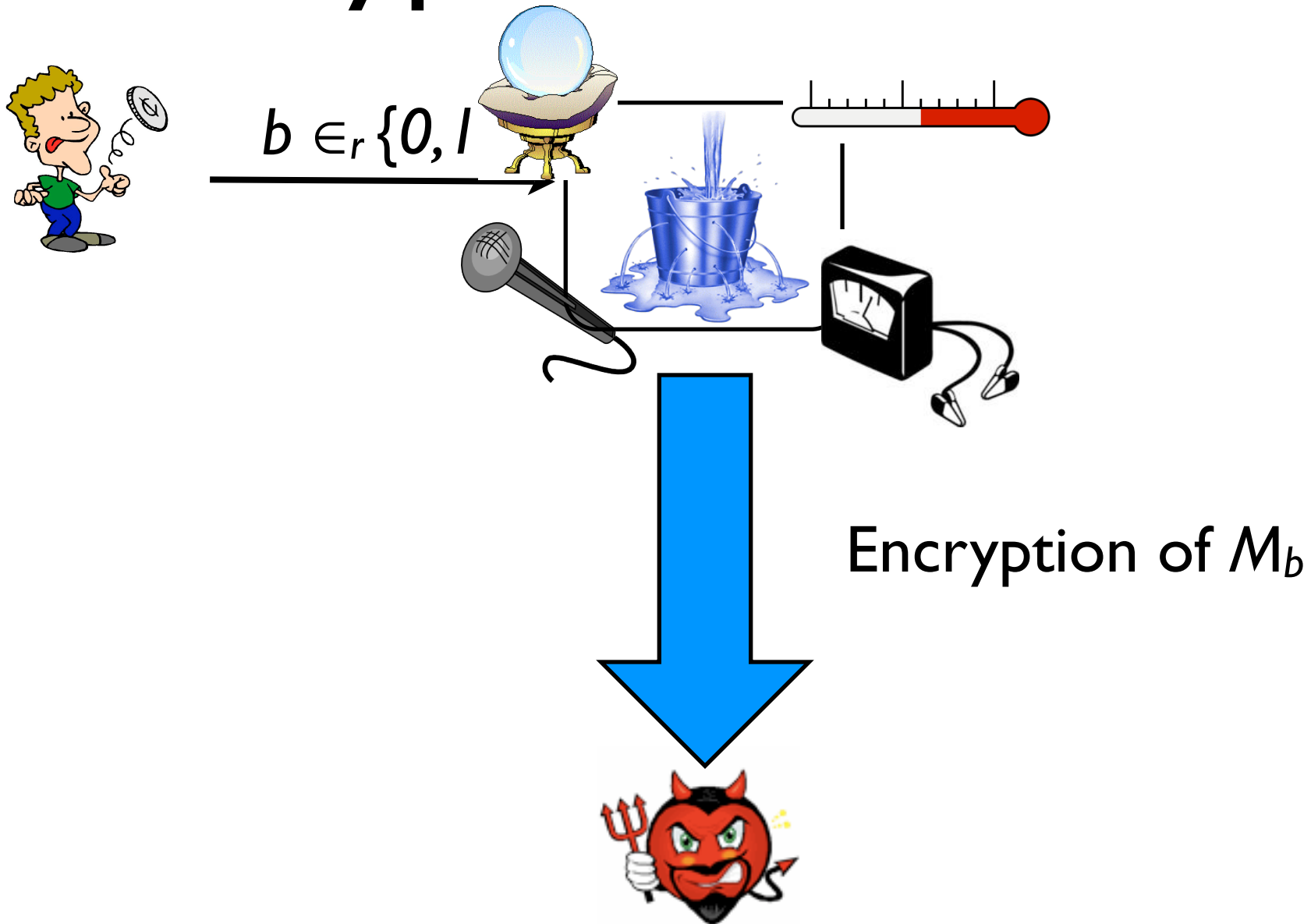
Encryption of M_b



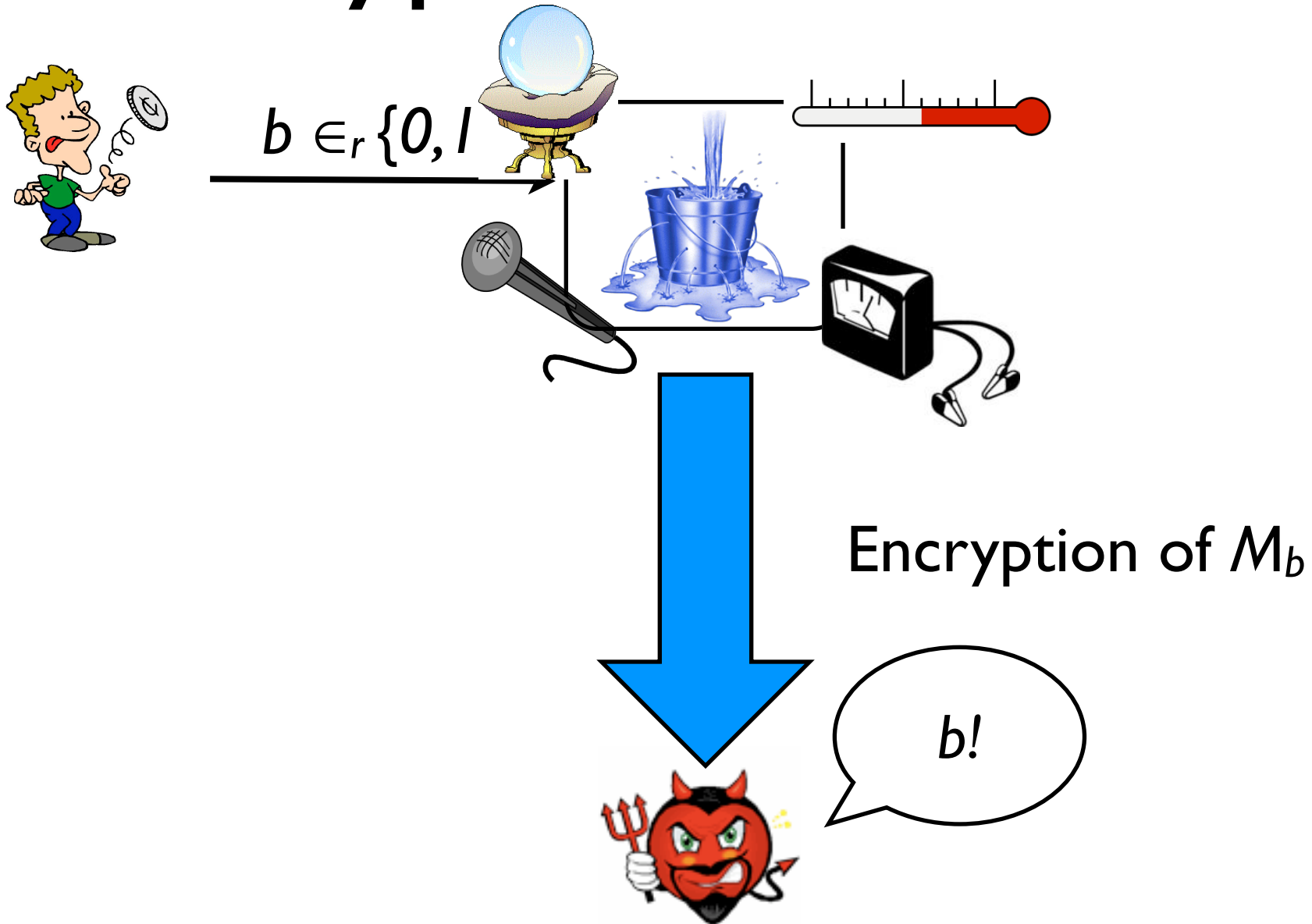
Crypto in Real Life



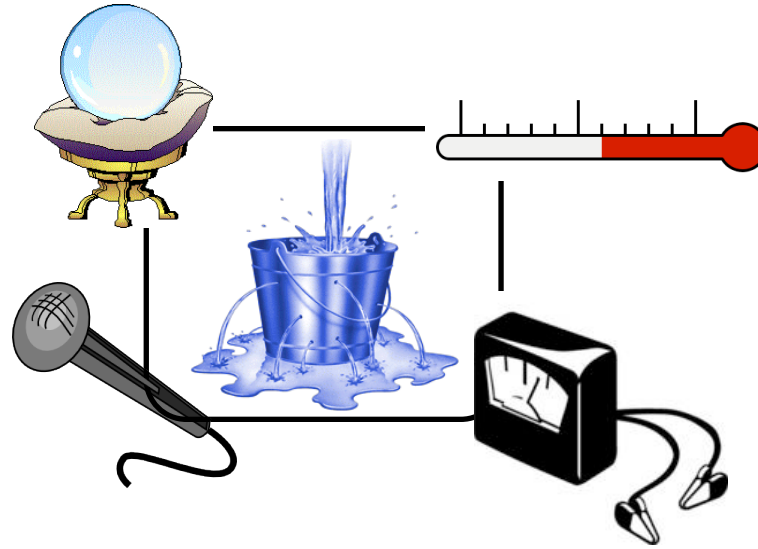
Crypto in Real Life



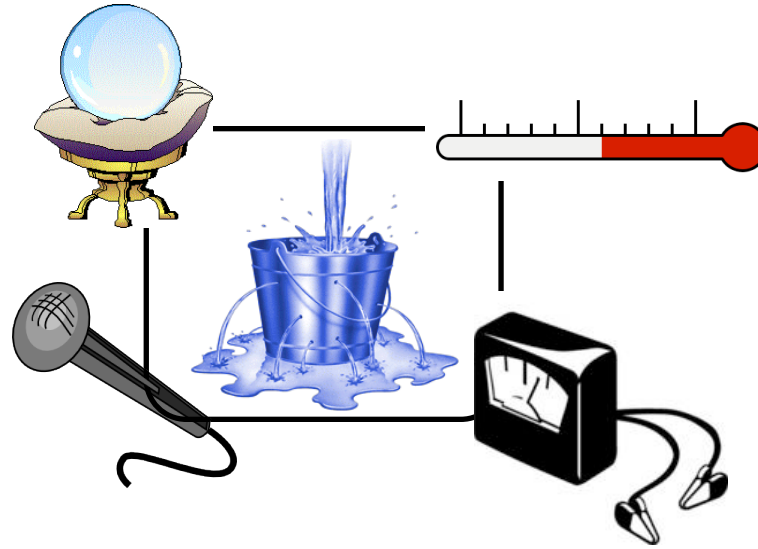
Crypto in Real Life



Crypto in Real Life

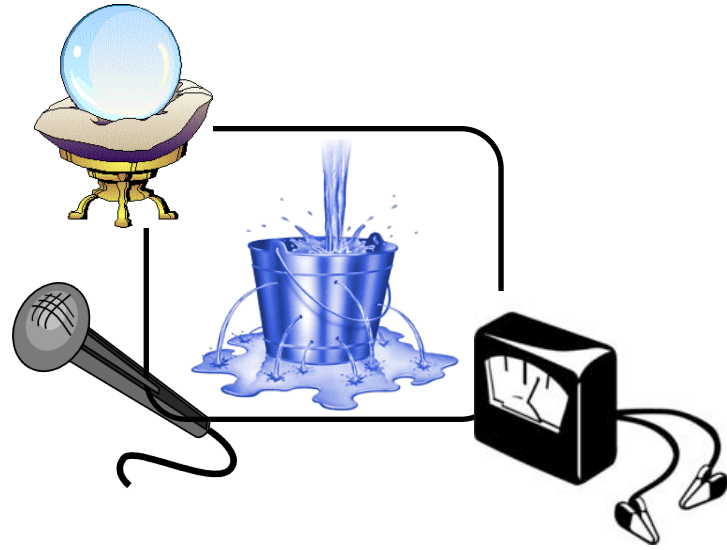


Crypto in Real Life



Solutions:

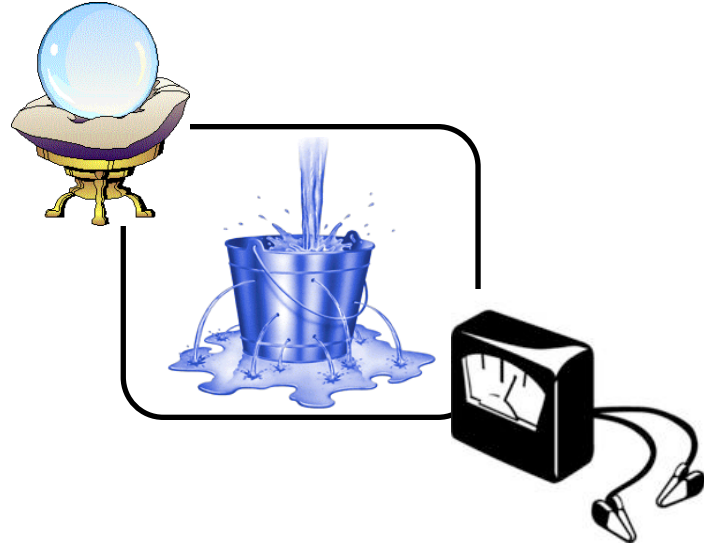
Crypto in Real Life



Solutions:

- Heat isolation

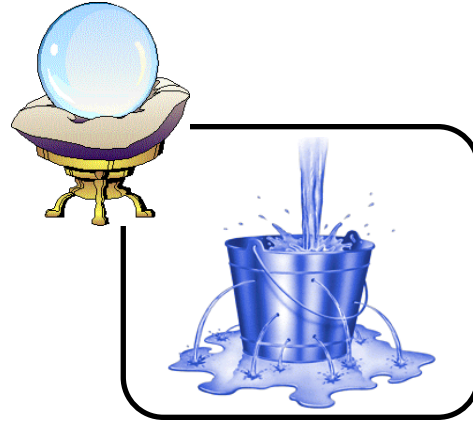
Crypto in Real Life



Solutions:

- Heat isolation
- Uniform cooling and quiet fans

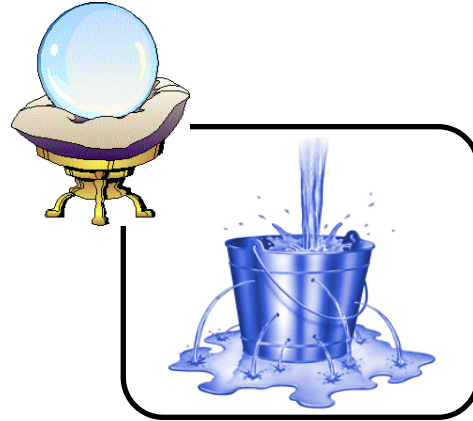
Crypto in Real Life



Solutions:

- Heat isolation
- Uniform cooling and quiet fans
- Electromagnetic isolation

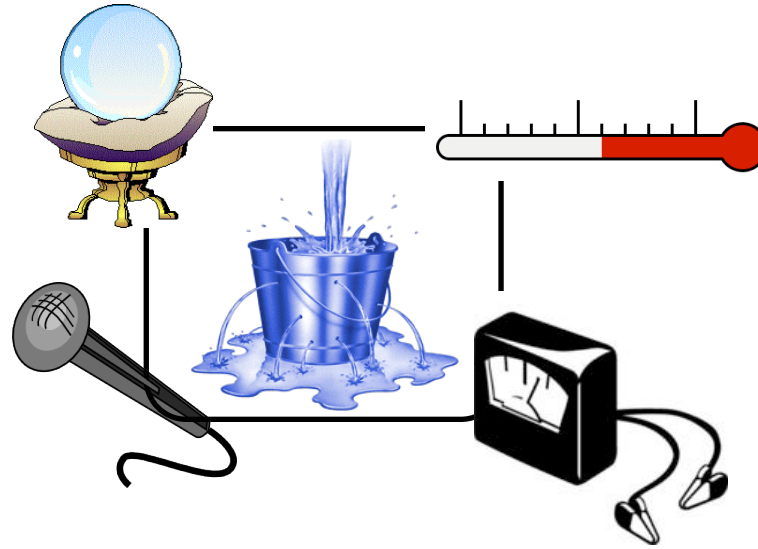
Crypto in Real Life



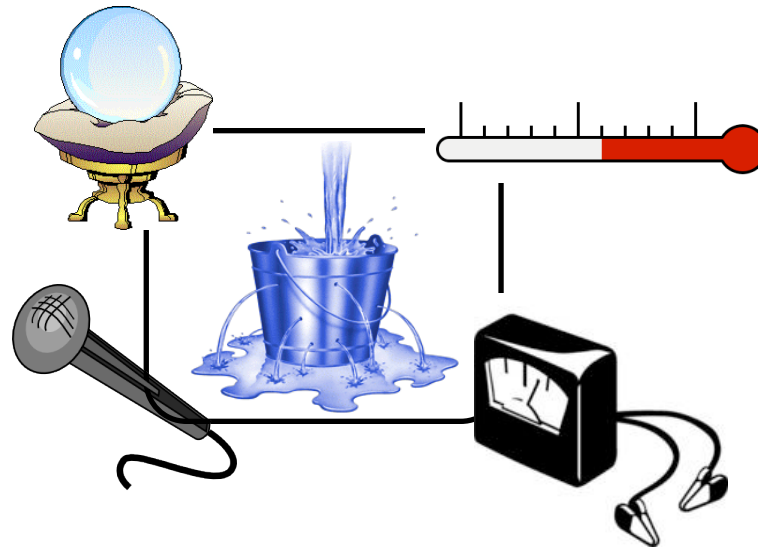
Solutions:

- Heat isolation
- Uniform cooling and quiet fans
- Electromagnetic isolation
- ???

Algorithmic Protection

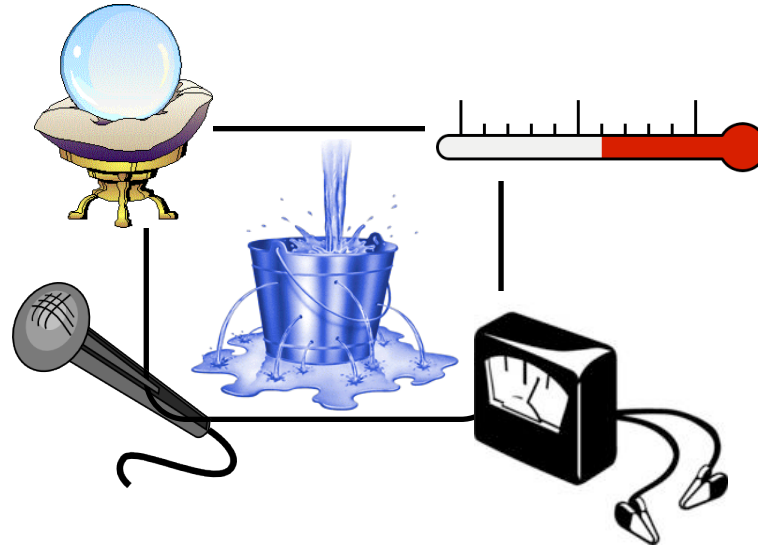


Algorithmic Protection



In recent years:

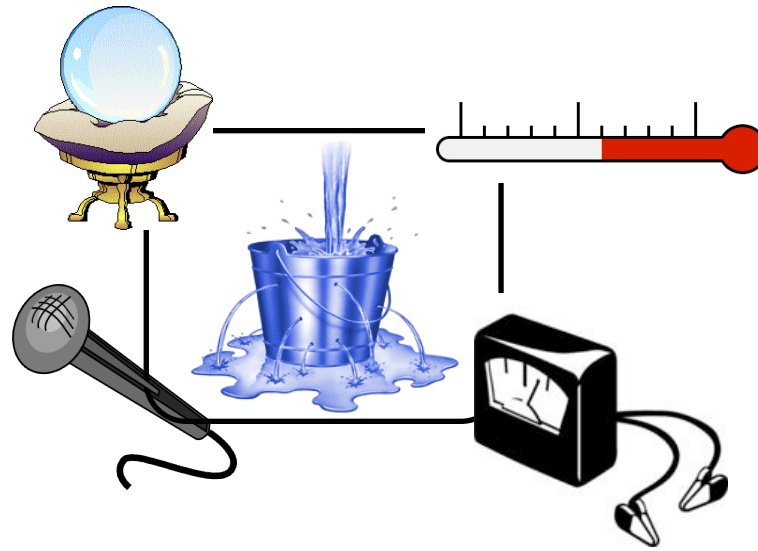
Algorithmic Protection



In recent years:

- Reduce assumptions about hardware

Algorithmic Protection



In recent years:

- Reduce assumptions about hardware
- Design algorithms that are secure under leakage

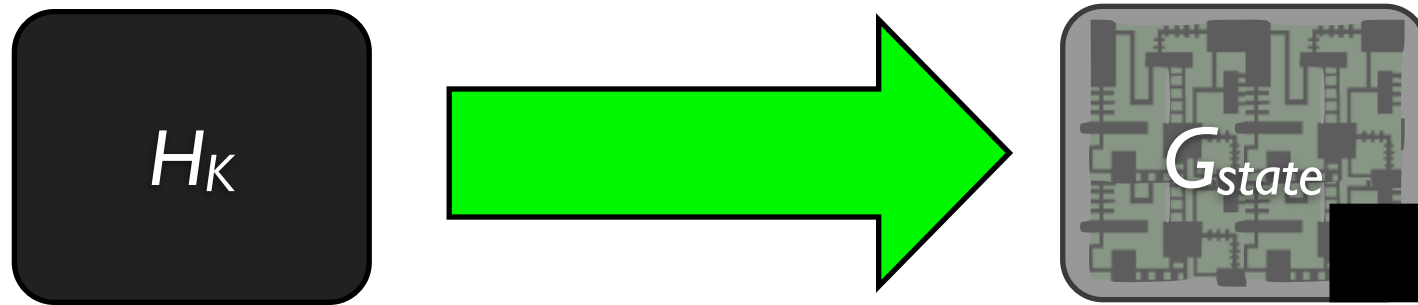
Previous Work

- [Goldreich & Ostrovsky 96] protect against complete leakage of memory when CPU is secure.
- [Ishai & Sahai & Wagner 2003] assume adversary leaks value of a fixed number of wires
- [Micali & Reyzin 04] introduce axioms and framework
- [Goldwasser & Kalai & Rothblum 2008] one-time programs
- [Dziembowski & Pietrzak 2008] Leakage resilient stream cipher in the split state model
- [Faust & Reyzin & Tromer 2009] protect against AC0 leakage functions (needs secure component)

Our Results

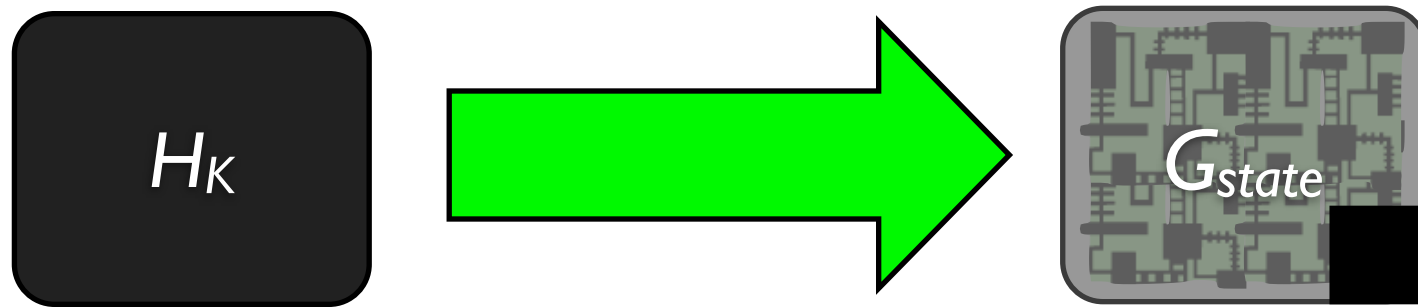
Our Results

- A compiler that transforms any keyed primitive H_K into a stateful algorithm G_{state}
- G_{state} is resilient against length bounded leakage in each invocation
- Need a fixed size, memory-less secure component



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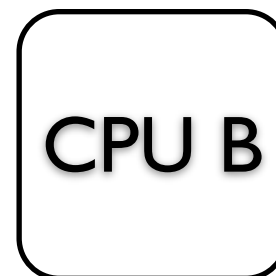
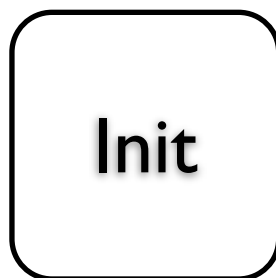
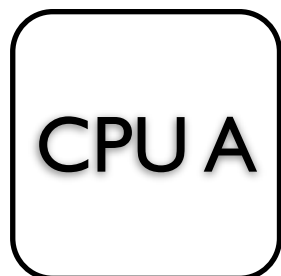


Achieve leak-resilience for arbitrary complexity
from leak-resilience for fixed complexity

Model

Want to protect $H_K(x)$

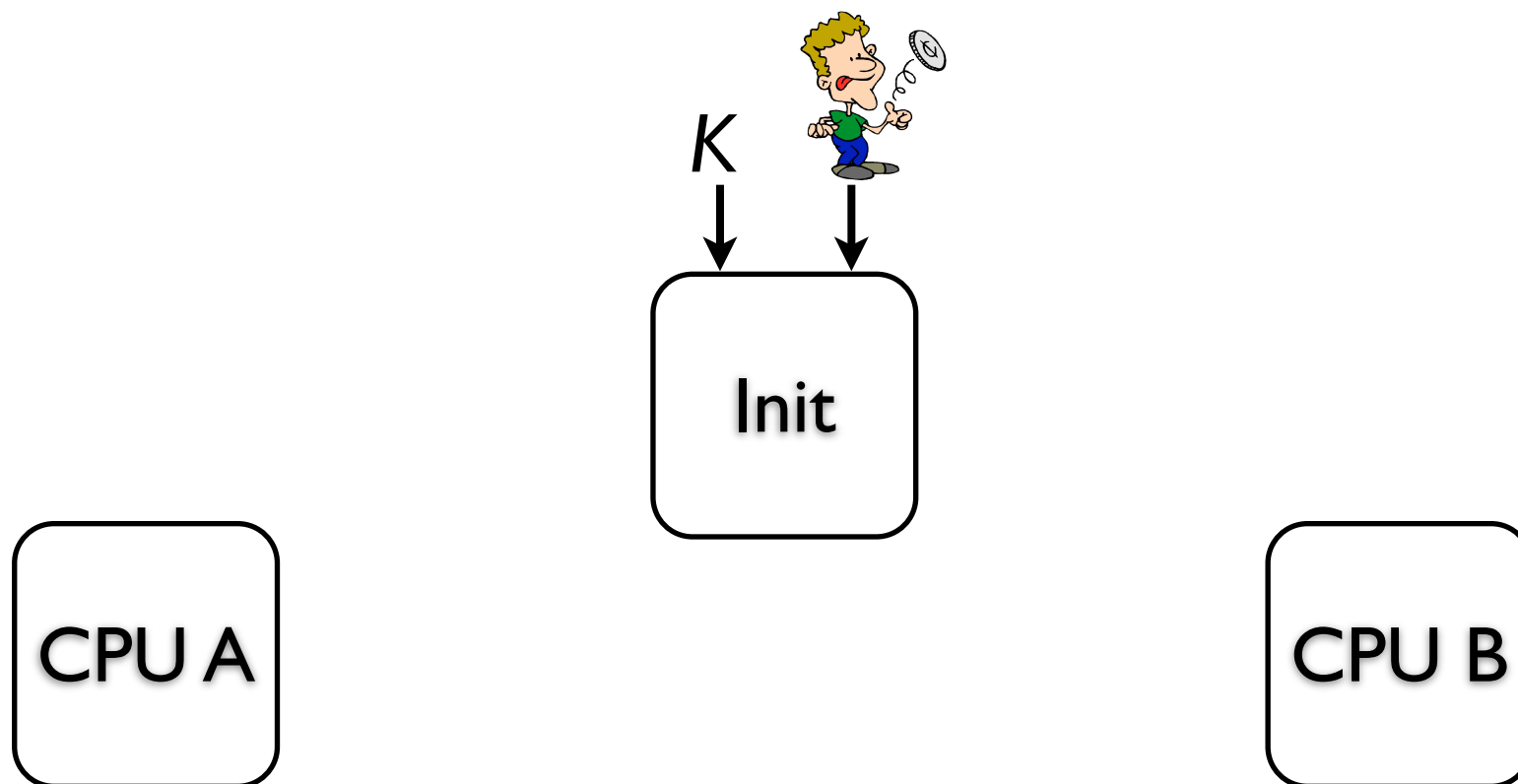
Two computers that communicate over a *public* channel. Initialization is secure.



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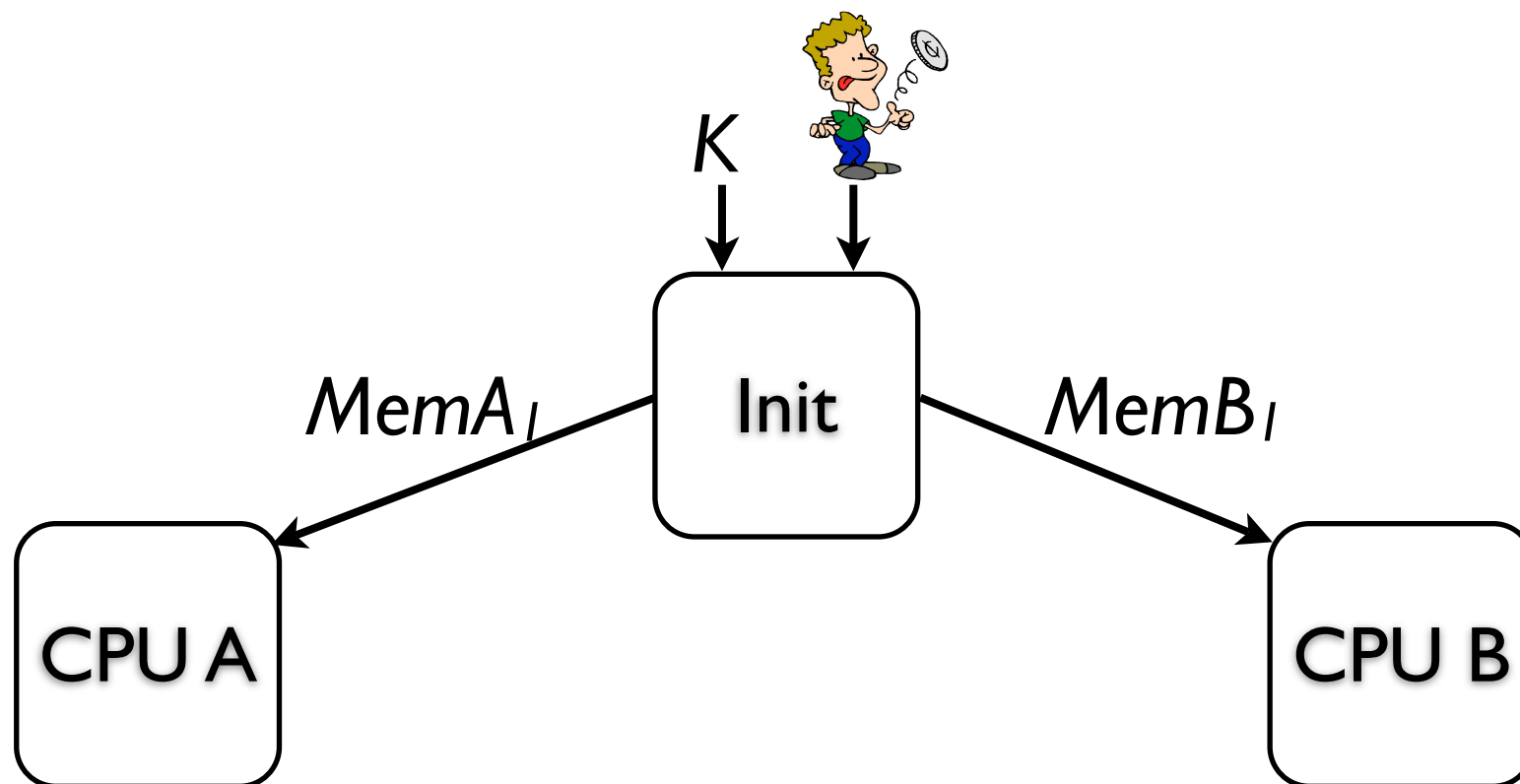
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Model

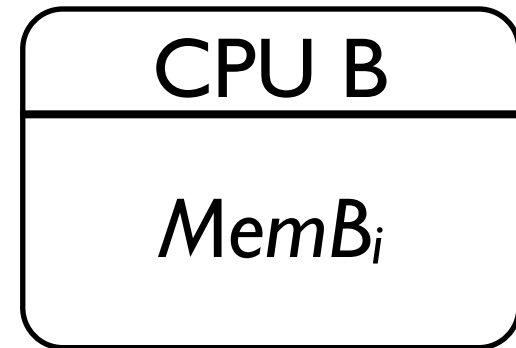
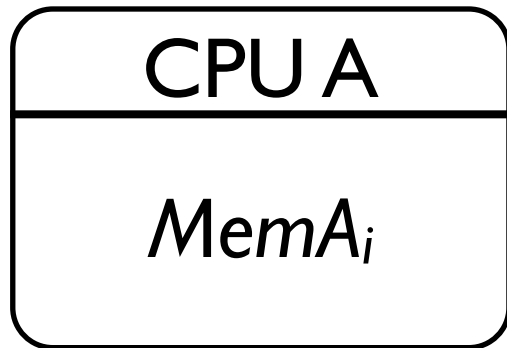
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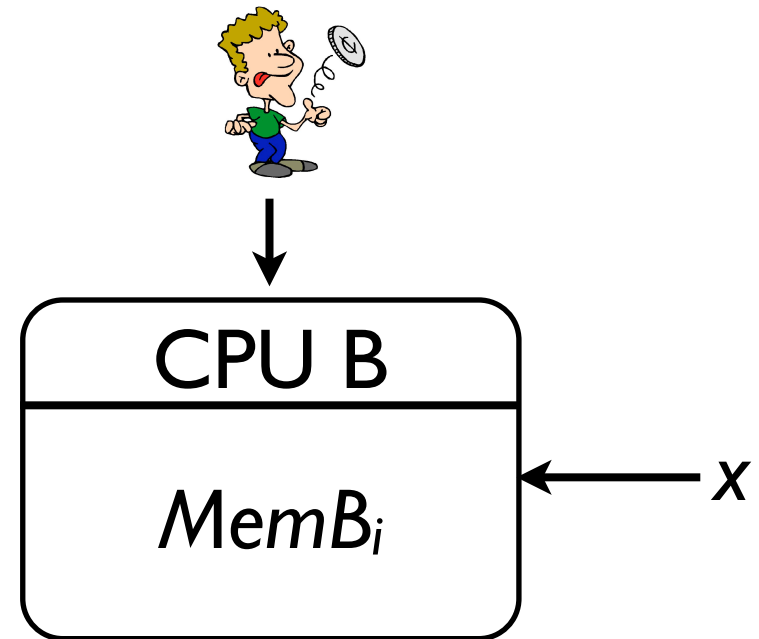
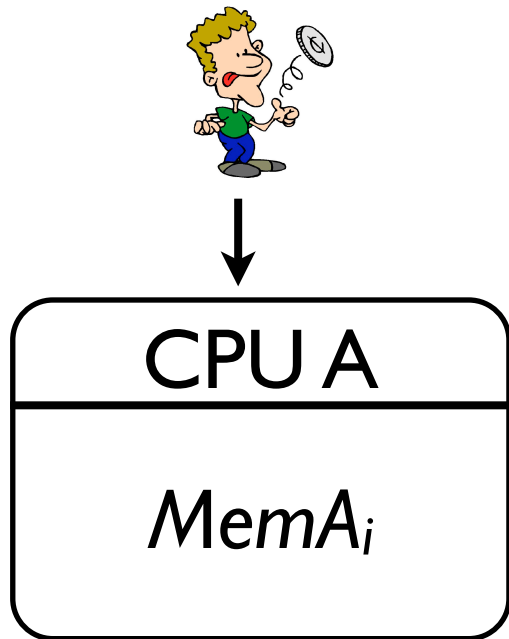
Model

Evaluating $H_K(x)$



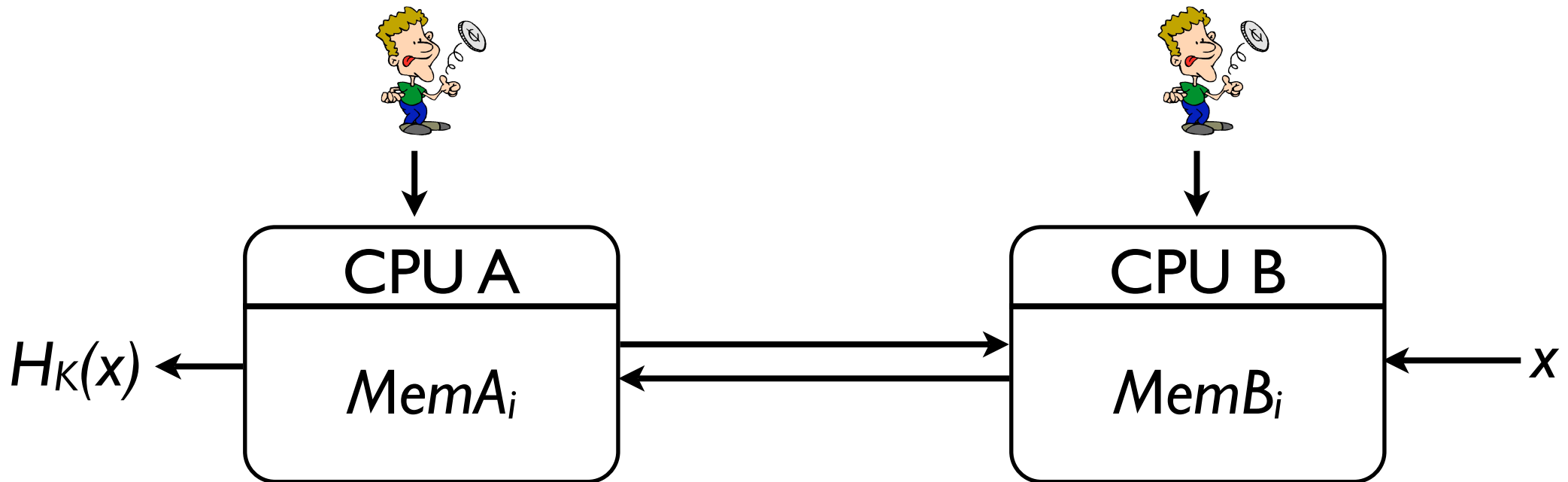
Model

Evaluating $H_K(x)$



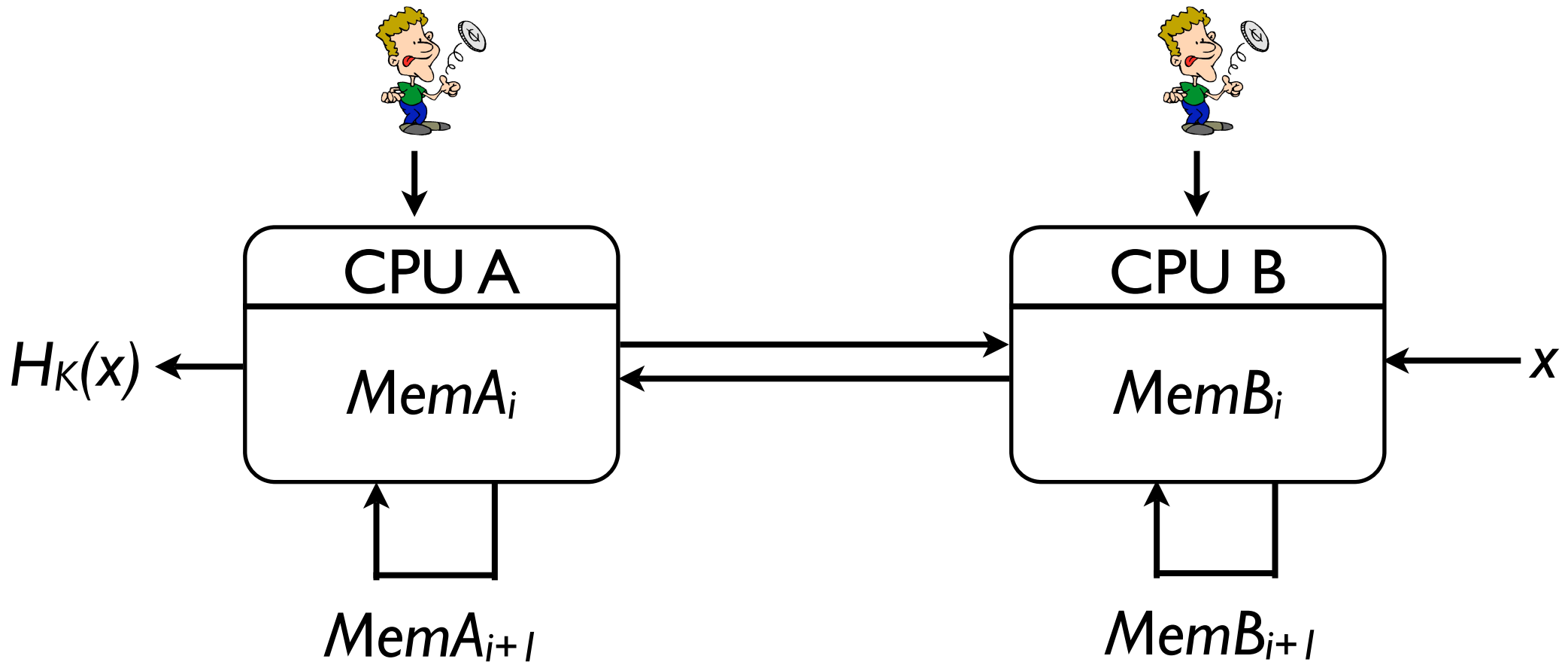
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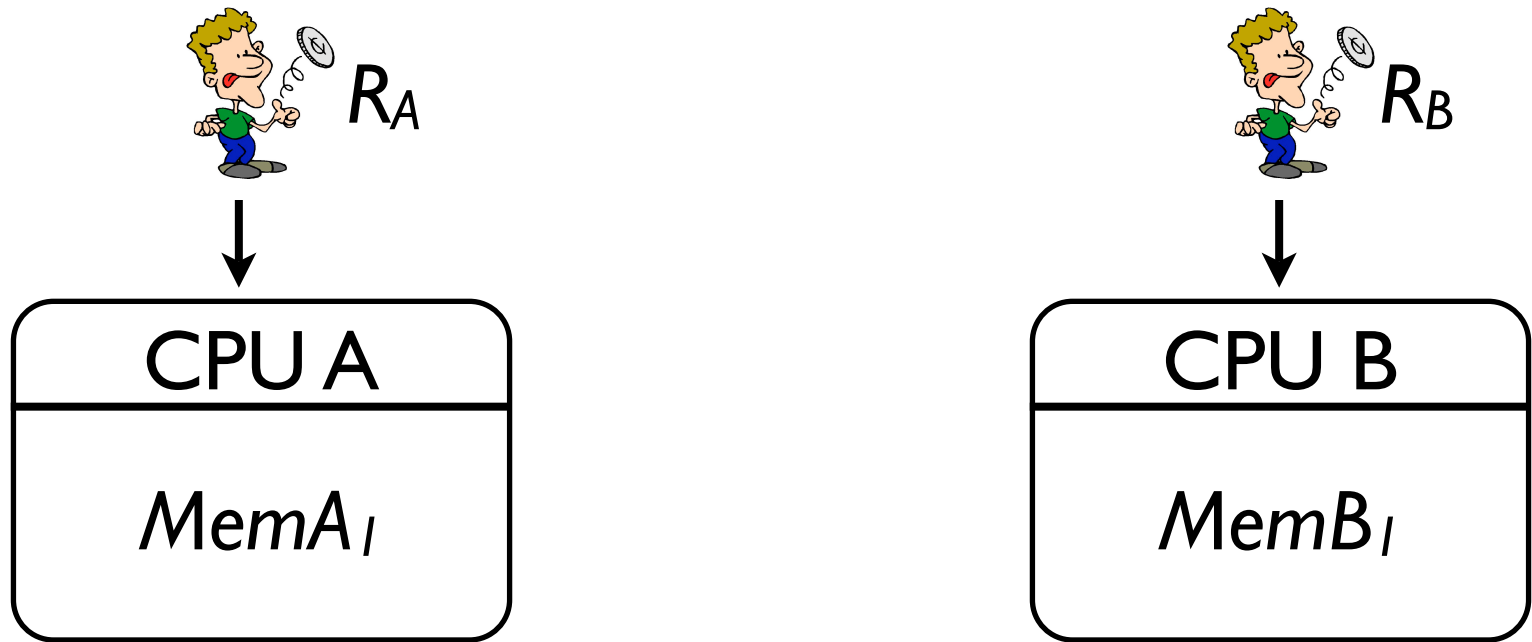


Model

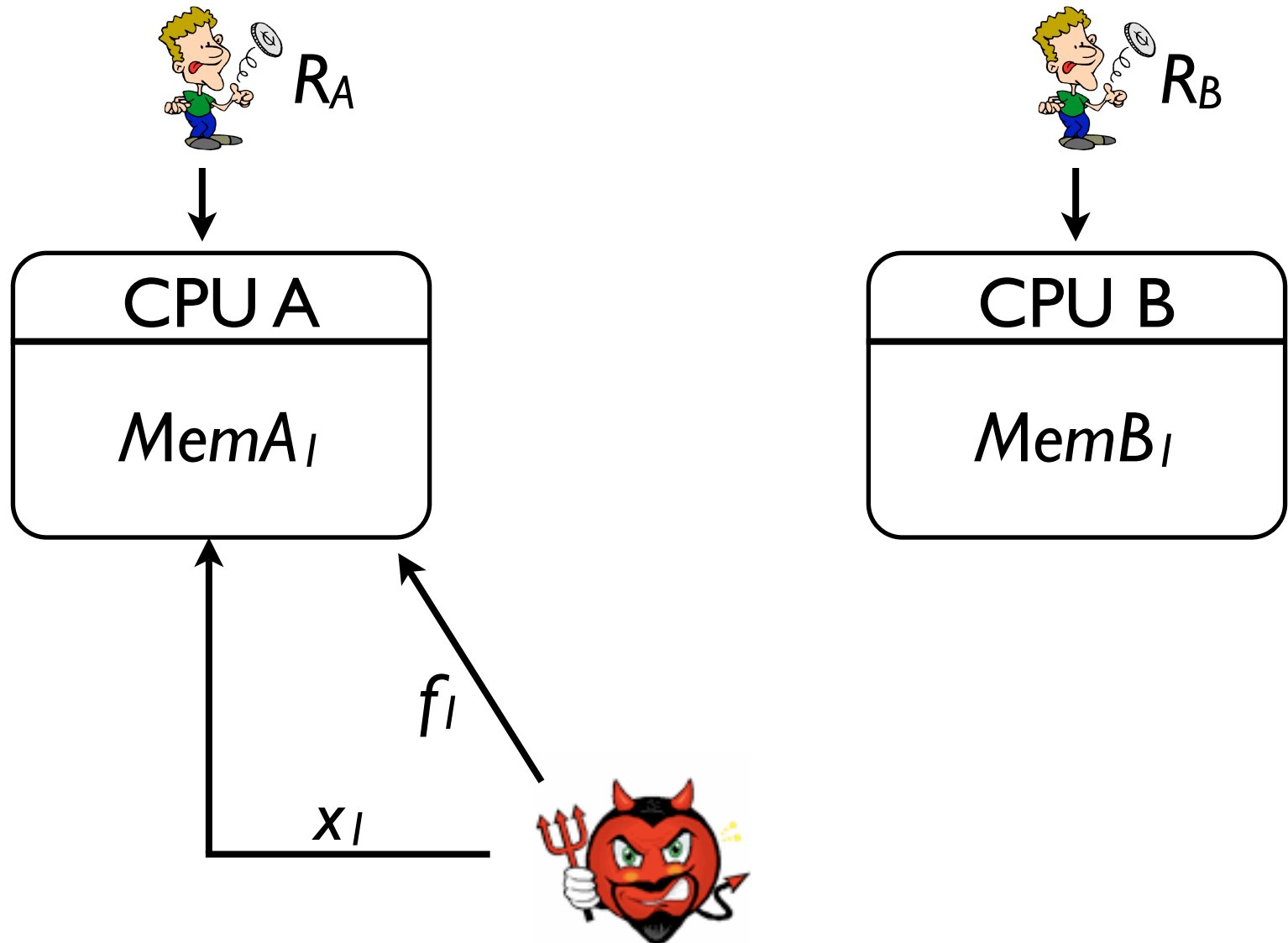
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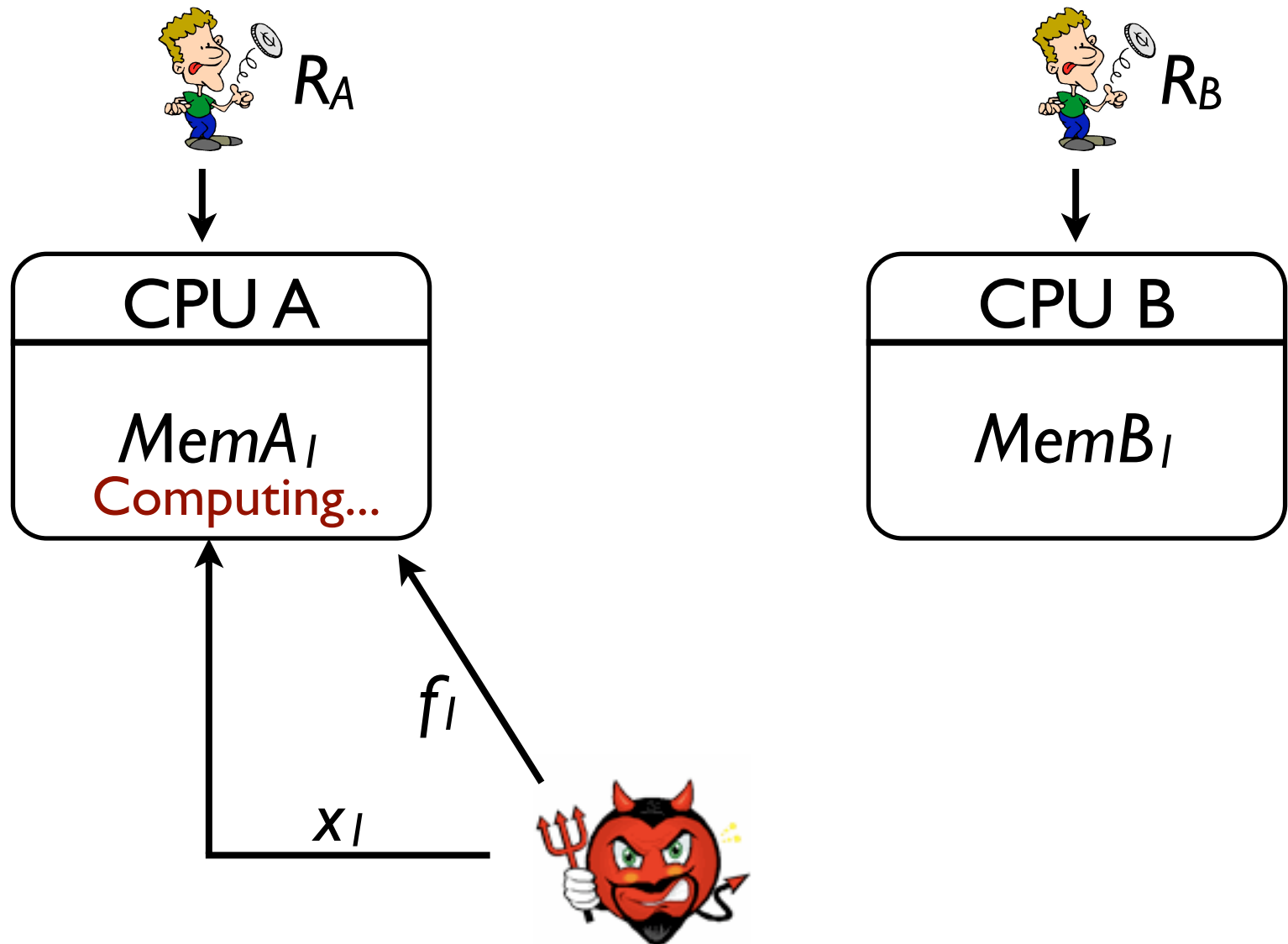
Leakage



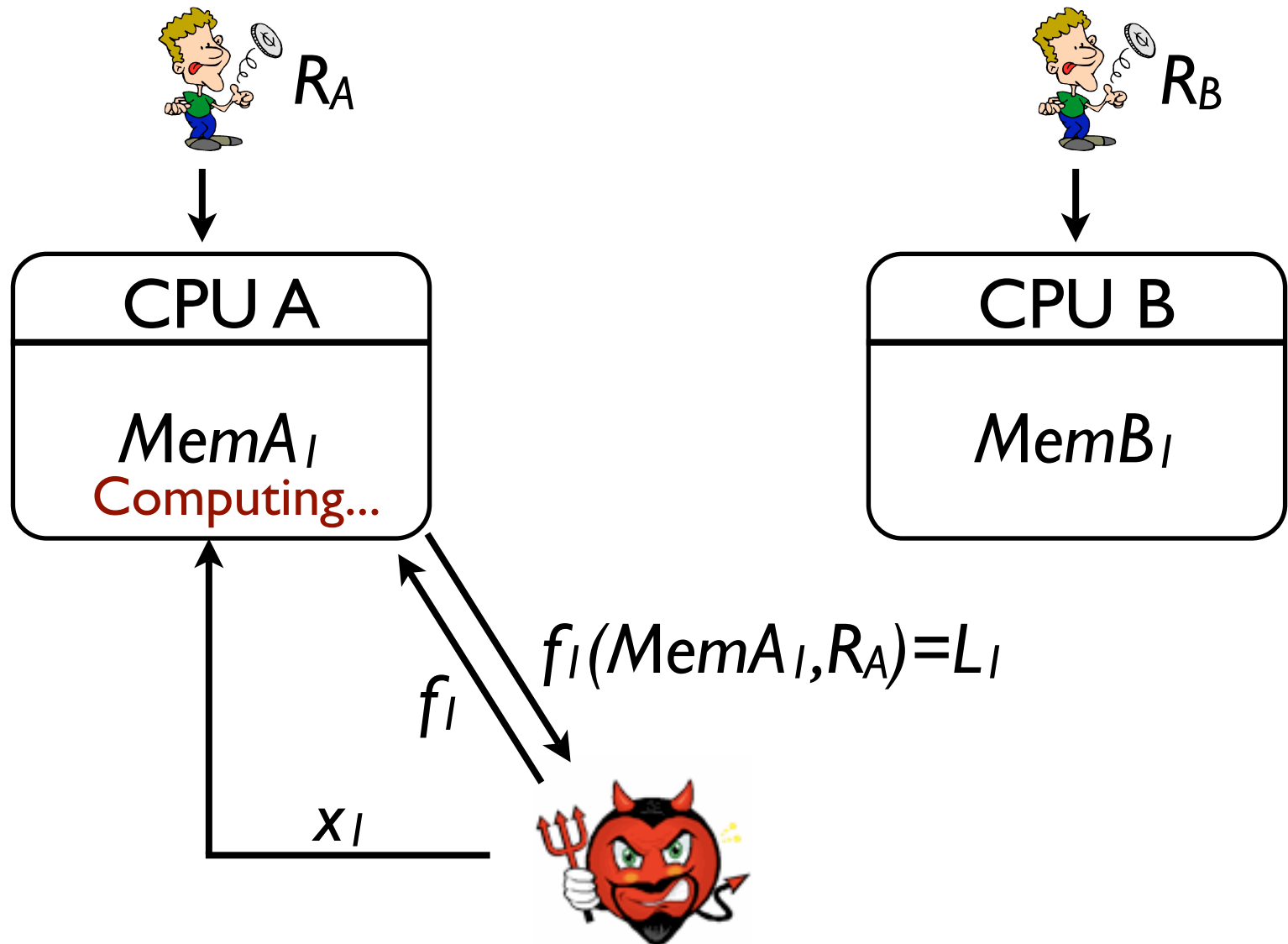
Leakage



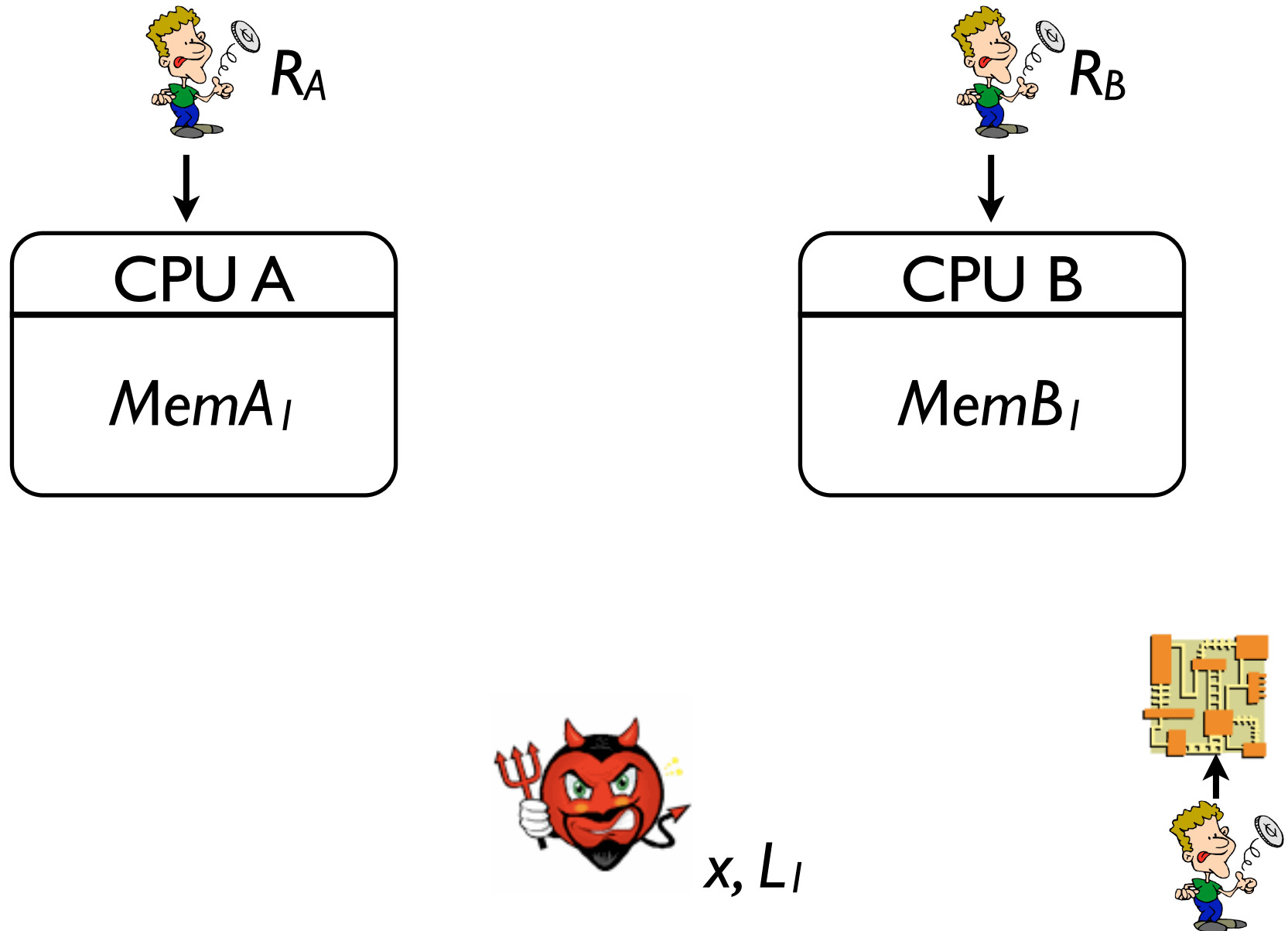
Leakage



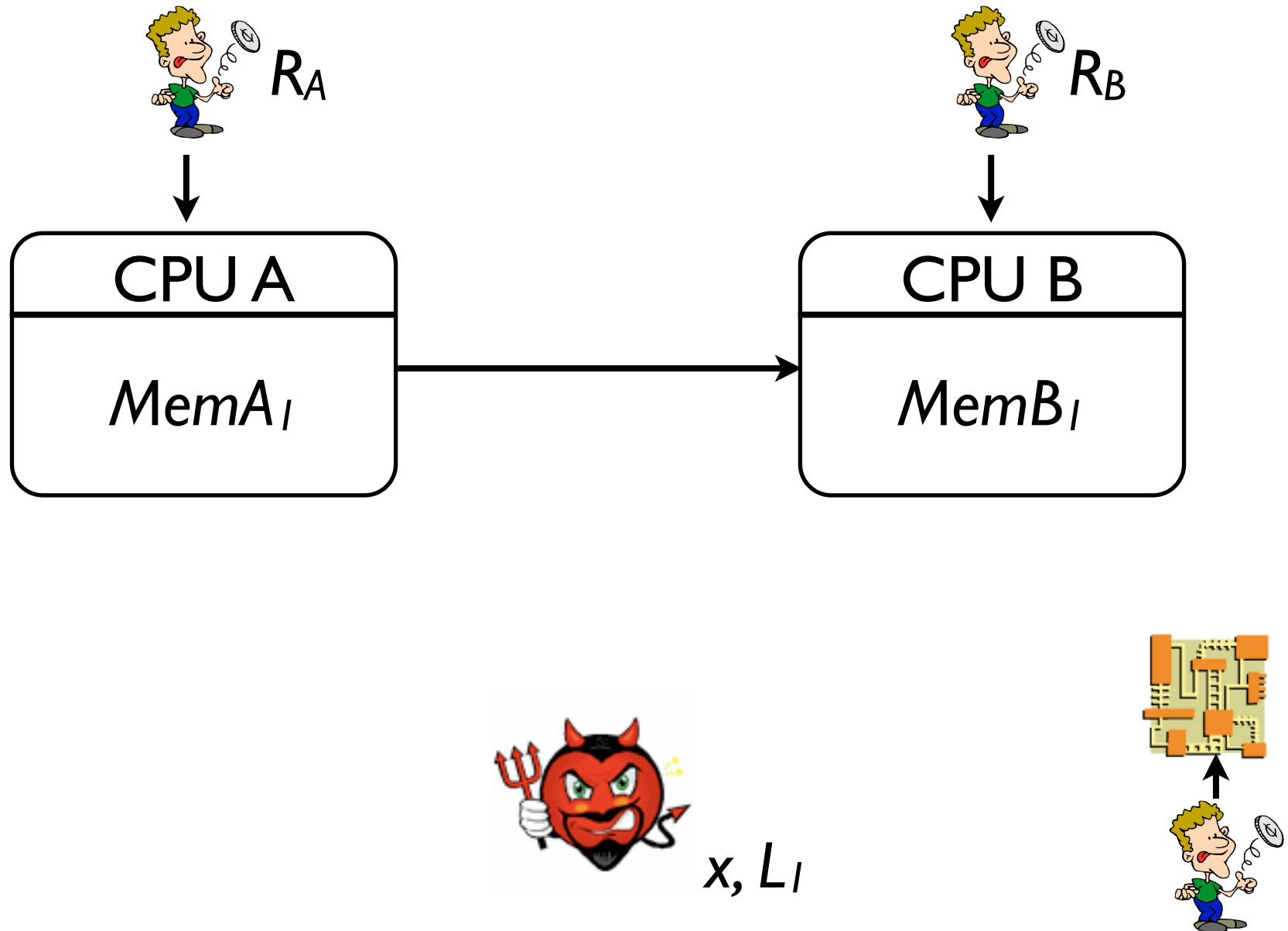
Leakage



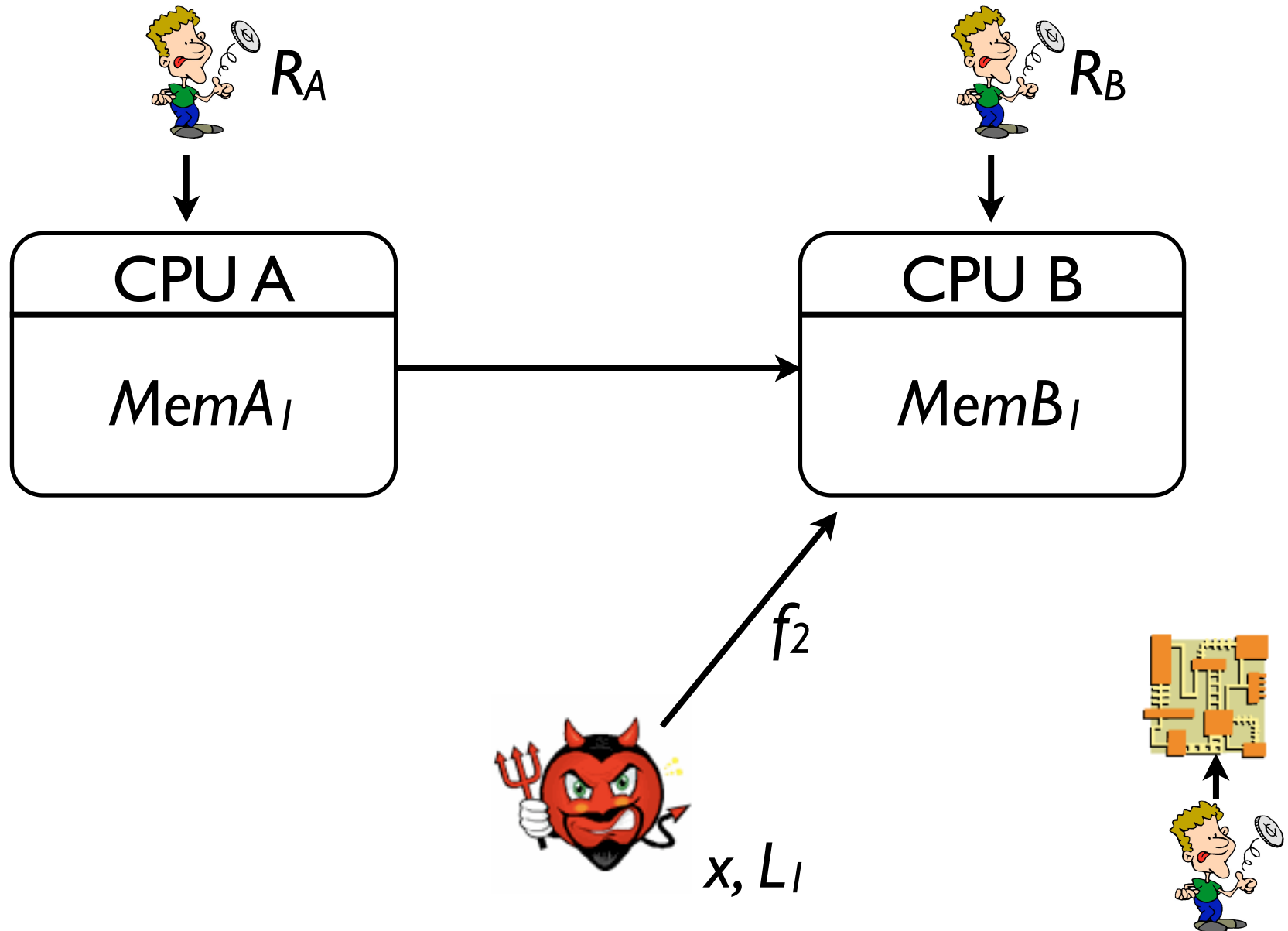
Leakage



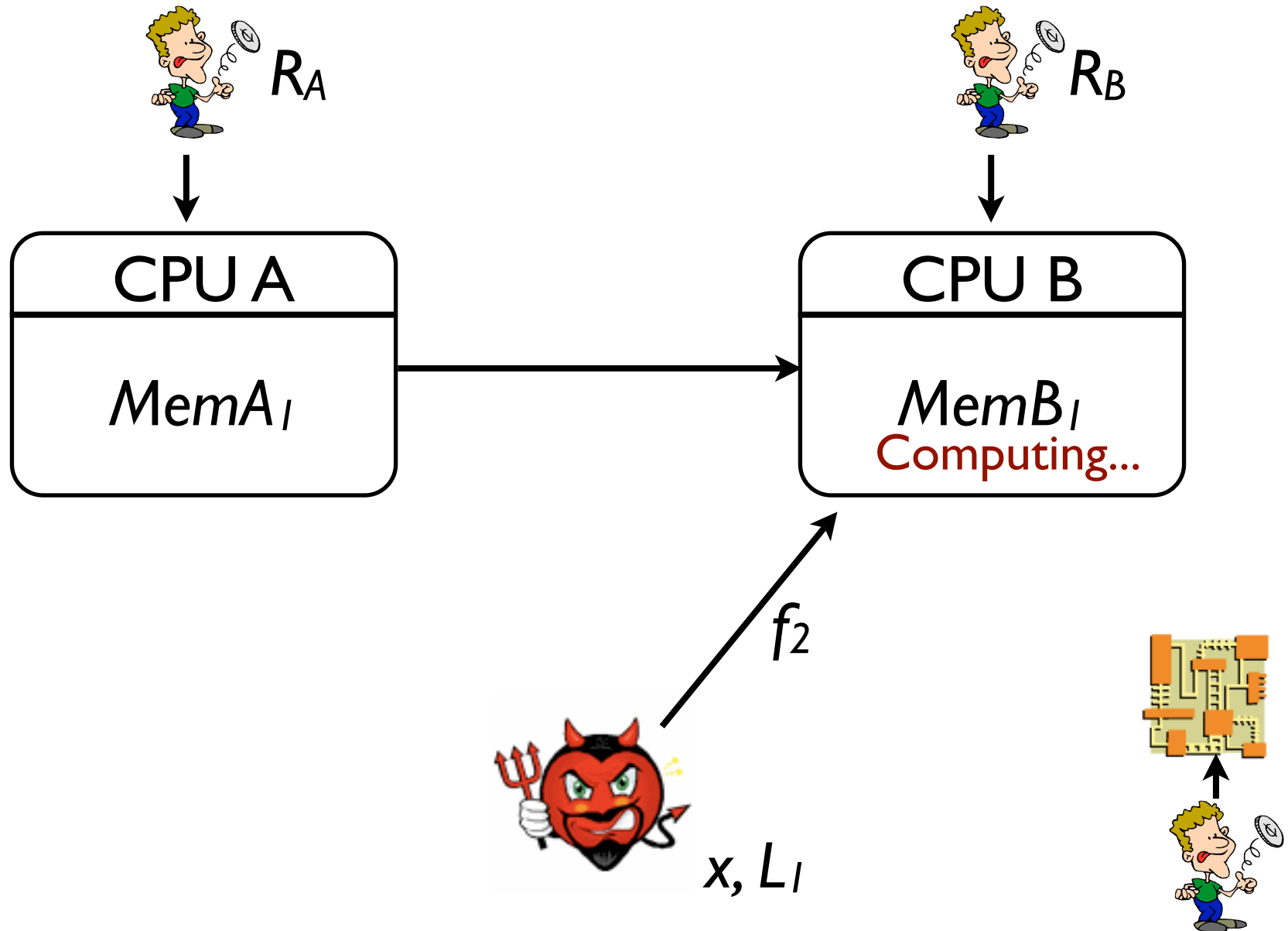
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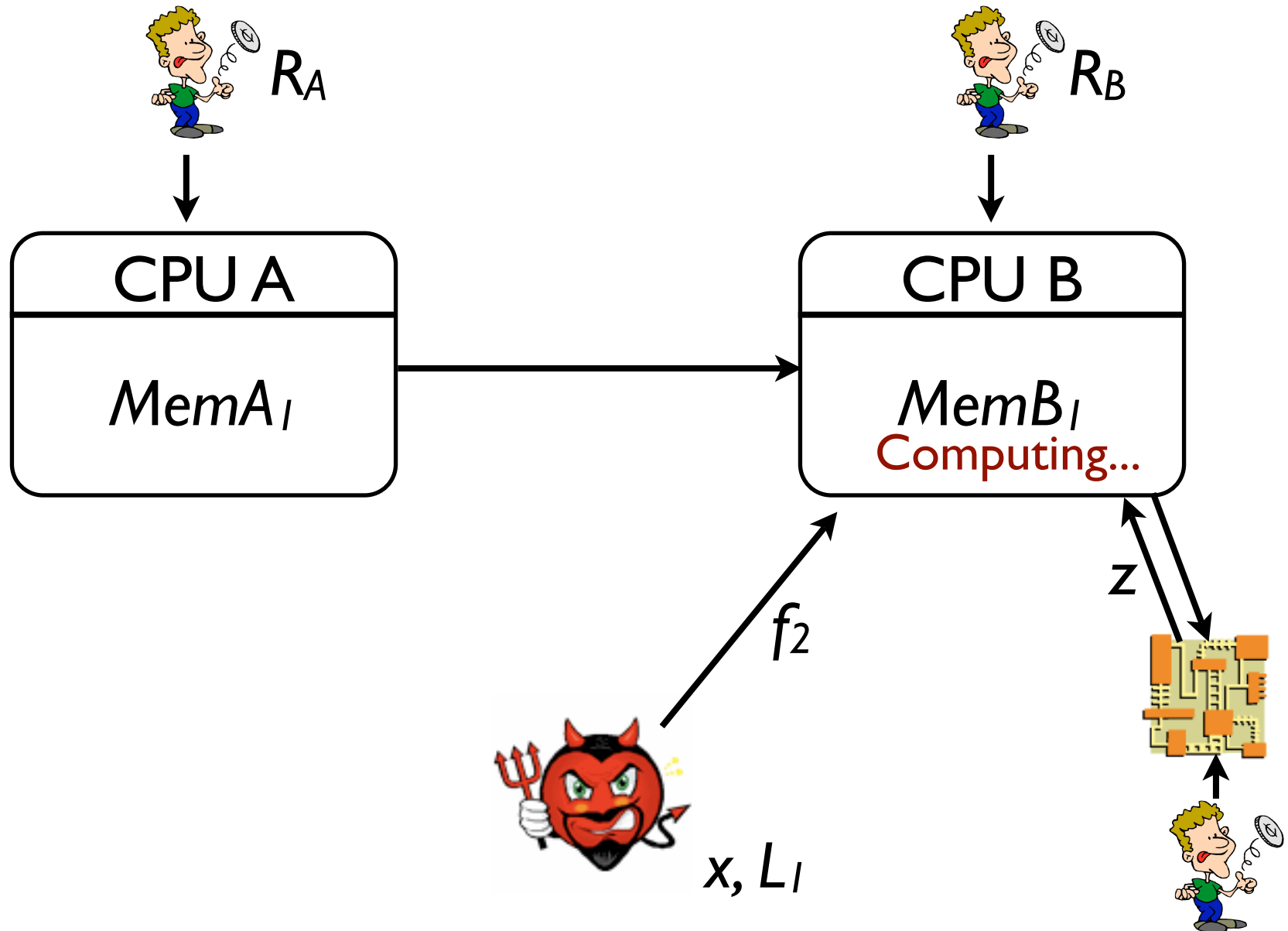
Leakage



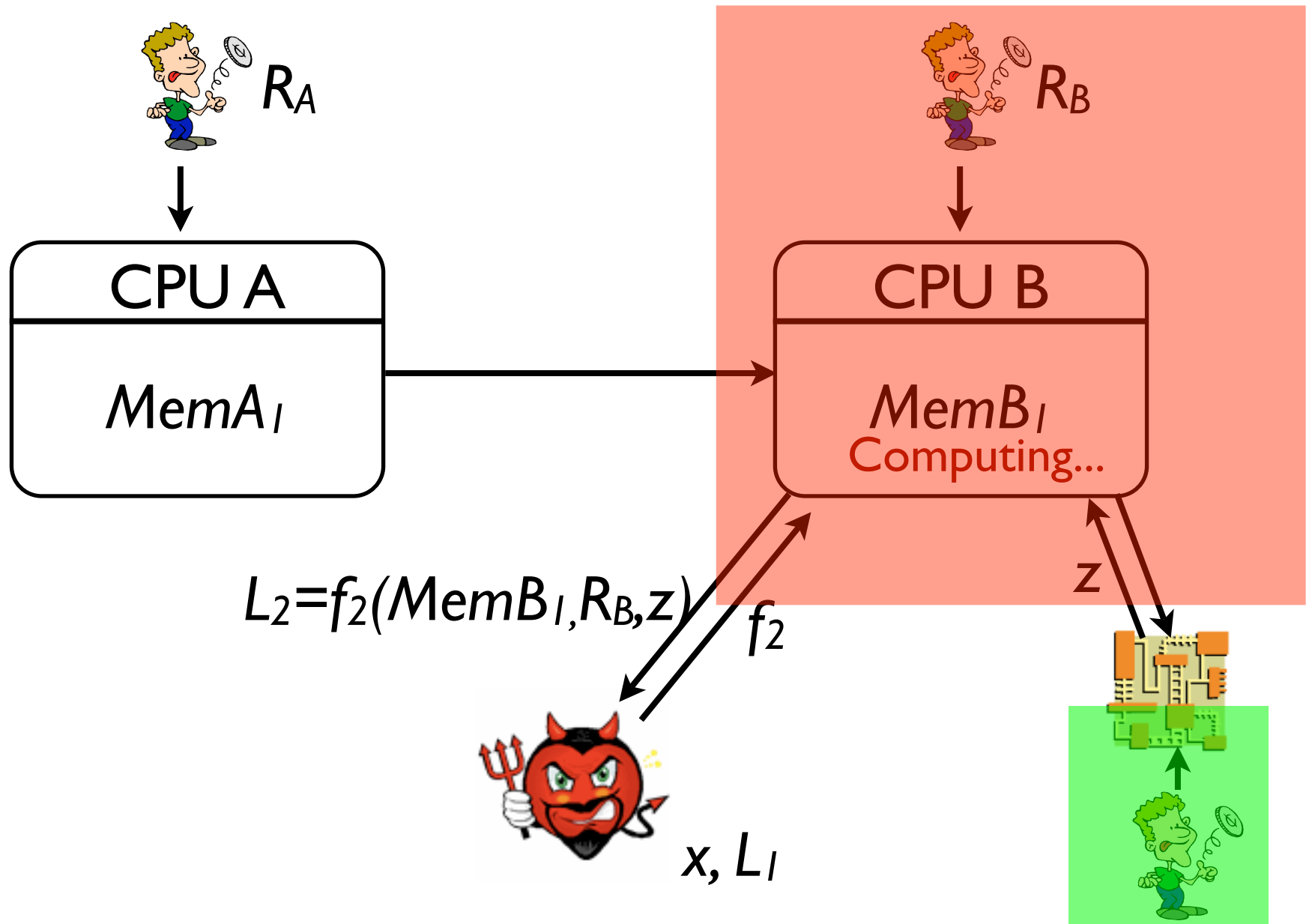
Leakage



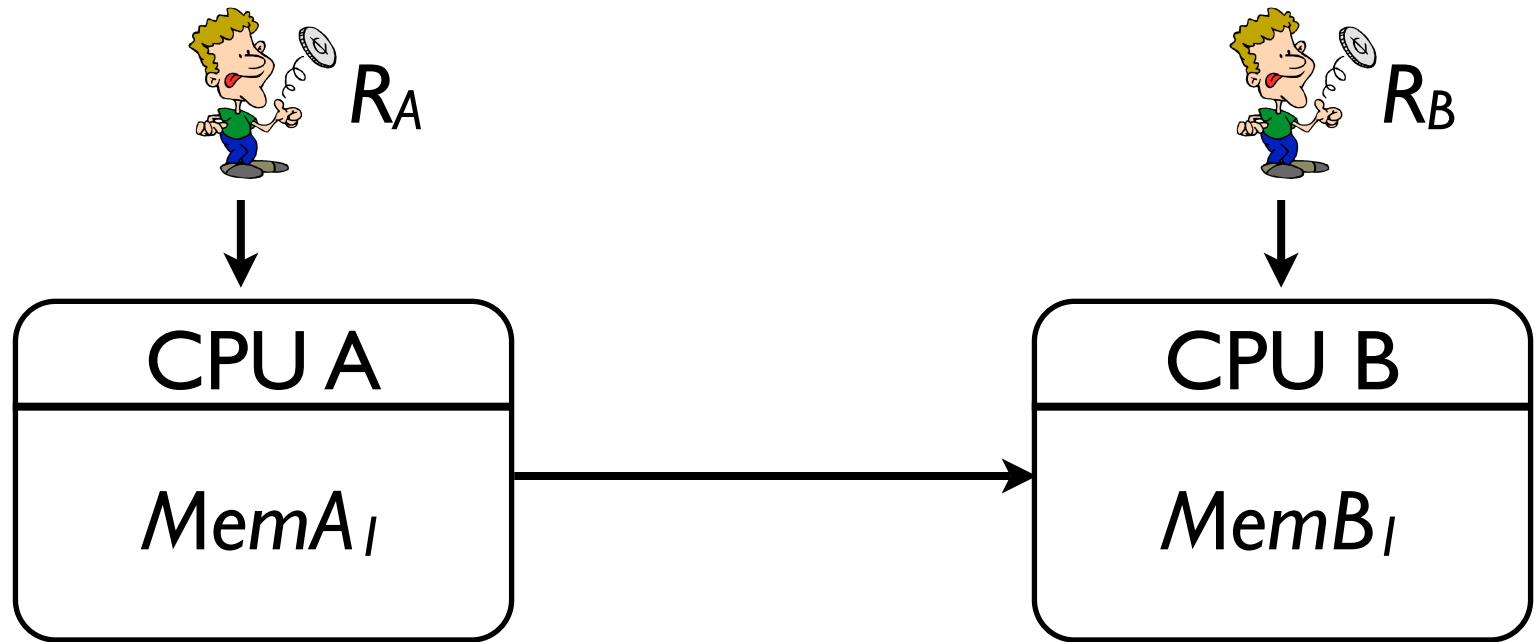
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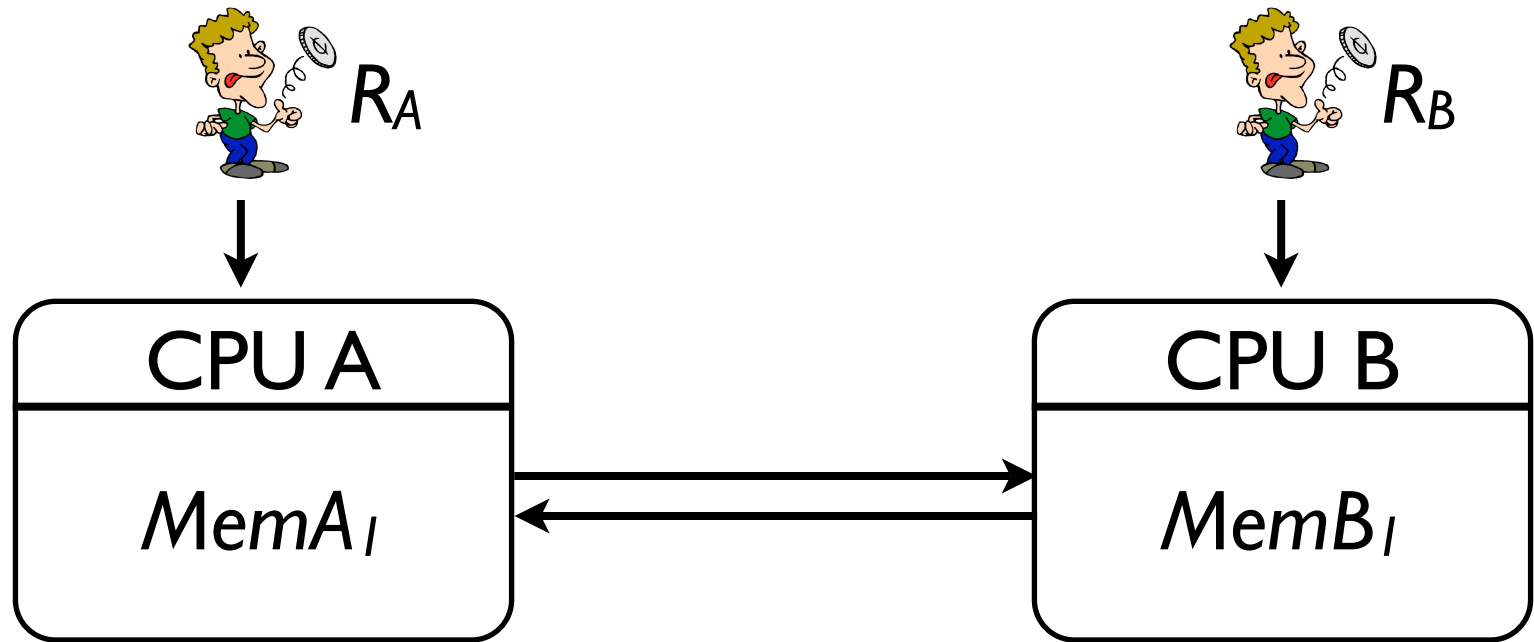
Leakage



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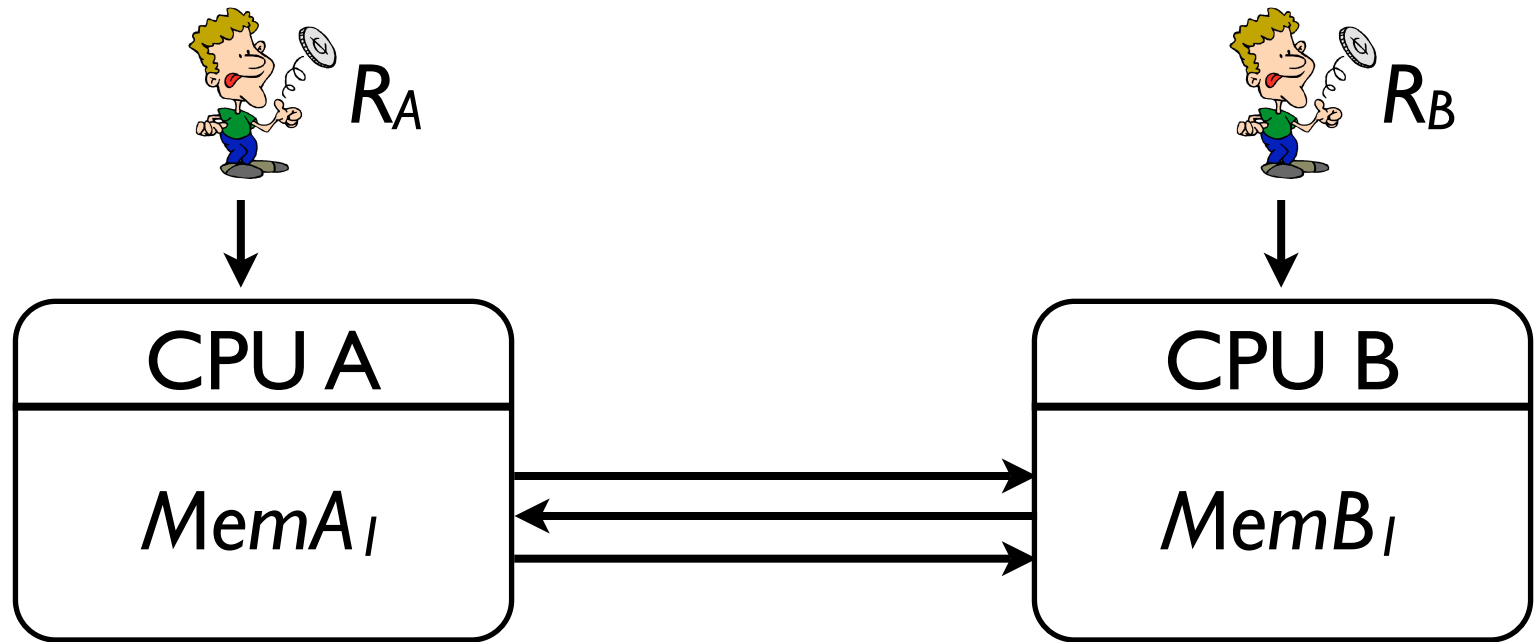


Leakage



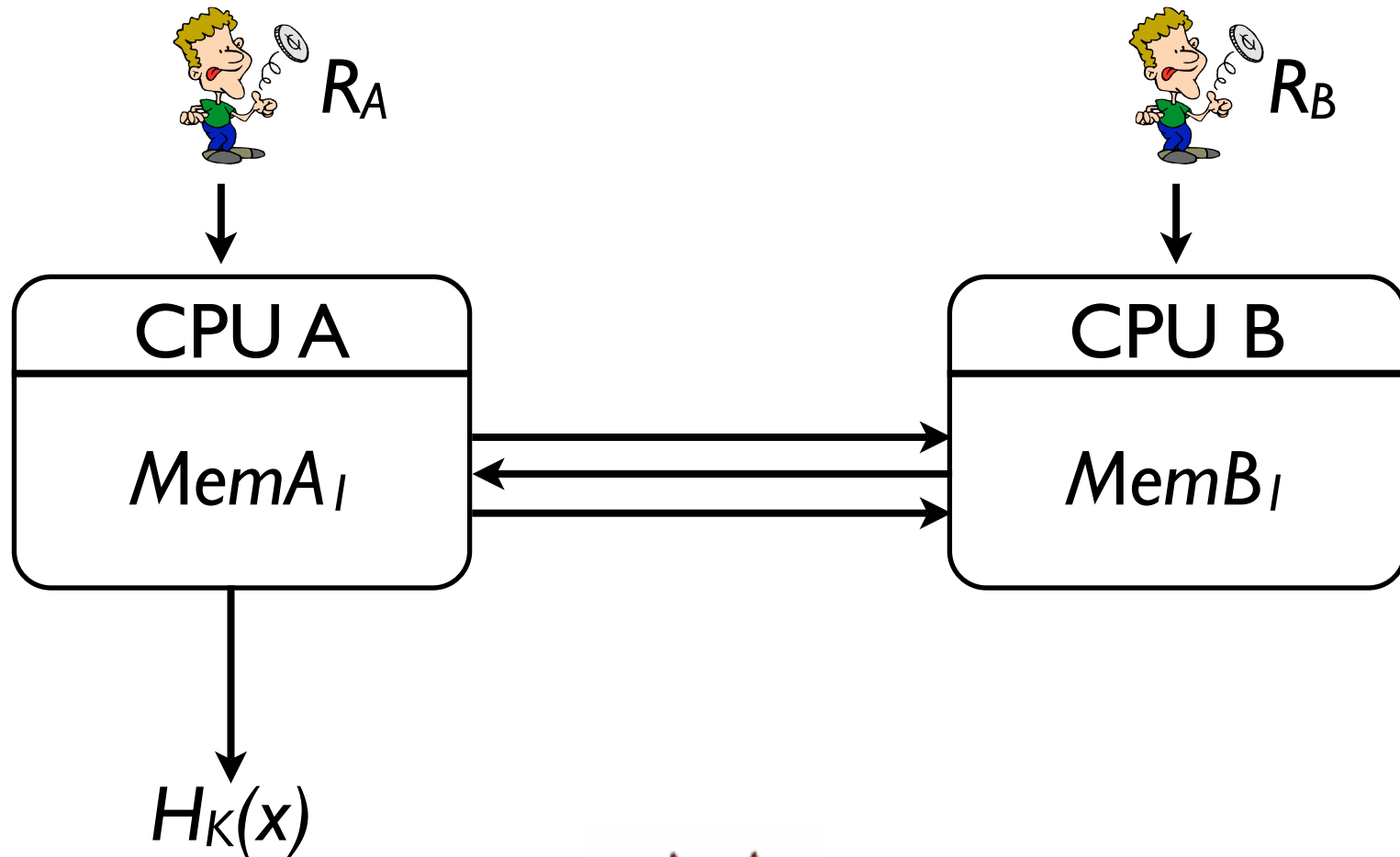
x, L_1, L_2

Leakage



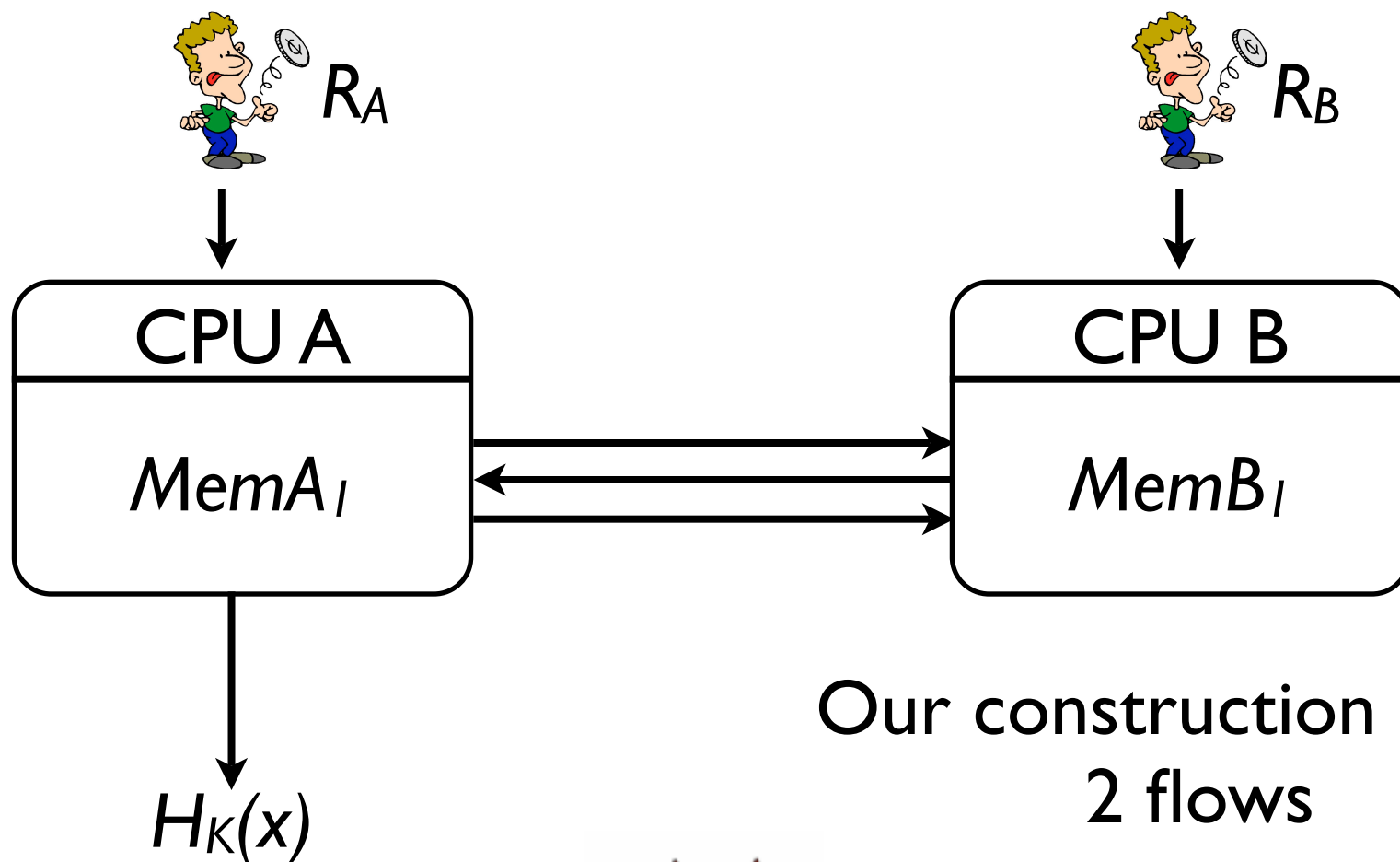
$x, L_1, L_2, L_3 \dots$

Leakage



$x, L_1, L_2, L_3 \dots$

Leakage



Our construction needs
2 flows

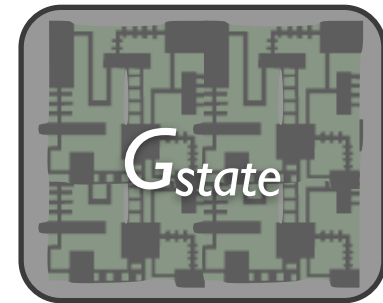


$x, L_1, L_2, L_3 \dots$

Definition of Security

Definition of Security

Real World

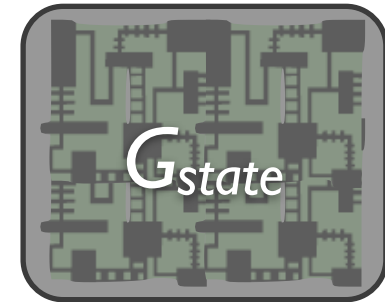


Definition of Security

Real World



$x, leakage()$



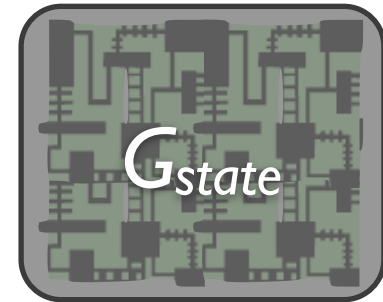
Definition of Security

Real World

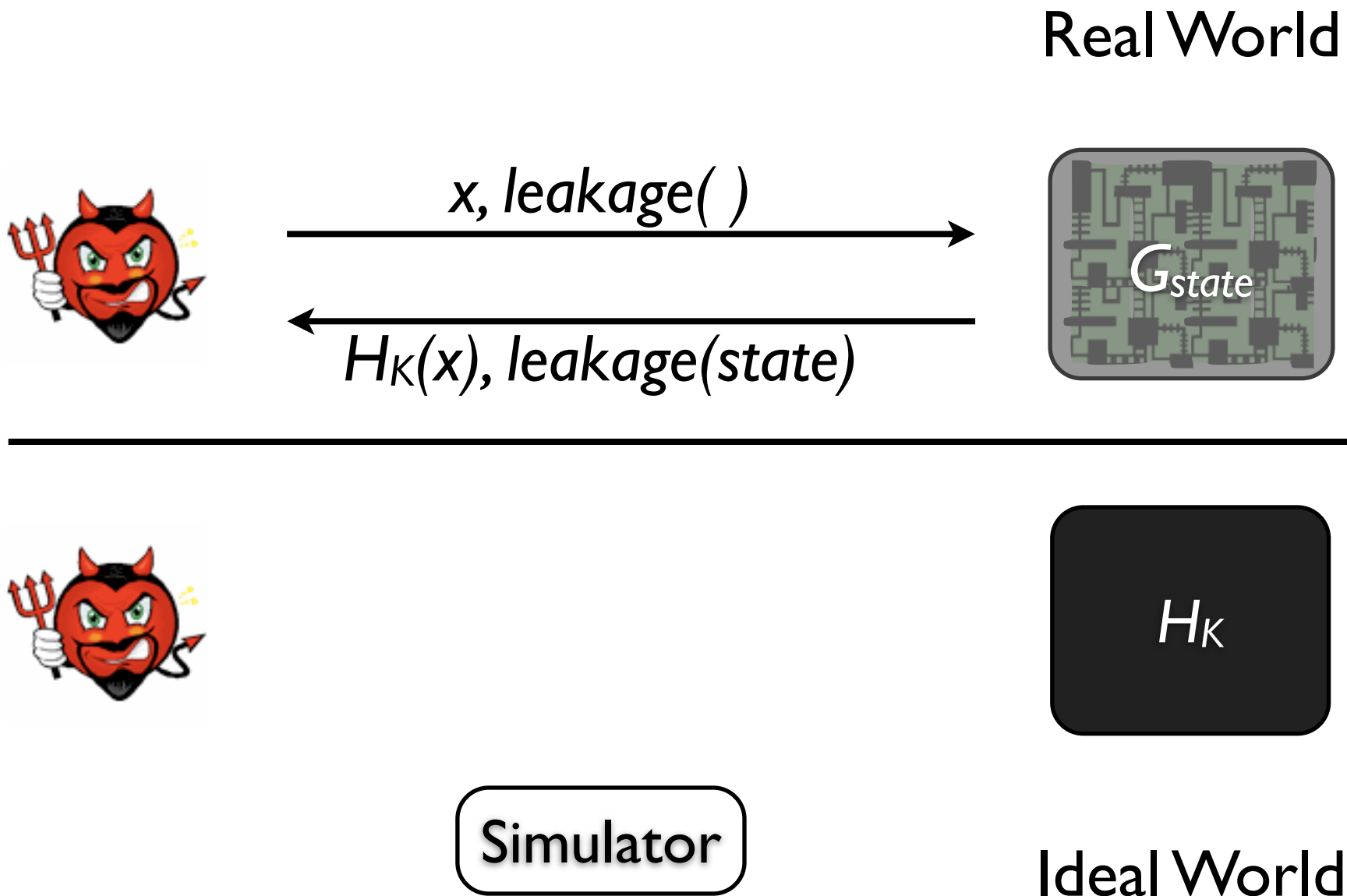


$x, \text{leakage}(\)$

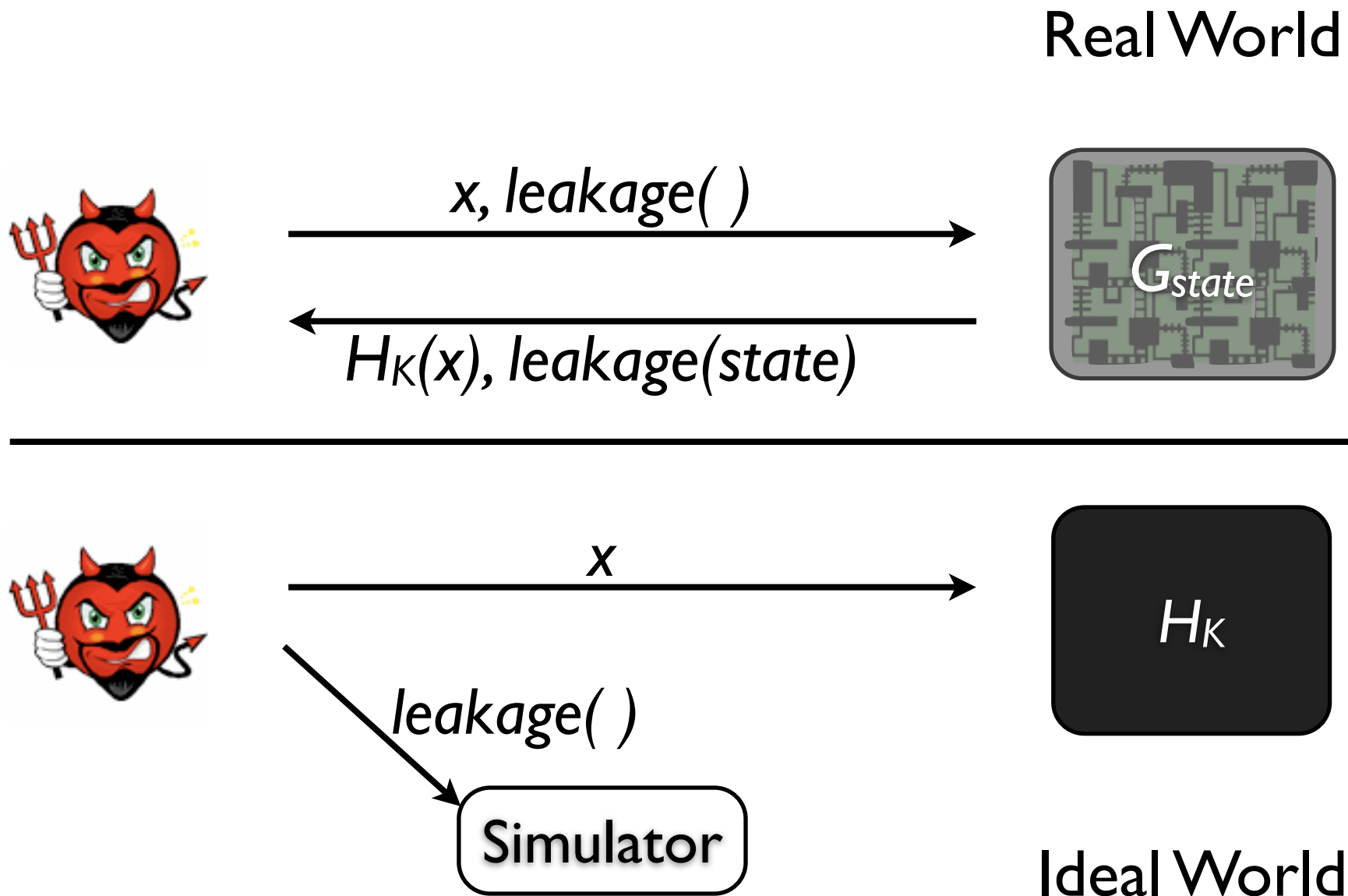
$H_K(x), \text{leakage}(\text{state})$



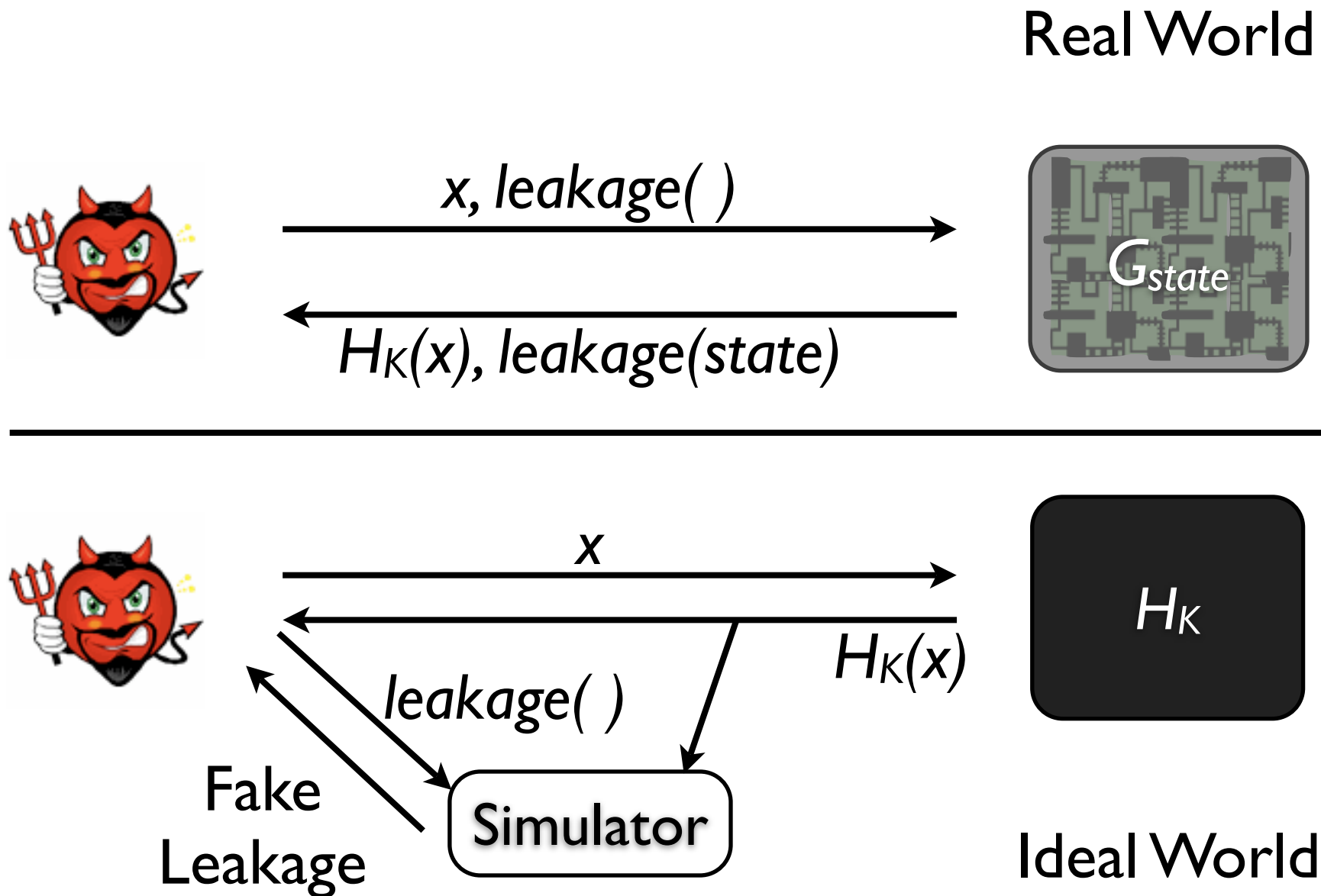
Definition of Security



Definition of Security



Definition of Security



Main Tool

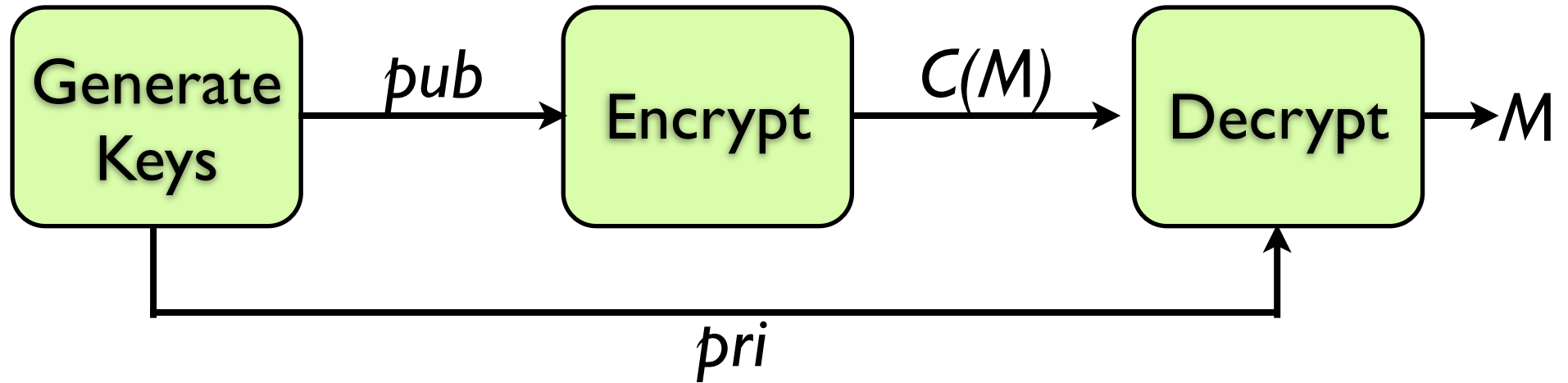
Fully Homomorphic Encryption (FHE)

First construction by Gentry at STOC 09
Based on Ideal Lattices

Other restricted constructions are known
[Boneh Goh Nissim 2005]
[Melchor Gaborit Herranz 2008]

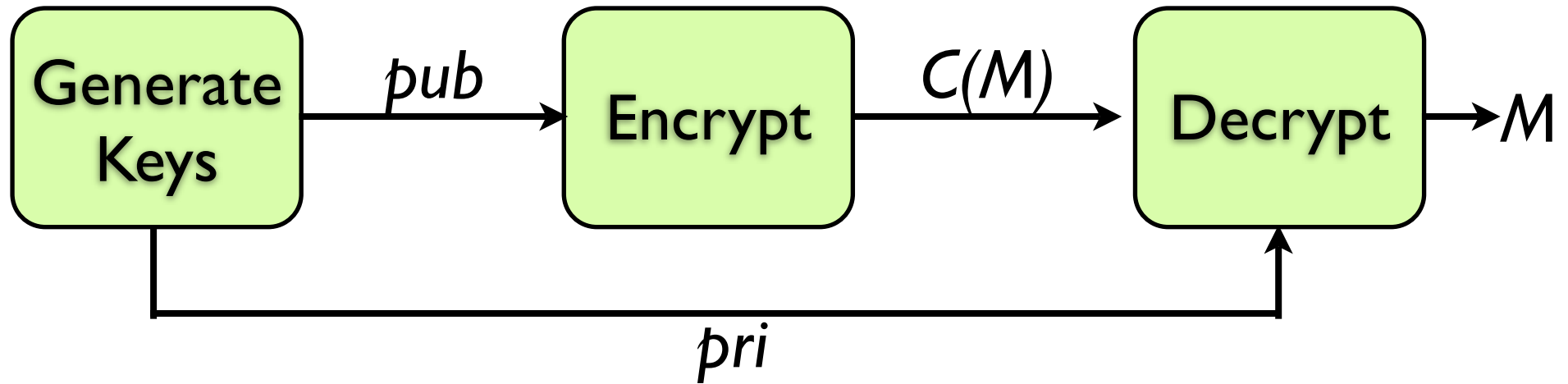
Homomorphic Encryption

Regular Public Key Encryption



Homomorphic Encryption

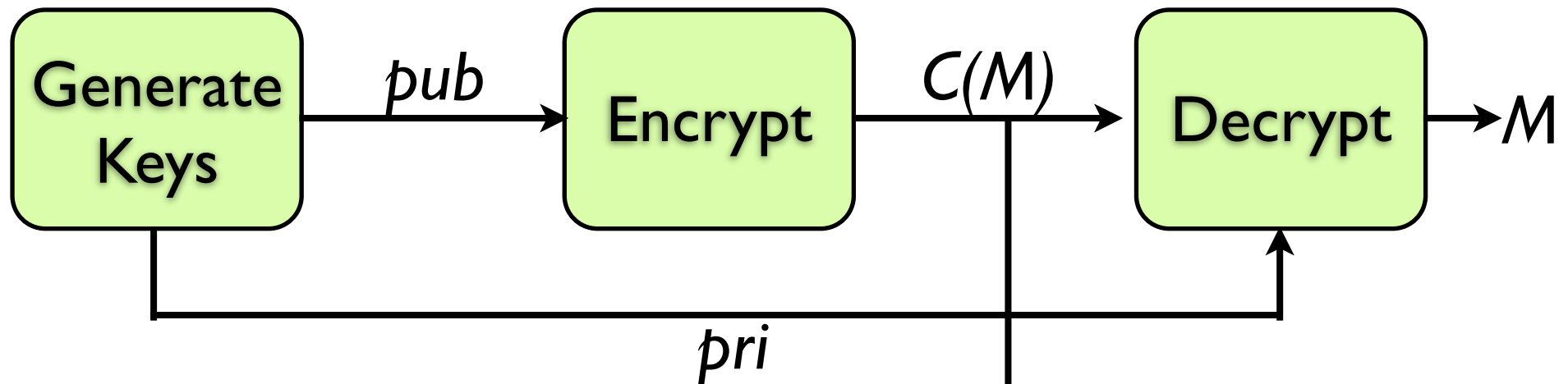
Regular Public Key Encryption



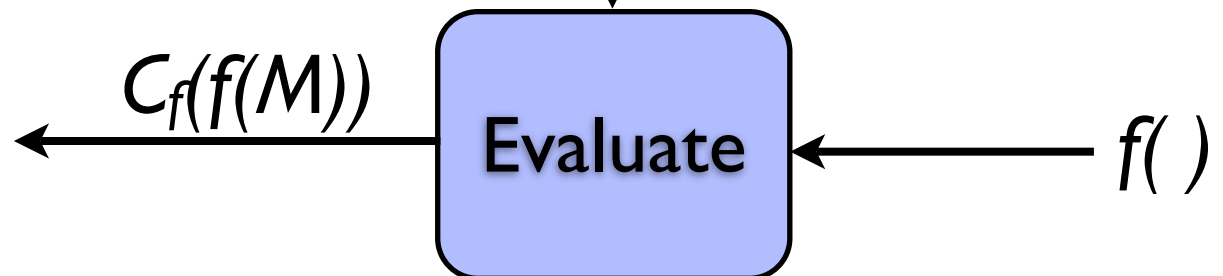
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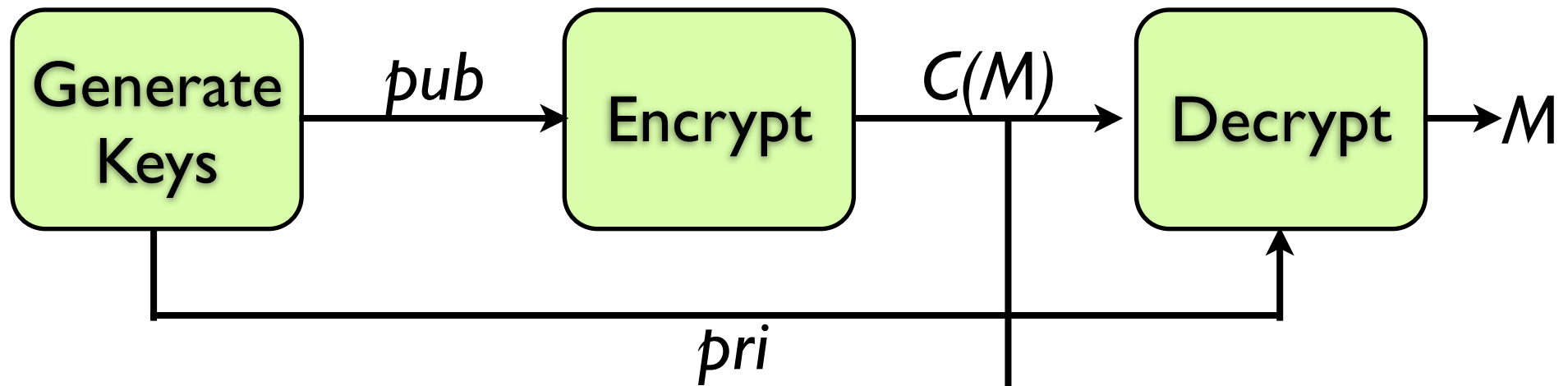


Homomorphic Encryption

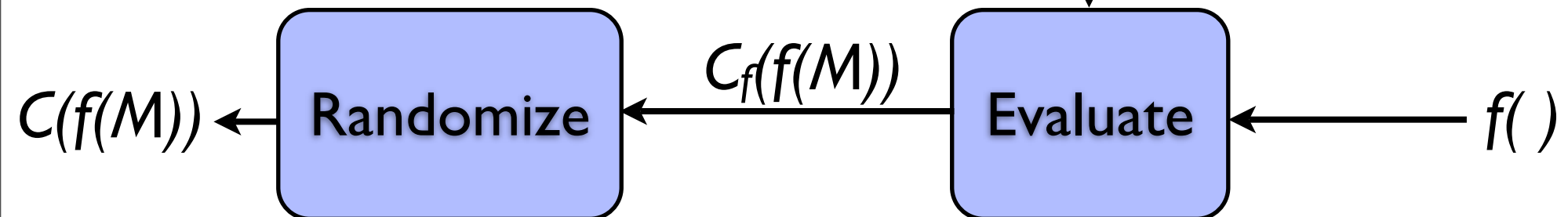


Homomorphic Encryption

Regular Public Key Encryption



Homomorphic Encryption

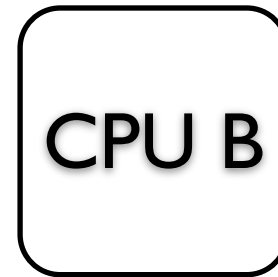
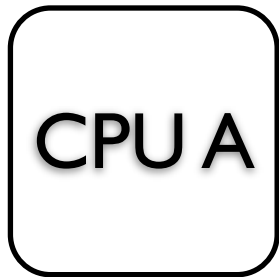


Our Construction

Initialization(K):

Generate keys pr_i, pub

$$C(K) = Enc_{pub}(K)$$



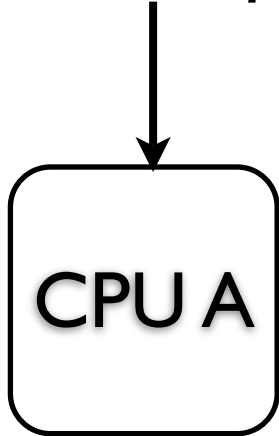
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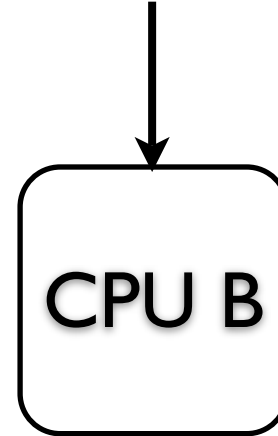
Generate keys pr_i, pub

$$C(K) = Enc_{pub}(K)$$

$MemA_i = pri$



$MemB_i = C(K)$



First Attempt

Step 1: CPU A

$MemA_i = pri_i$

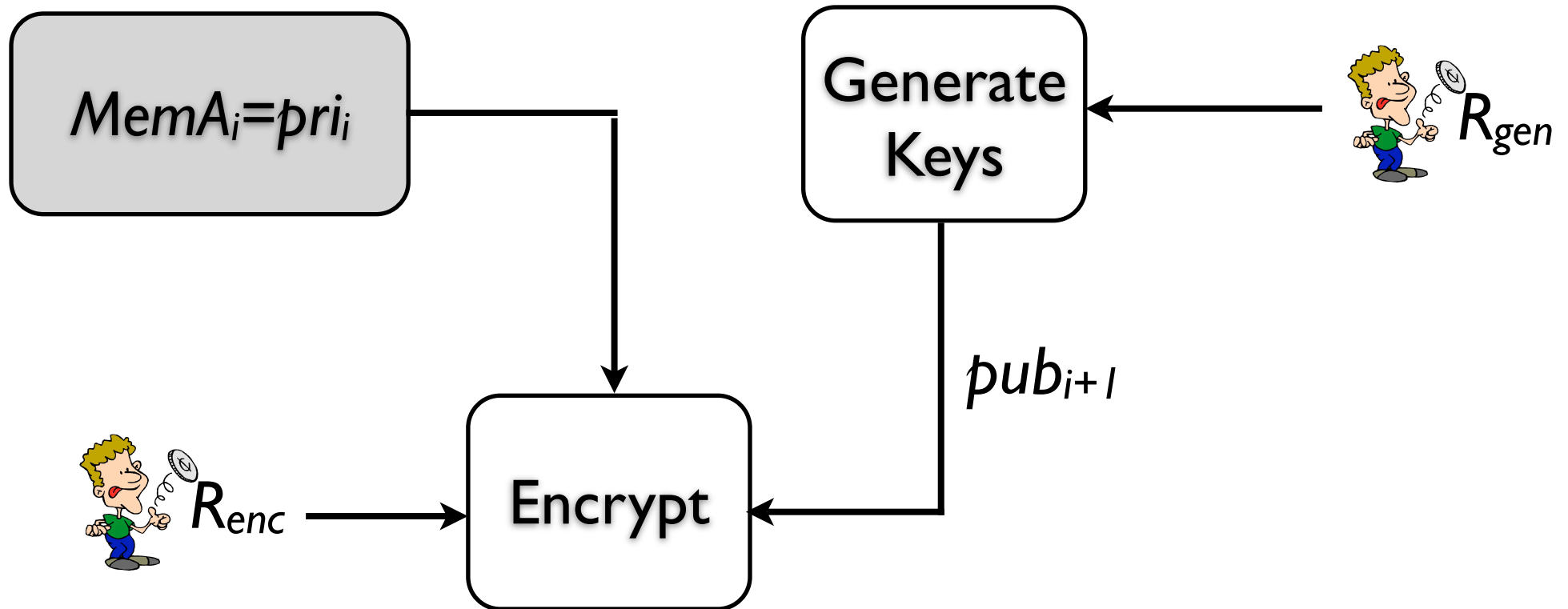
Generate
Keys



Encrypt

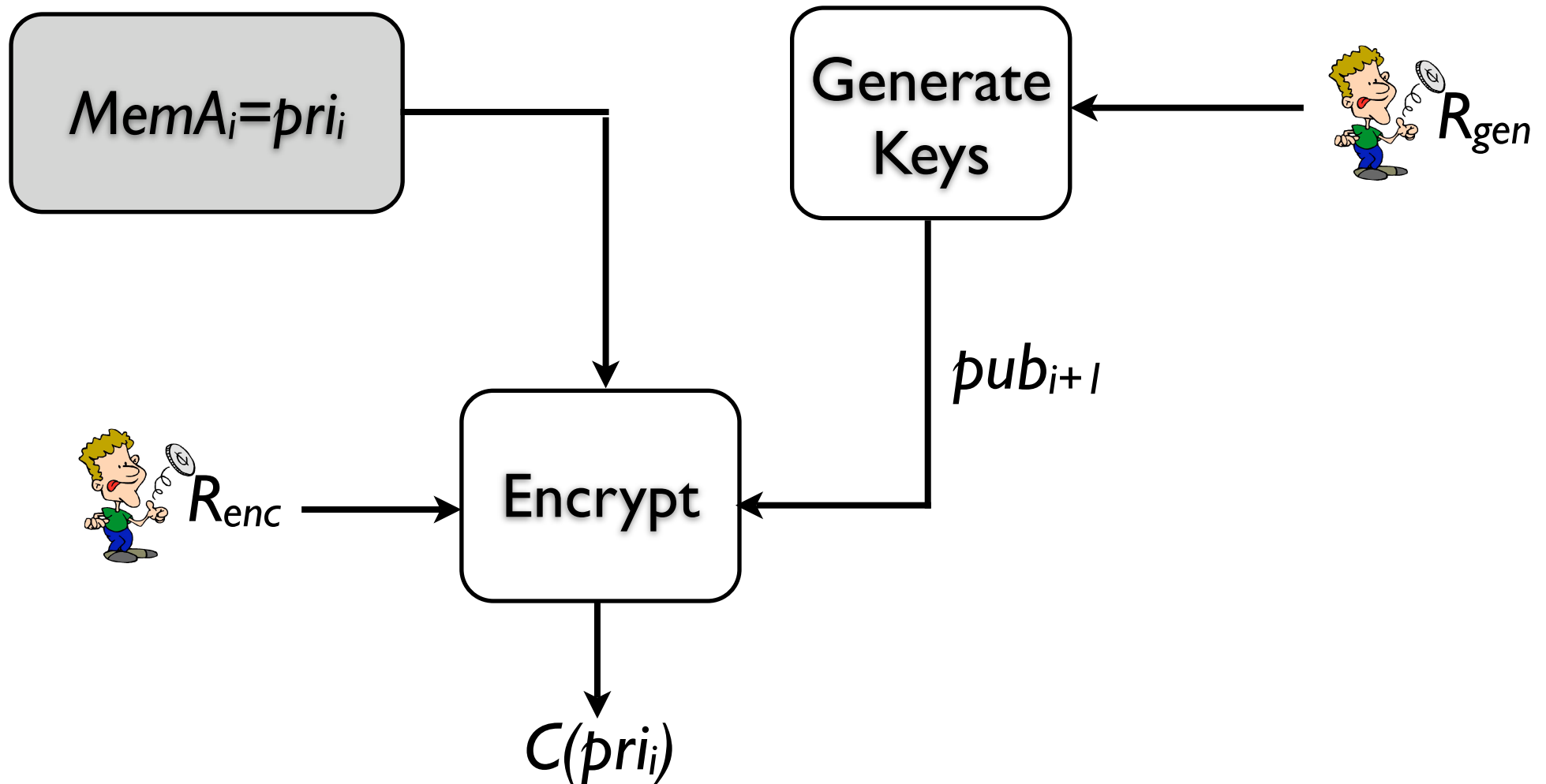
First Attempt

Step 1: CPU A



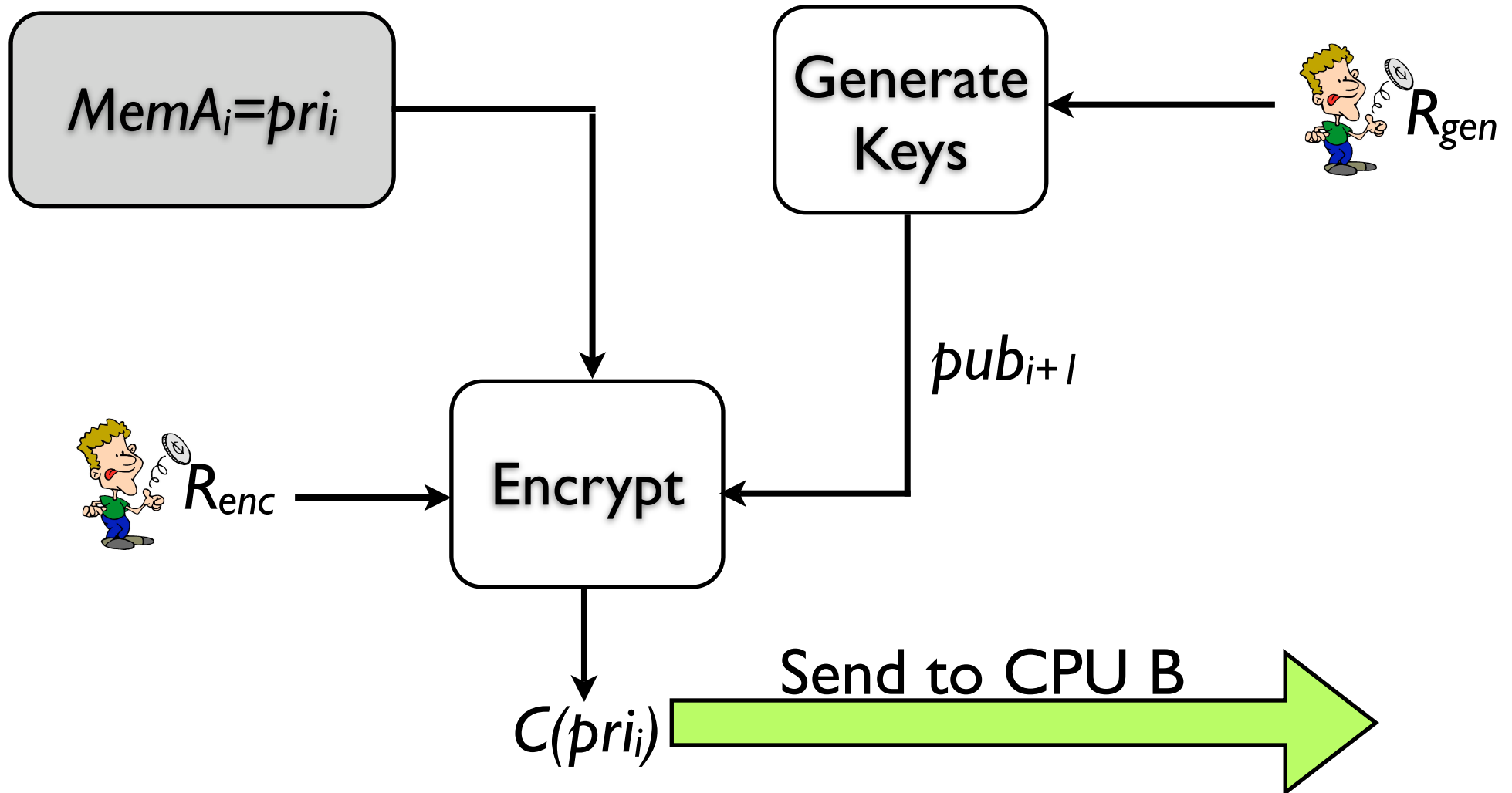
First Attempt

Step 1: CPU A



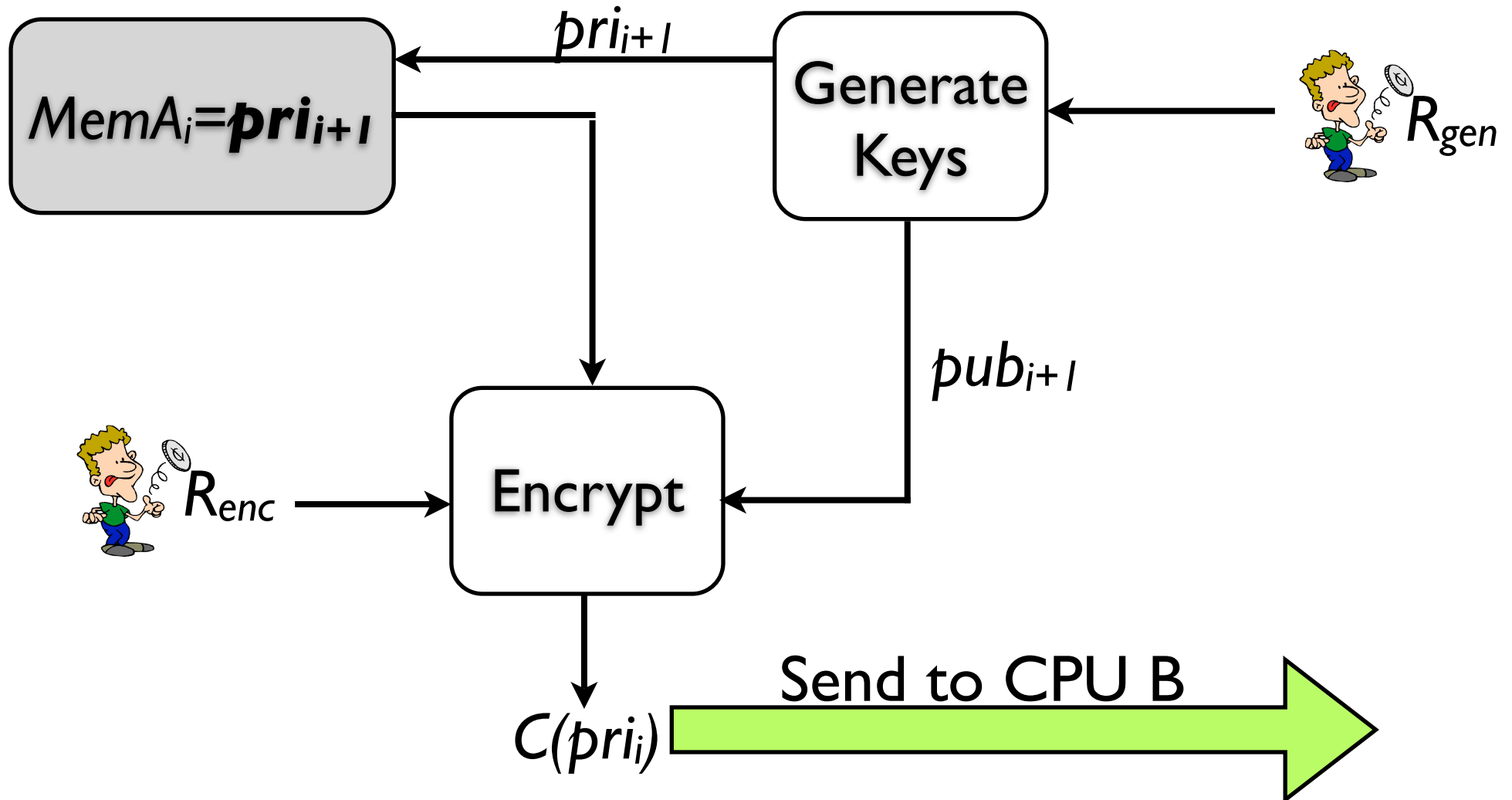
First Attempt

Step 1: CPU A



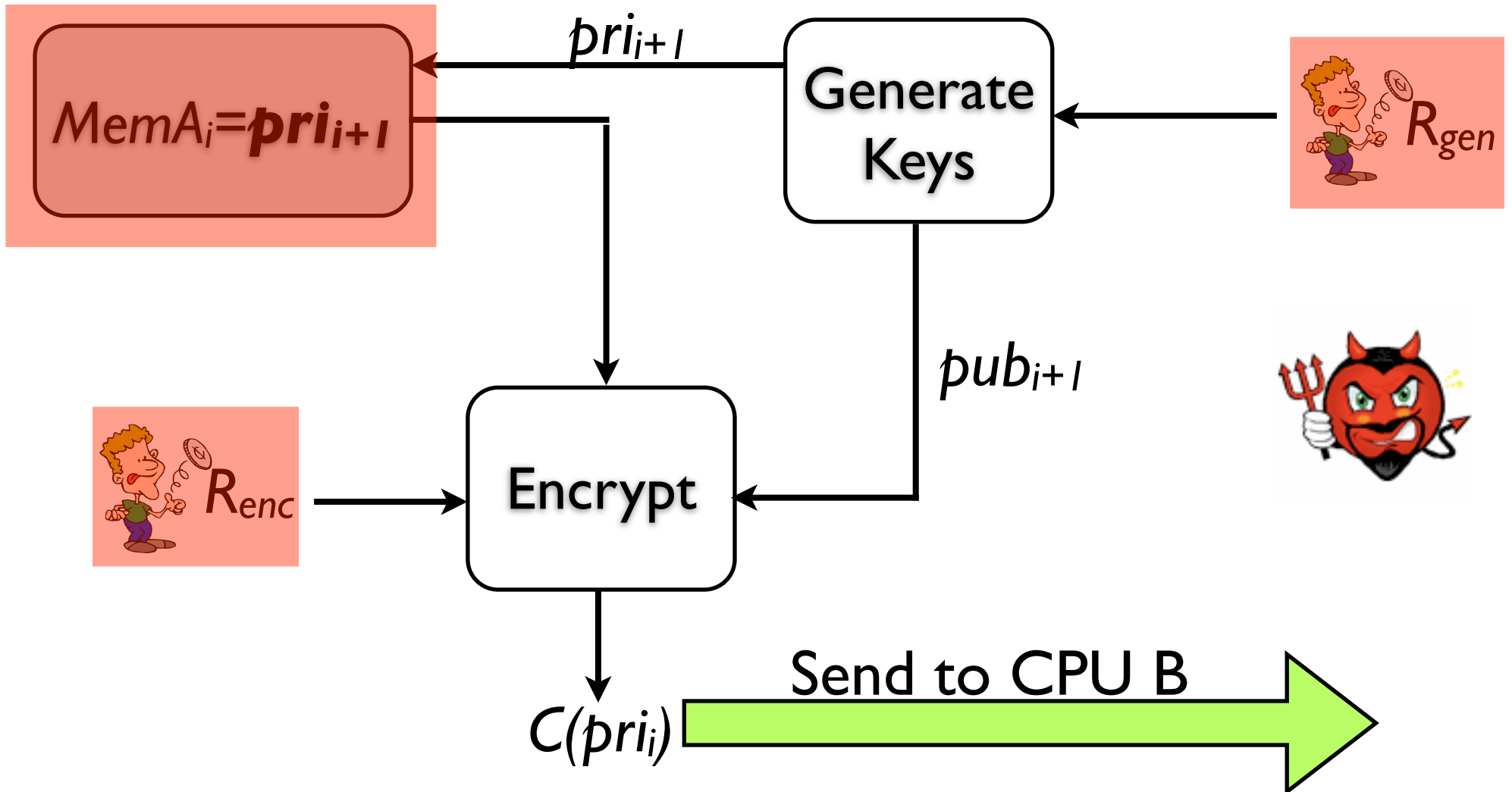
First Attempt

Step 1: CPU A



First Attempt

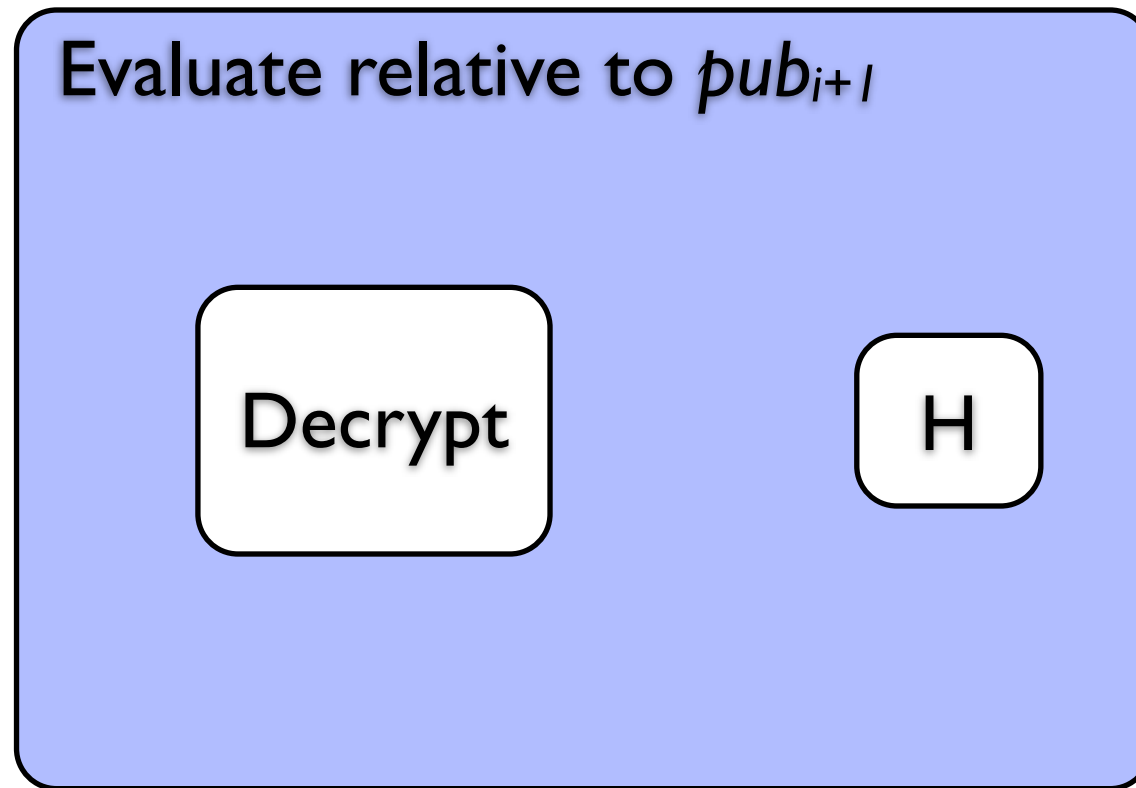
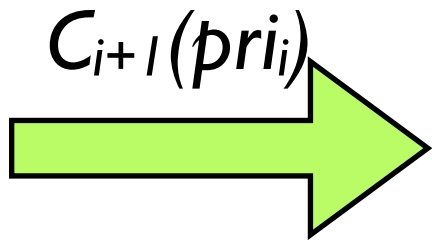
Step 1: CPU A



First Attempt

Step 2: CPU B

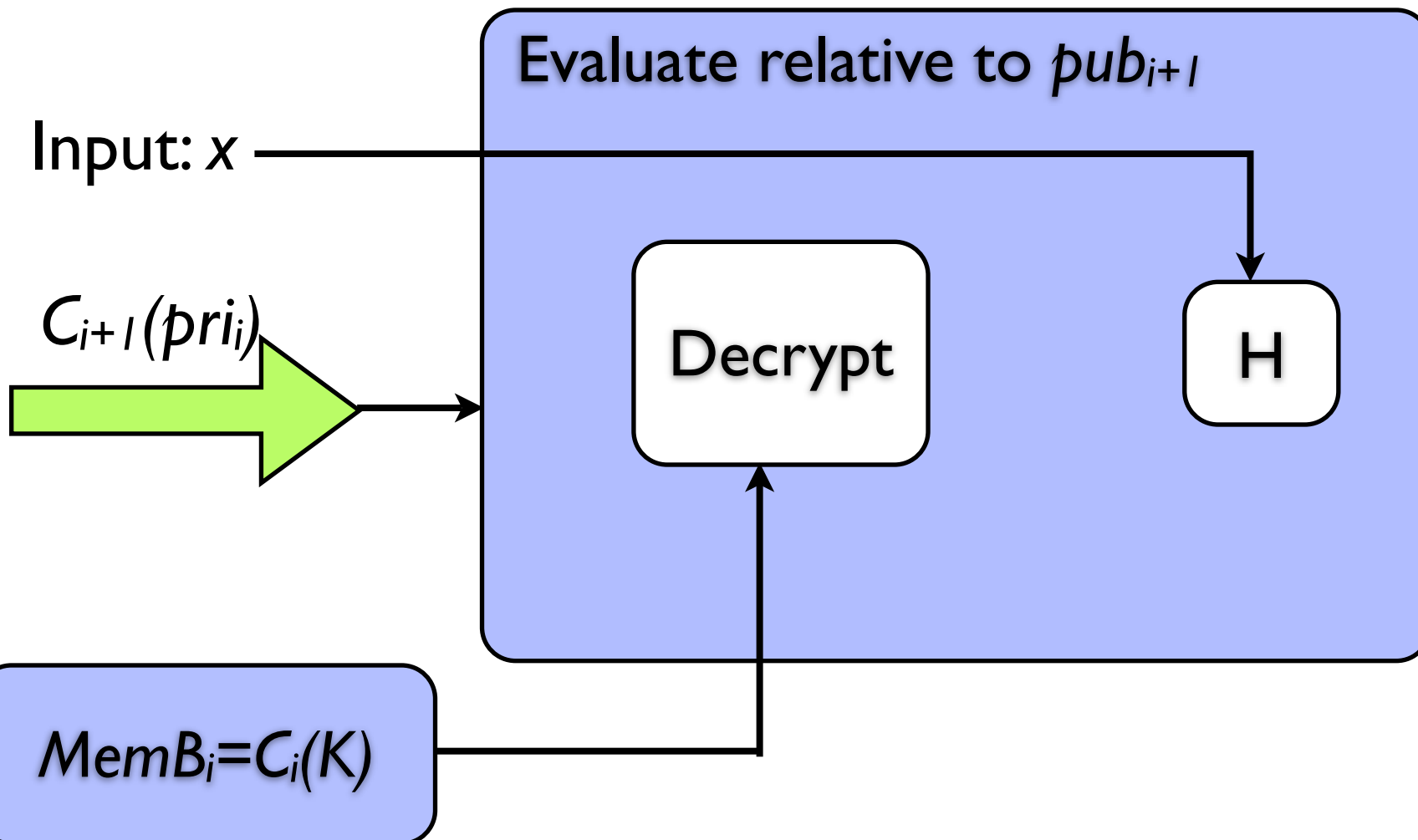
Input: x



$MemB_i = C_i(K)$

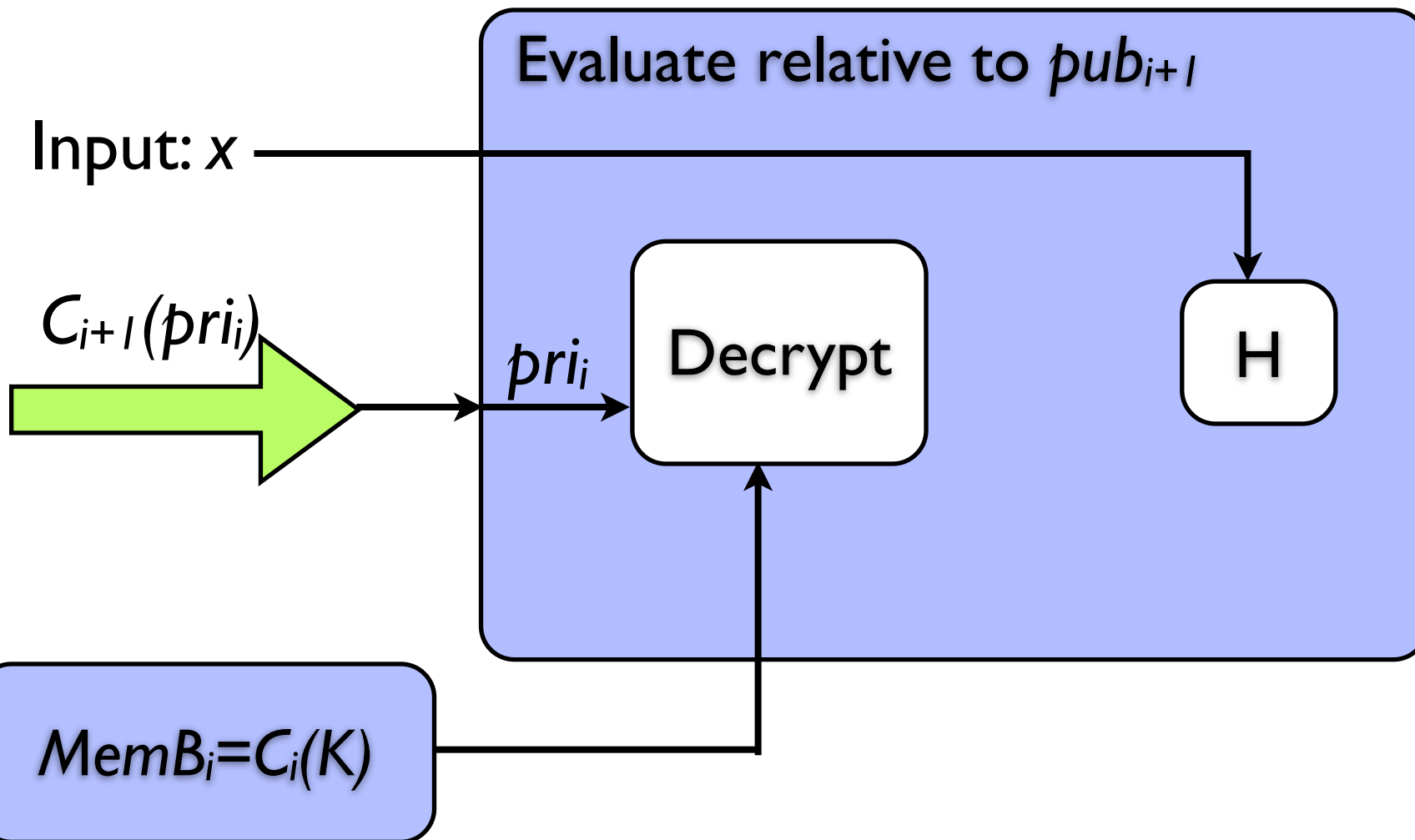
First Attempt

Step 2: CPU B



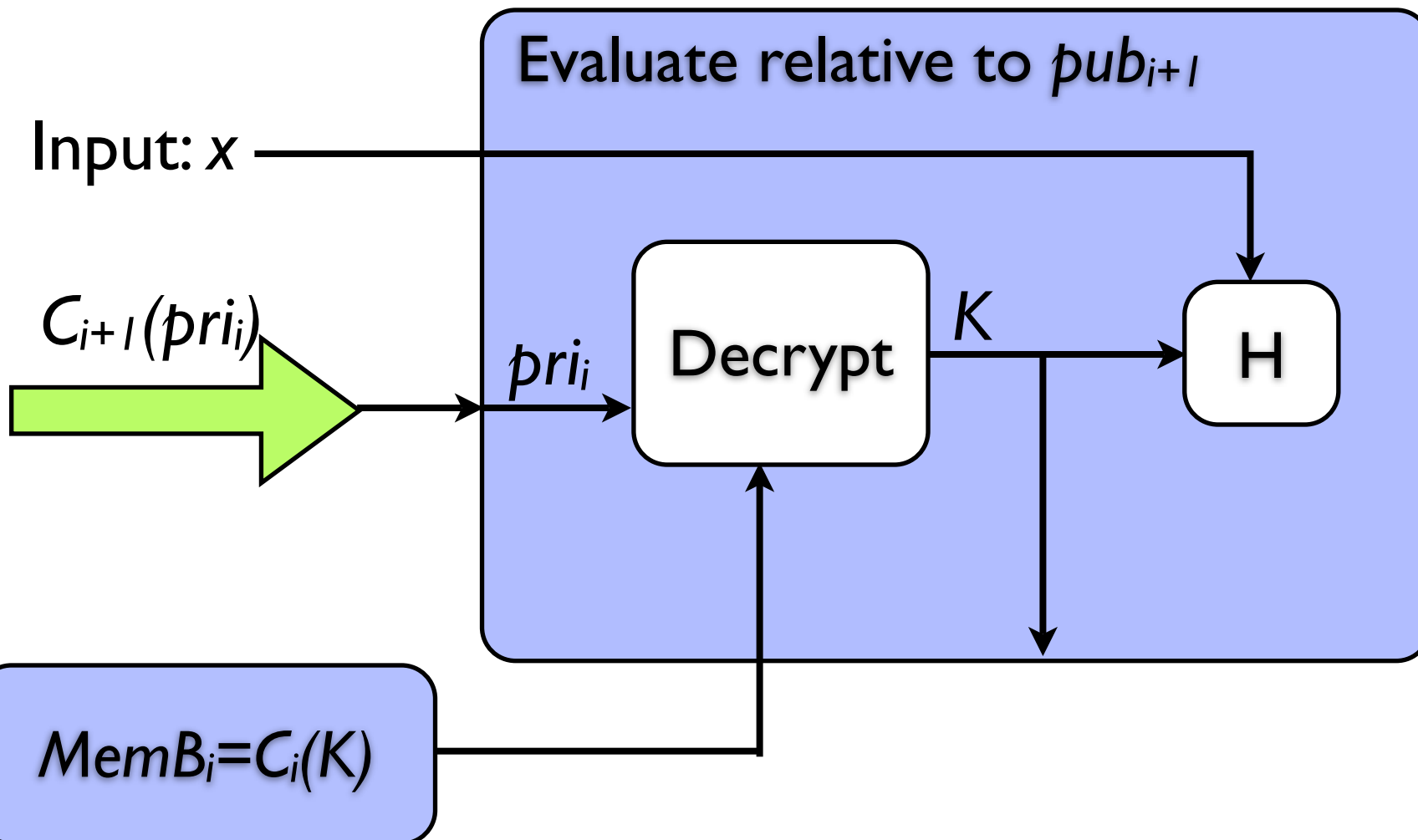
First Attempt

Step 2: CPU B



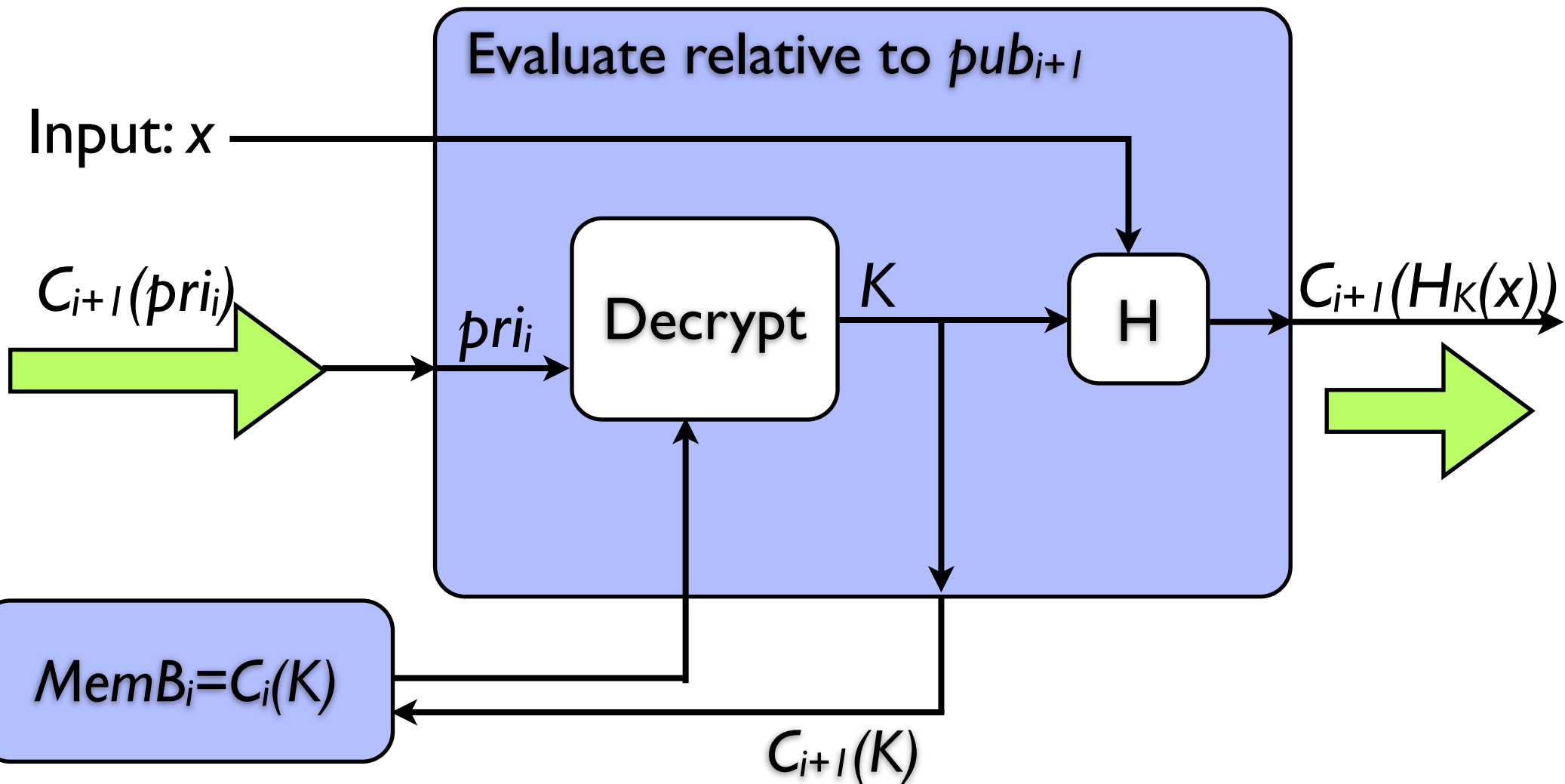
First Attempt

Step 2: CPU B



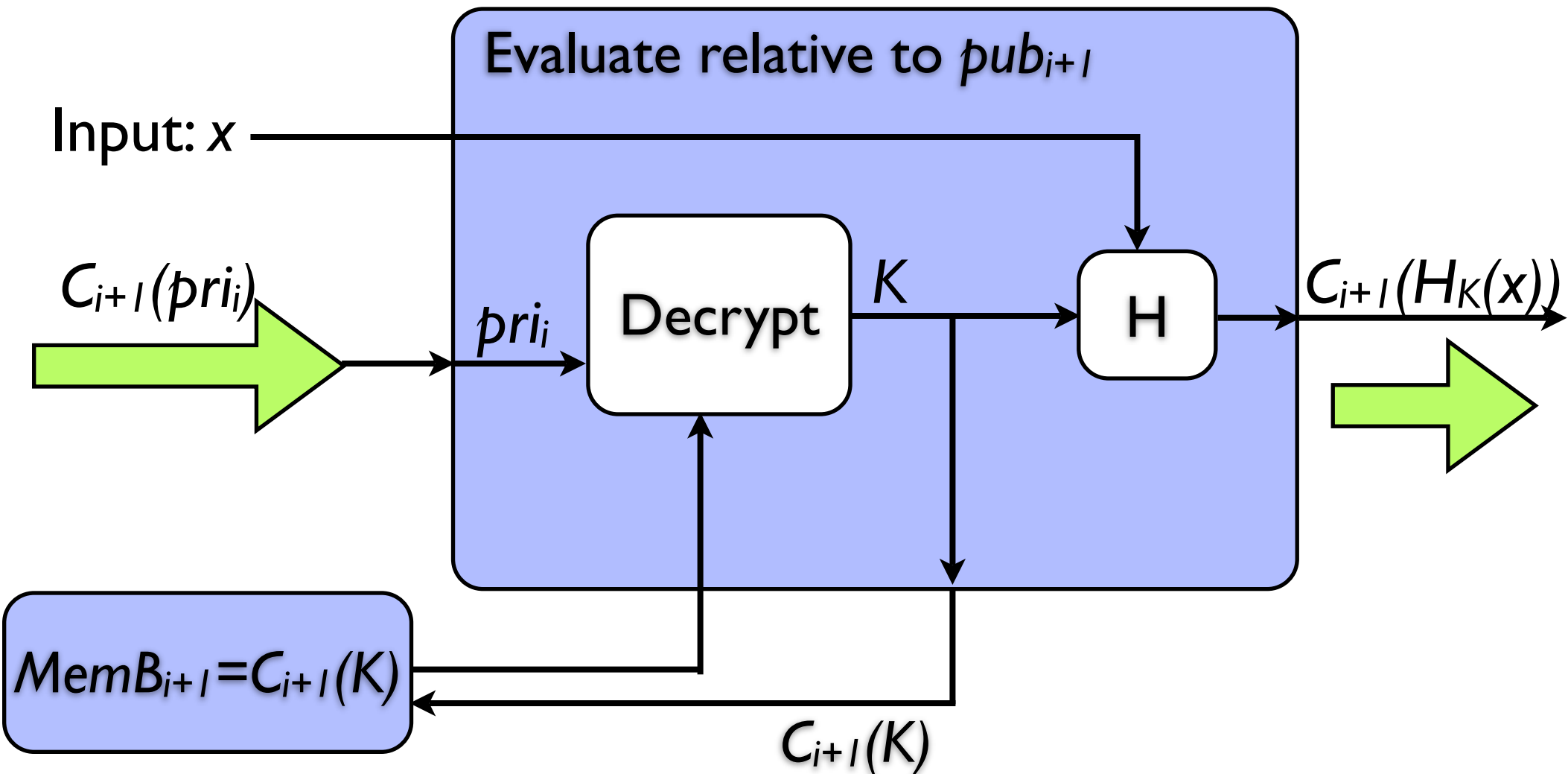
First Attempt

Step 2: CPU B



First Attempt

Step 2: CPU B



First Attempt

Step 3: CPU A

$MemA_i = pri_{i+1}$

$C_{i+1}(H_K(x))$

Decrypt

```
graph LR; A[MemA_i = pri_{i+1}] --> B[Decrypt]; C[C_{i+1}(H_K(x))] --> B;
```

First Attempt

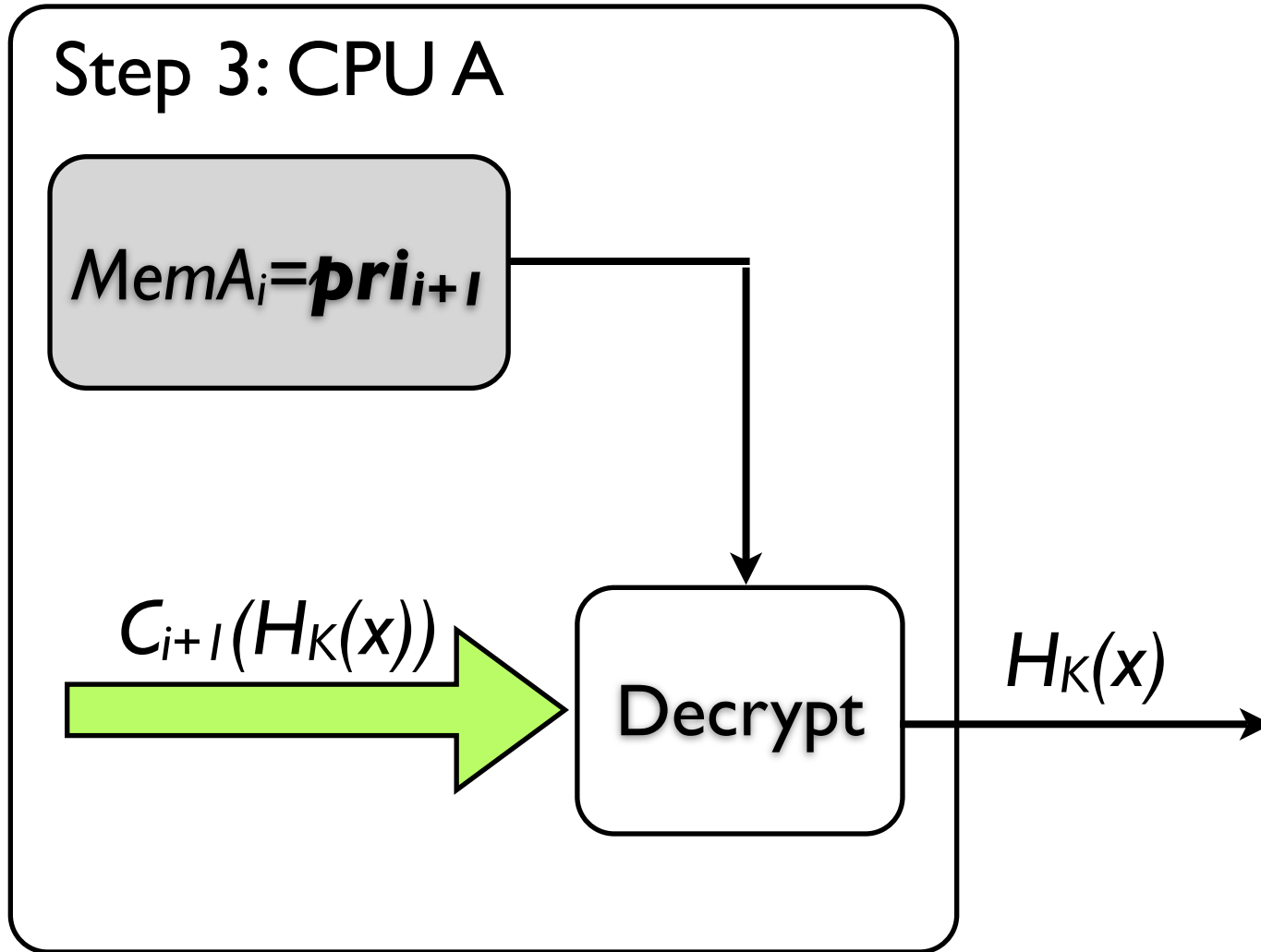
Step 3: CPU A

$MemA_i = pri_{i+1}$

$C_{i+1}(H_K(x))$

Decrypt

$H_K(x)$



First Attempt

Step 3: CPU A

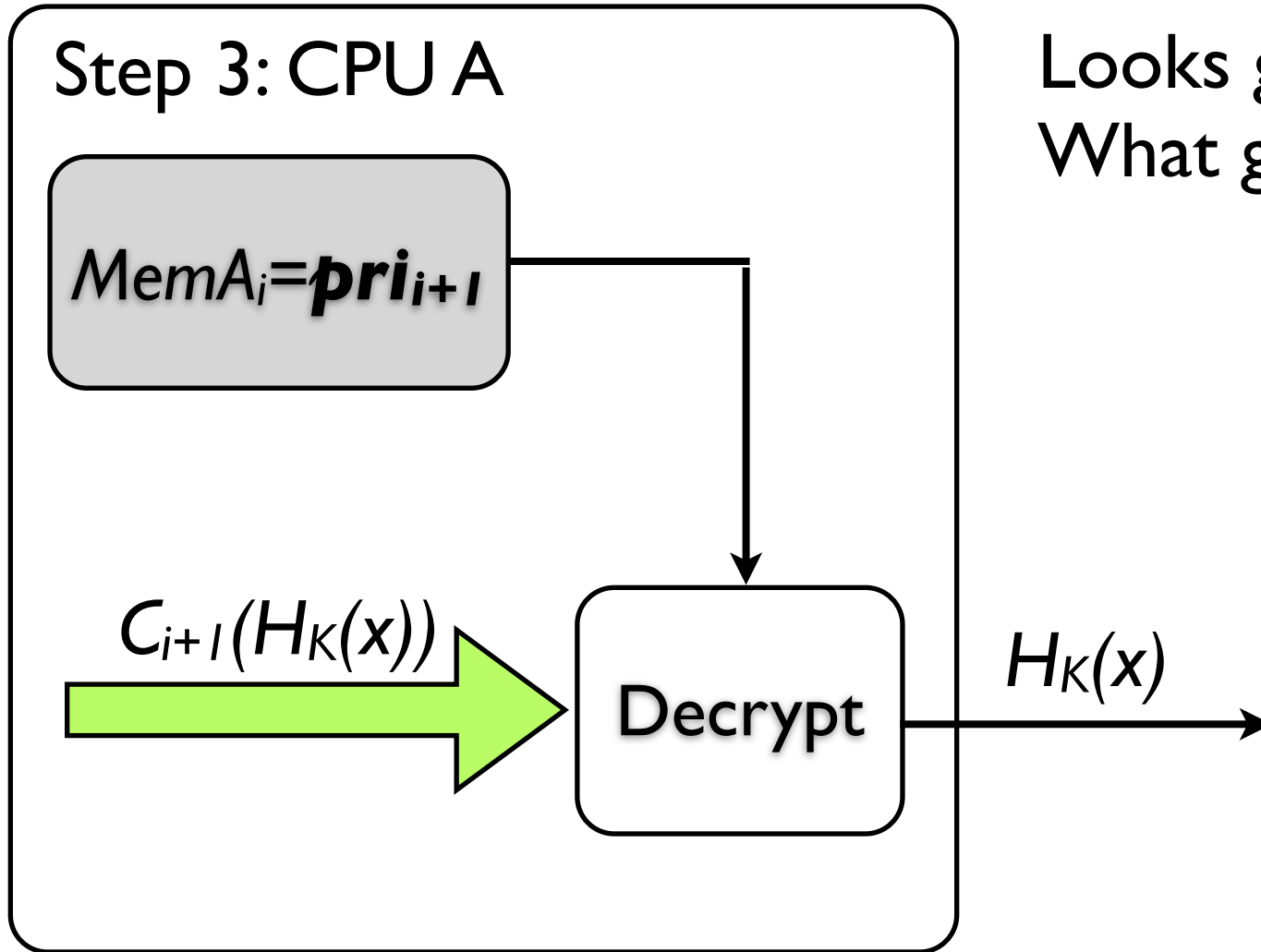
$MemA_i = pri_{i+1}$

$C_{i+1}(H_K(x))$

Decrypt

$H_K(x)$

Looks great!
What goes wrong?



First Attempt

Step 3: CPU A

$MemA_i = pri_{i+1}$

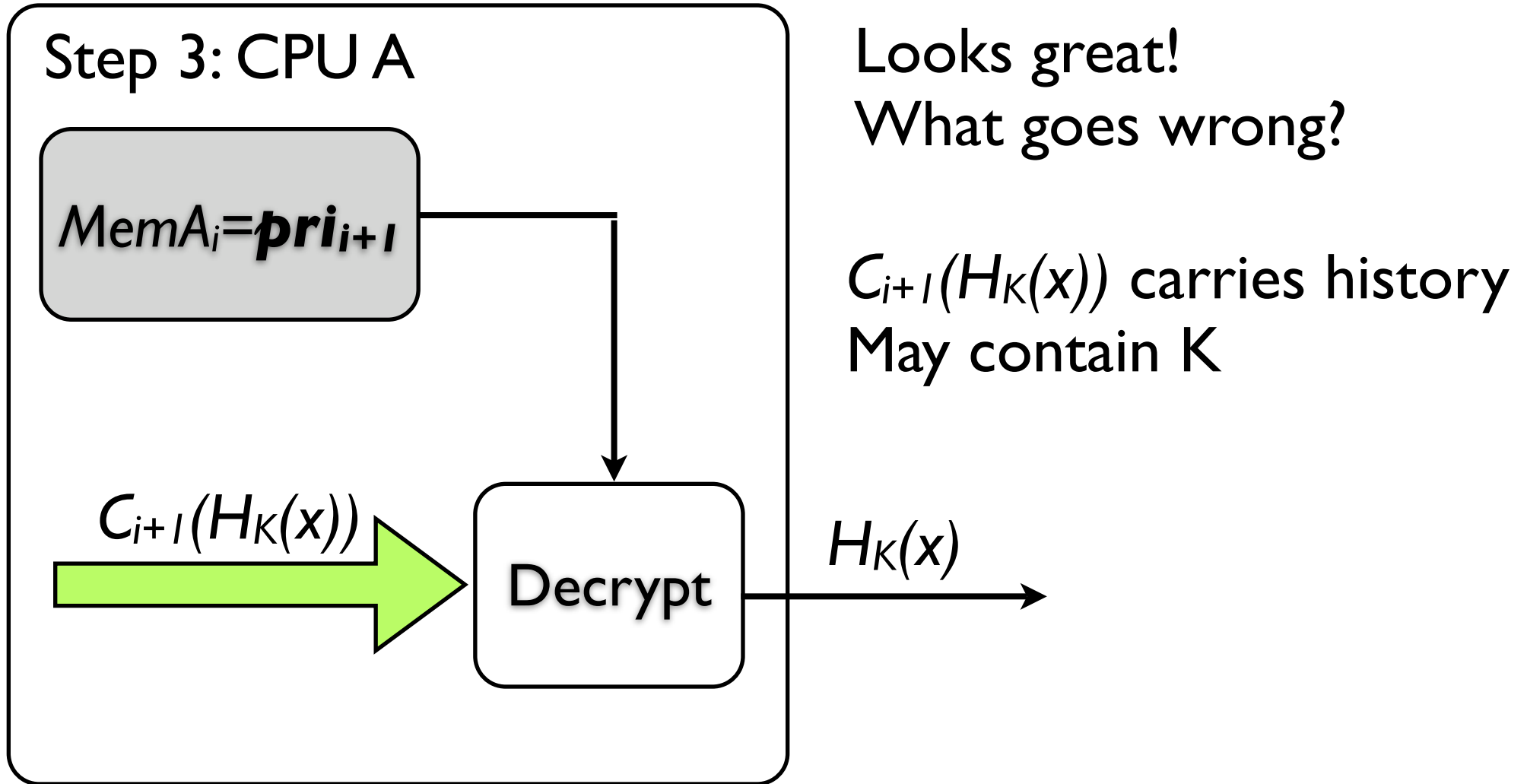
$C_{i+1}(H_K(x))$

Decrypt

$H_K(x)$

Looks great!
What goes wrong?

$C_{i+1}(H_K(x))$ carries history
May contain K



First Attempt

Step 3: CPU A

$MemA_i = pri_{i+1}$

$C_{i+1}(H_K(x))$

Decrypt'

$H_K(x)$

K

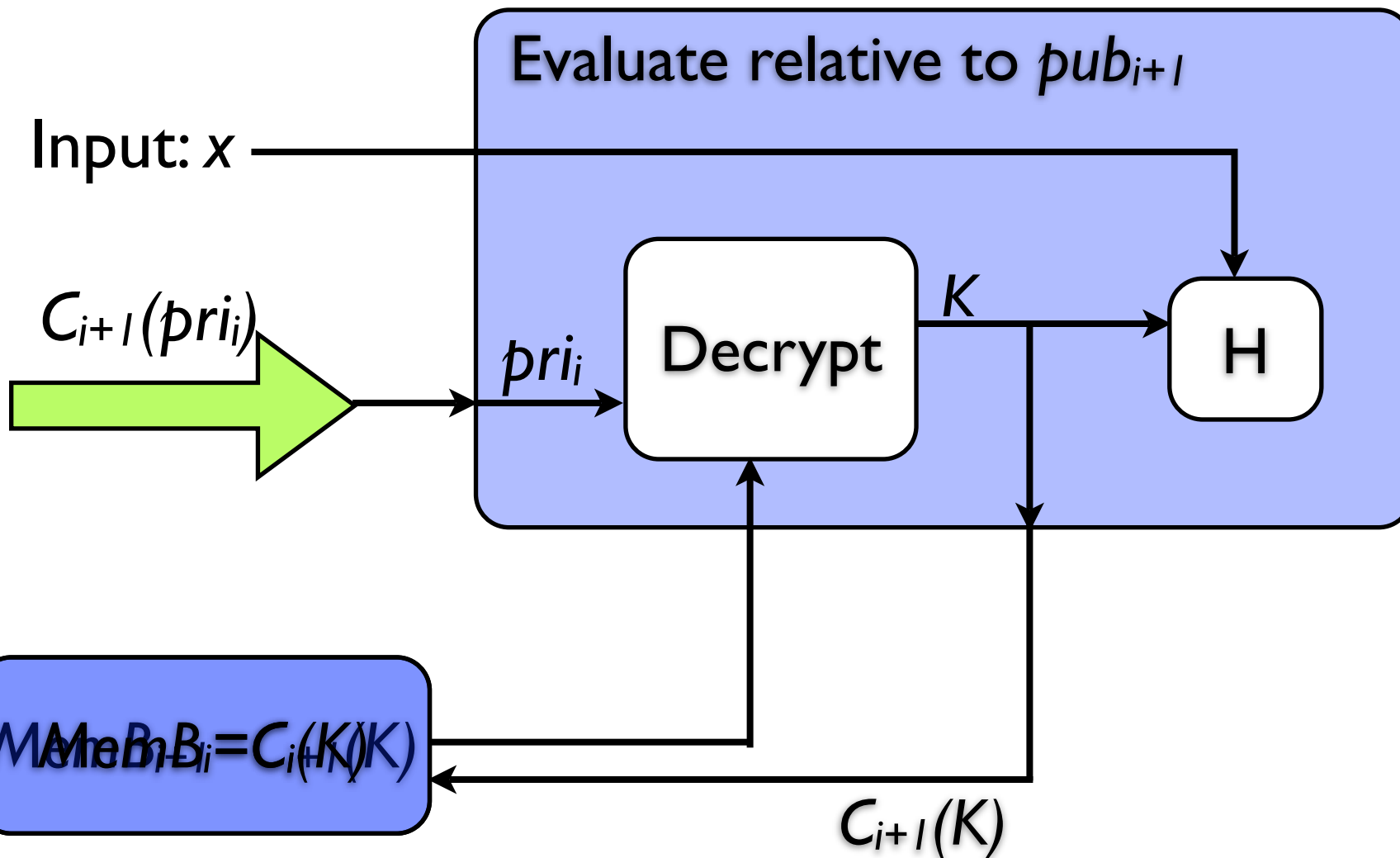
Looks great!
What goes wrong?

$C_{i+1}(H_K(x))$ carries history
May contain K



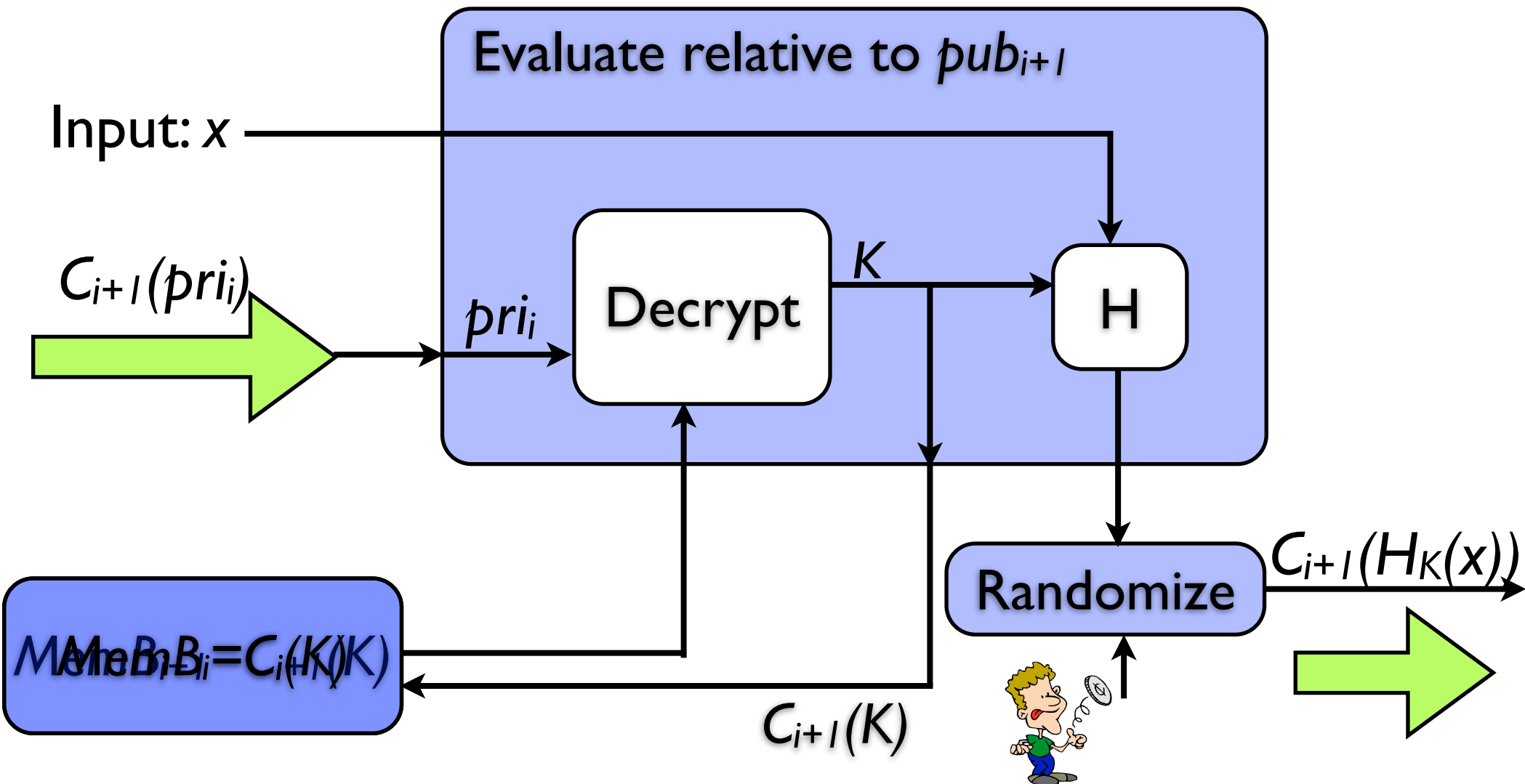
Second Attempt

Step 2': CPU B



Second Attempt

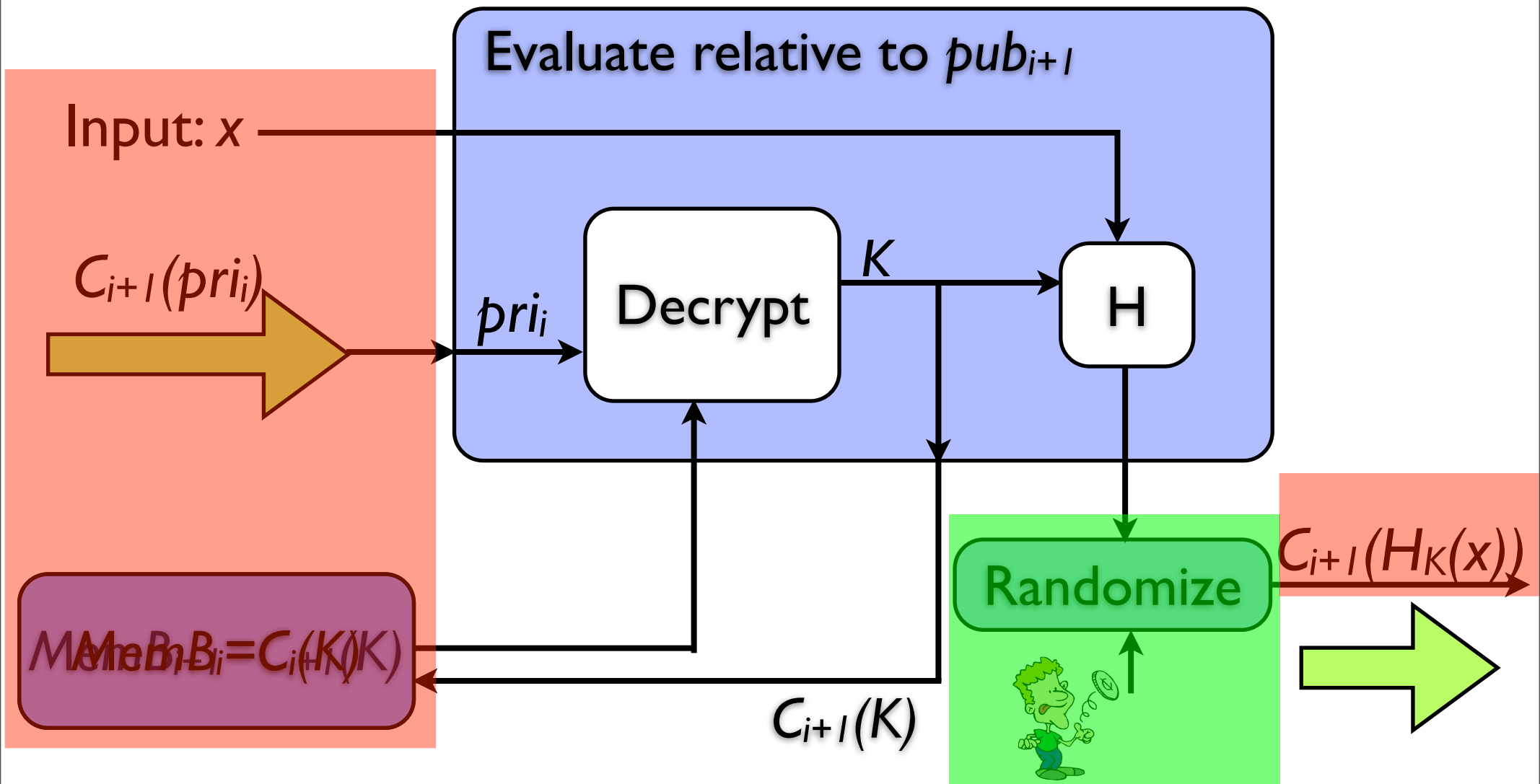
Step 2': CPU B



Second Attempt



Step 2': CPU B



Second Attempt

Step 3: CPU A

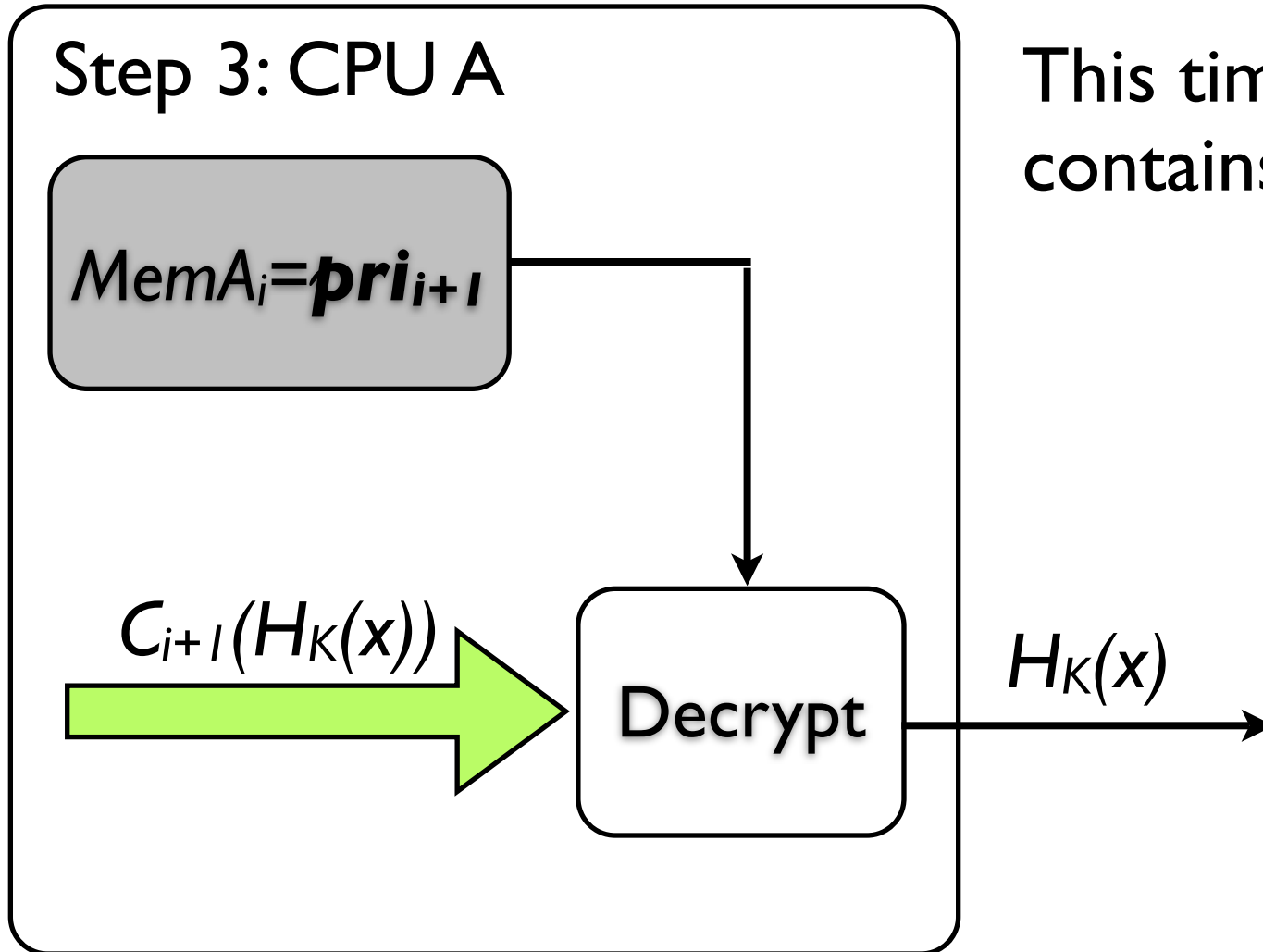
$MemA_i = pri_{i+1}$

$C_{i+1}(H_K(x))$

Decrypt

$H_K(x)$

This time $C_{i+1}(H_K(x))$ only contains $H_K(x)$



Second Attempt

Step 3: CPU A

$MemA_i = pri_{i+1}$

$C_{i+1}(H_K(x))$

Decrypt'

$H_K(x)$

?

This time $C_{i+1}(H_K(x))$ only contains $H_K(x)$



Second Attempt

Step 3: CPU A

$MemA_i = pri_{i+1}$

$C_{i+1}(H_K(x))$

Decrypt'

$H_K(x)$

?

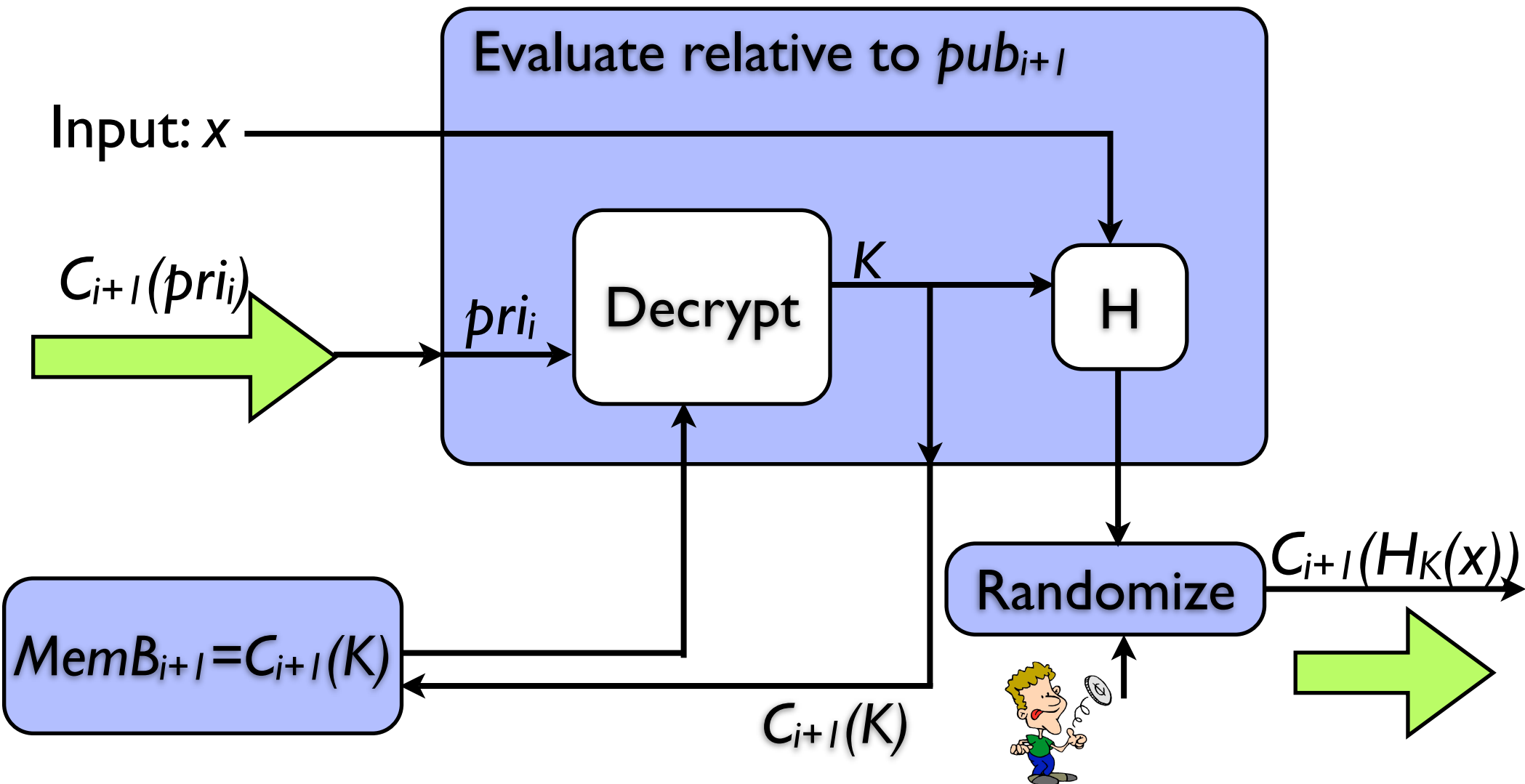
This time $C_{i+1}(H_K(x))$ only contains $H_K(x)$

Are we done?
Not quite...



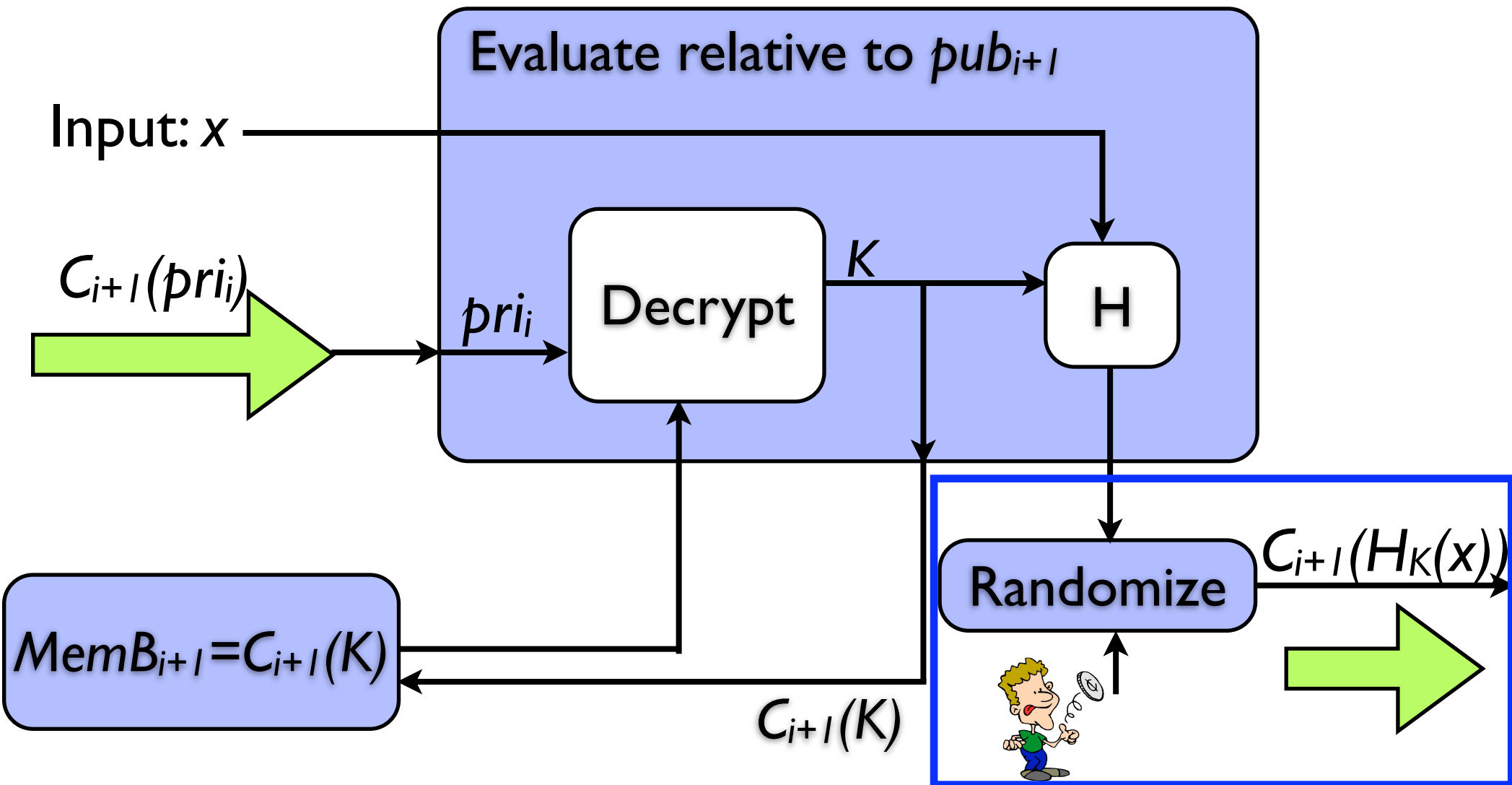
Second Attempt

Step 2': CPU B



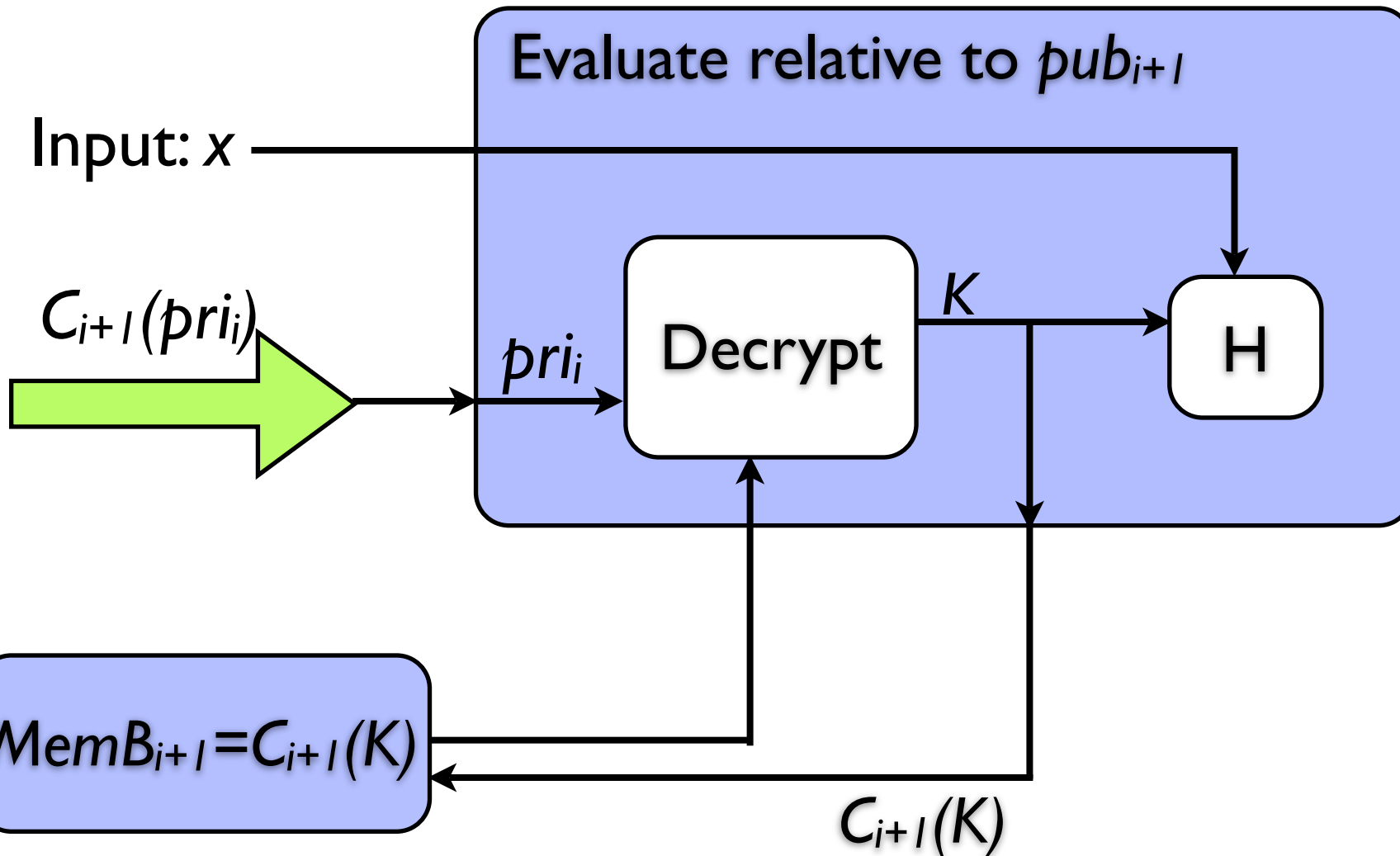
Second Attempt

Step 2': CPU B



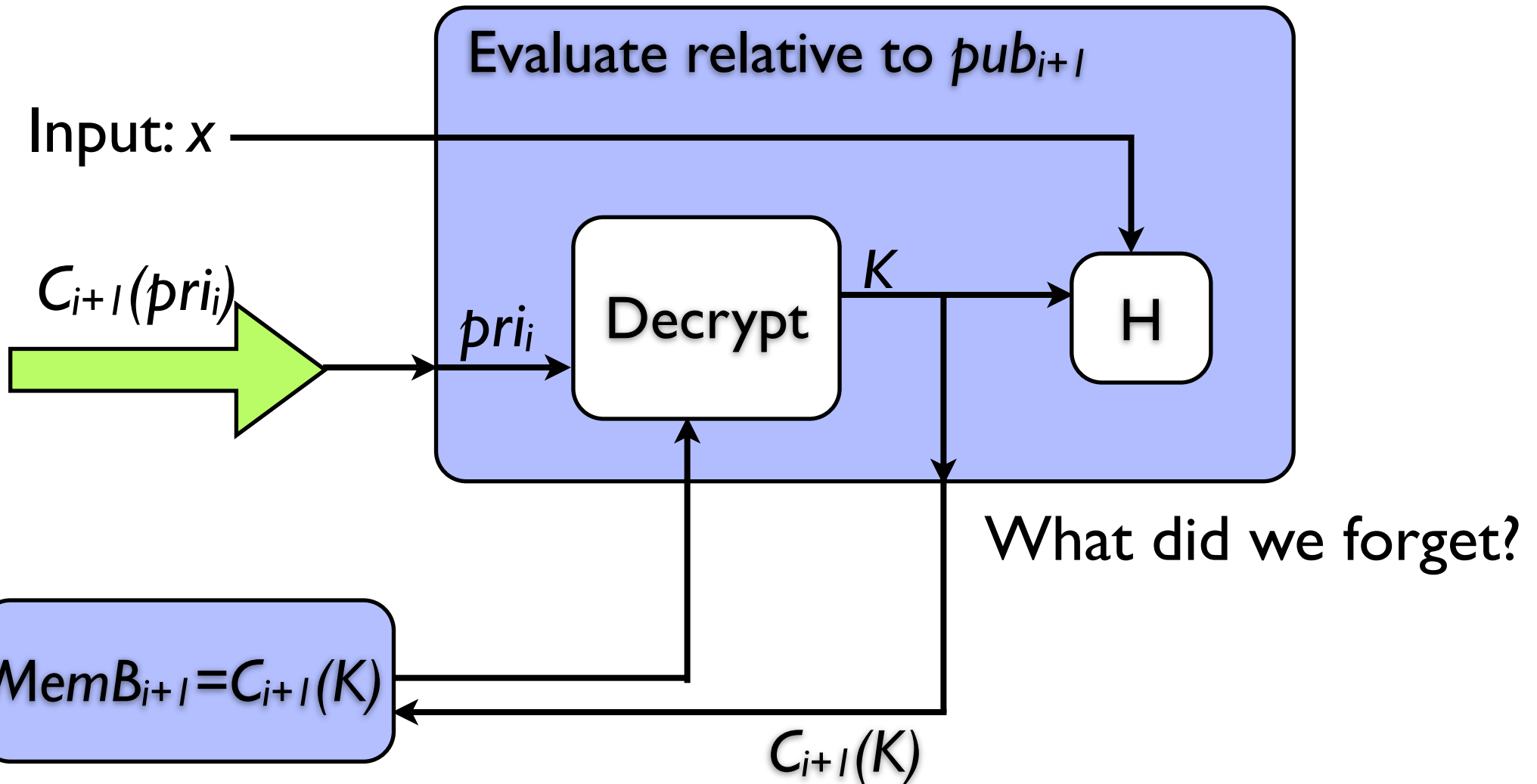
Second Attempt

Step 2': CPU B



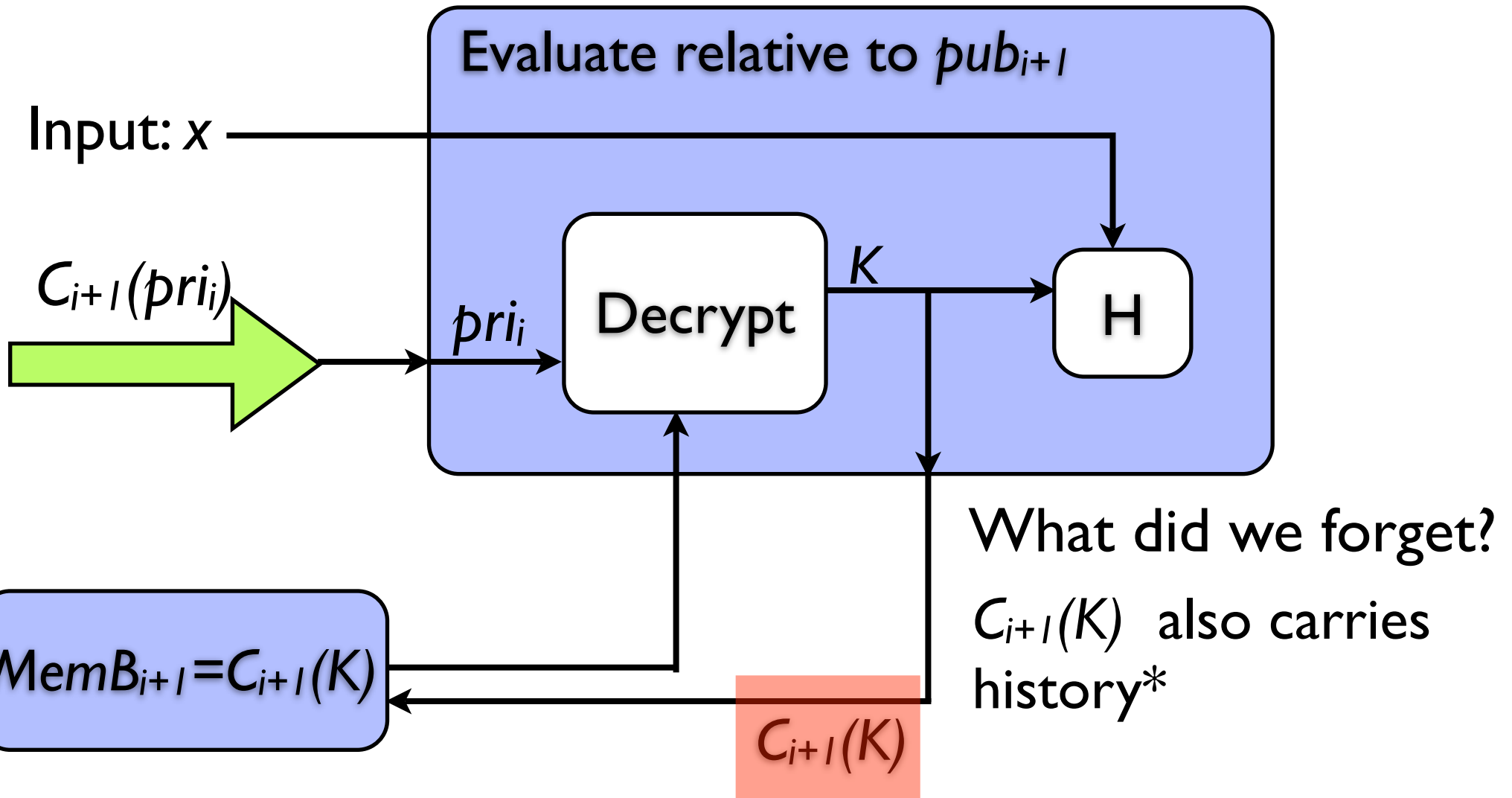
Second Attempt

Step 2': CPU B



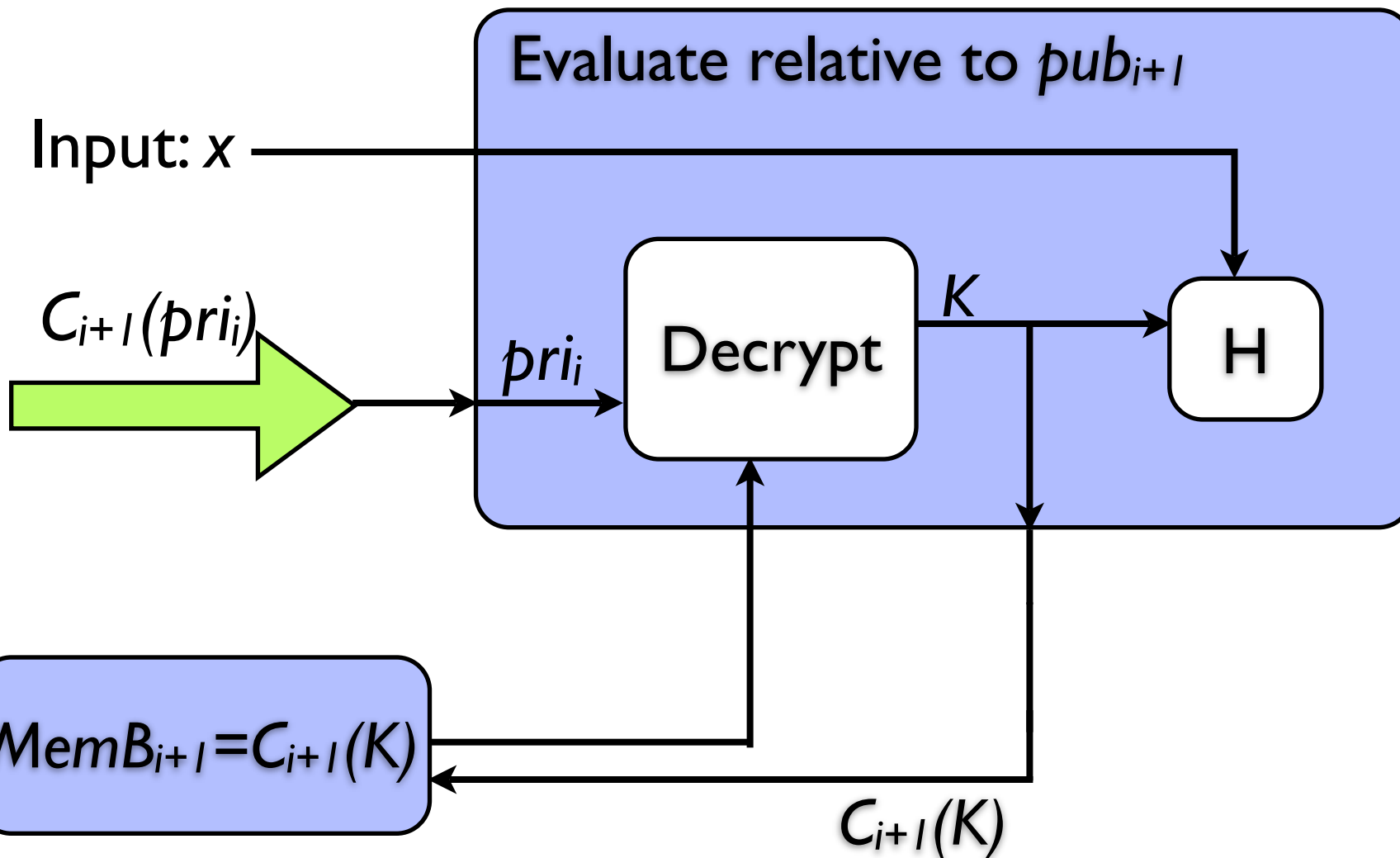
Second Attempt

Step 2': CPU B



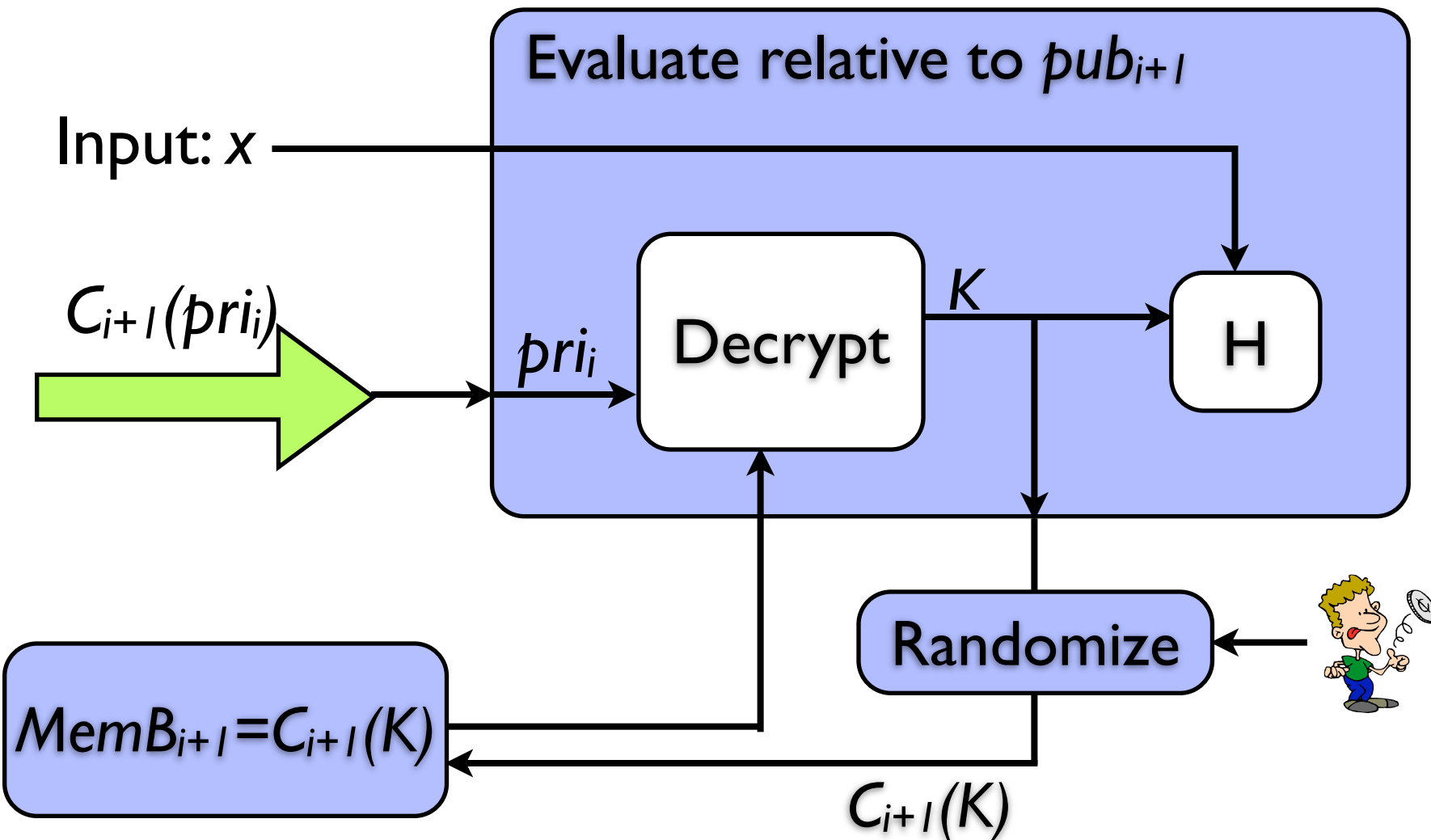
Third Attempt

Step 2'': CPU B



Third Attempt

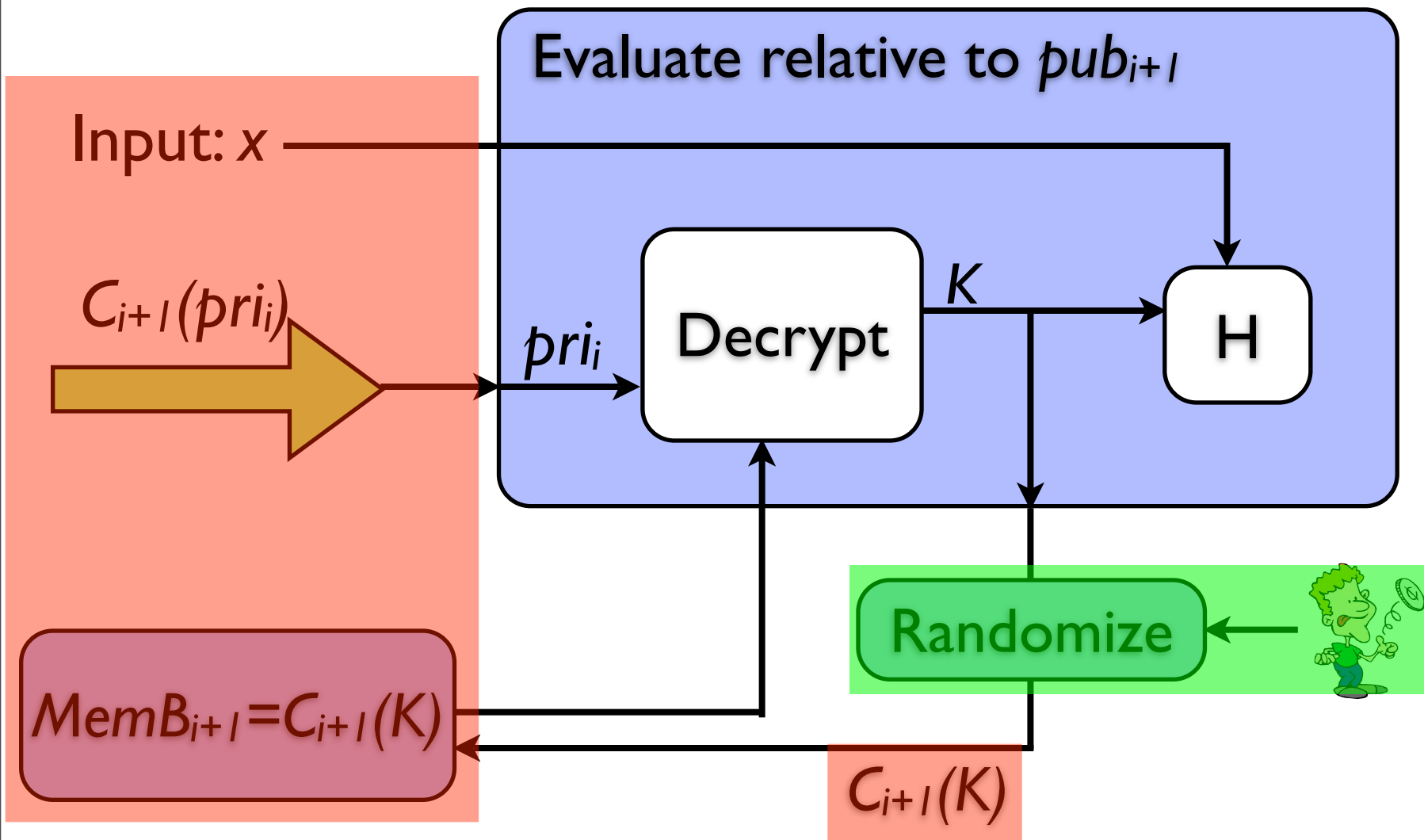
Step 2'': CPU B



Third Attempt



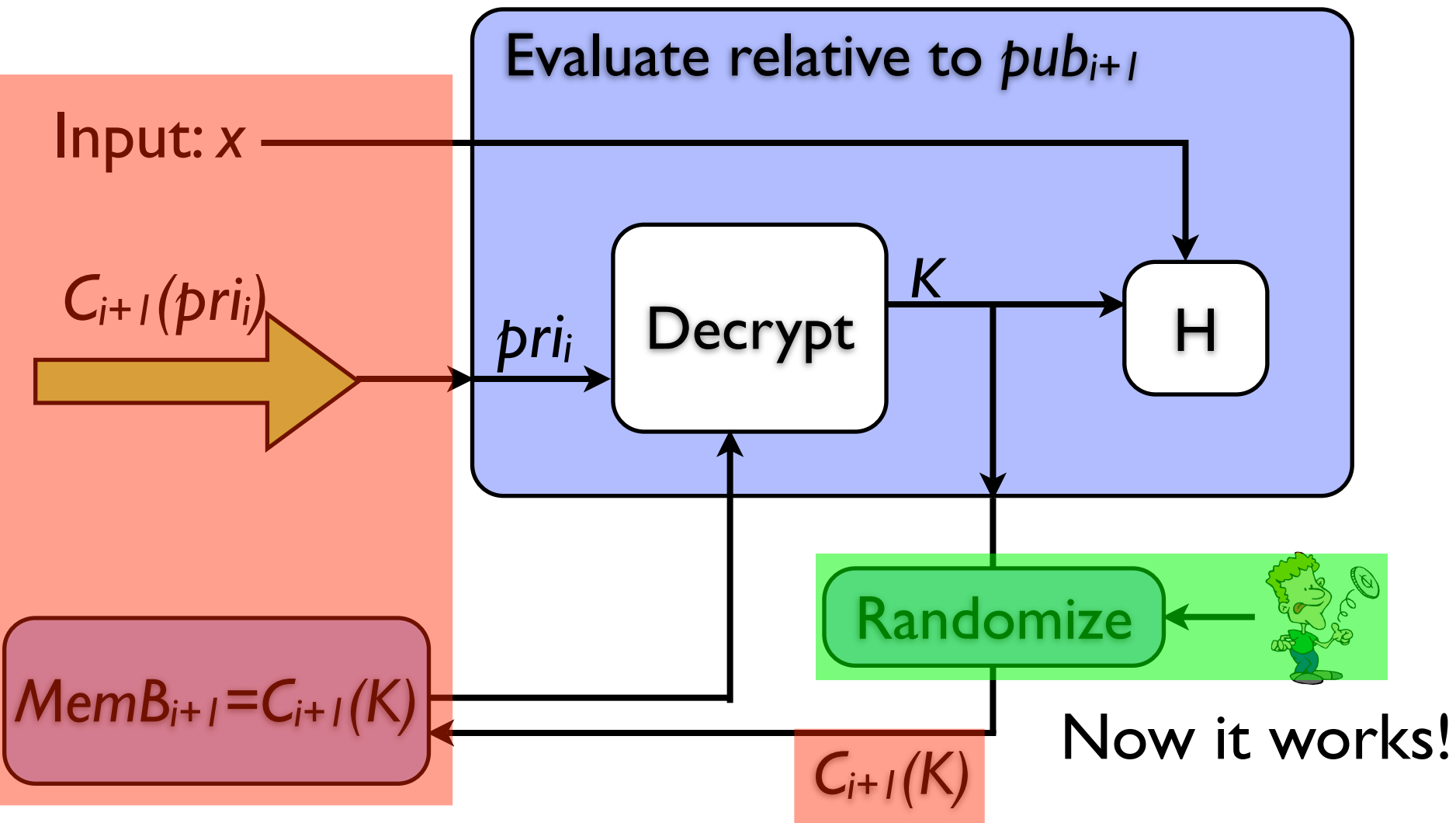
Step 2'': CPU B



Third Attempt



Step 2'': CPU B



Complete Construction

Memory A: pk_i
Randomness: r_{gen}

Memory B: C_K
Randomness: r, r'

$pk_{i+1}, sk_{i+1} = \text{KeyGen}(r_{gen})$

$C_{pri} = \text{Enc}(pk_i, pk_{i+1})$

Set Memory A = pk_{i+1}

C_{pri}

$C_{reply} = \text{Evaluate}(C_{pri}, C_K, x, H_K(x); r)$

$C'_K = \text{Evaluate}(C_{pri}, C_K, x, K; r')$

C'_{reply}

$C'_{reply} = \text{Randomize}(C_{reply}; r)$

set Memory B = $\text{Randomize}(C'_K; r')$

$Y = \text{Dec}(C'_{reply}; sk_{i+1})$

Output Y

Complete Construction

Memory A: pri_i
Randomness: r_{gen}

Memory B: C_K
Randomness: r, r'

$pub_{i+1}, pri_{i+1} = \text{KeyGen}(r_{gen})$
 $C_{pri} = \text{Enc}(pri_i, pub_{i+1})$
Set Memory A = pri_{i+1}

C_{pri}

$C_{reply} = \text{Evaluate}(C_{pri}, C_K, x, H_K(x); r)$
 $C'_K = \text{Evaluate}(C_{pri}, C_K, x, K; r')$

C'_{reply}

$Y = \text{Dec}(C'_{reply}; pri_{i+1})$
Output Y

$C'_{reply} = \text{Randomize}(C_{reply}; r)$
set Memory B = $\text{Randomize}(C'_K; r')$

Proof

Hybrid I

Memory A: pk_i
Randomness: r_{gen}

Memory B: C_K
Randomness: r, r'

$pk_{i+1}, sk_{i+1} = \text{KeyGen}(r_{gen})$

$C_{pri} = \text{Enc}(pk_i, pk_{i+1})$

Set Memory A = pk_{i+1}

C_{pri}

$C_{reply} = \text{Evaluate}(C_{pri}, C_K, x, H_K(x); r)$

$C'_K = \text{Evaluate}(C_{pri}, C_K, x, K; r')$

C'_{reply}

$Y = \text{Dec}(C'_{reply}; sk_{i+1})$

Output Y

$C'_{reply} = \text{Randomize}(C_{reply}; r)$

set Memory B = $\text{Randomize}(C'_K; r')$

Hybrid I

Memory A: pk_i
Randomness: r_{gen}

Memory B: C_K
Randomness: r, r'

$pk_{i+1}, sk_{i+1} = \text{KeyGen}(r_{gen})$

$C_{pri} = \text{Enc}(sk_i, pk_{i+1})$

Set Memory A = sk_{i+1}

C_{pri}

$C_{reply} = \text{Evaluate}(C_{pri}, C_K, x, H_K(x); r)$

$C'_K = \text{Evaluate}(C_{pri}, C_K, x, K; r')$

C''_{reply}

$C'_{reply} = \text{Randomize}(C_{reply}; r)$

set Memory B = $\text{Randomize}(C'_K; r')$

$Y = \text{Dec}(C'_{reply}; sk_{i+1})$

Output Y

$C''_{reply} = \text{Enc}(H_K(x), pk_{i+1})$

Hybrid I

Memory A: pk_i
Randomness: r_{gen}

Memory B: C_K
Randomness: r, r'

$pk_{i+1}, sk_{i+1} = \text{KeyGen}(r_{gen})$

$C_{pri} = \text{Enc}(pk_i, pk_{i+1})$

Set Memory A = pk_{i+1}

C_{pri}

$C_{reply} = \text{Evaluate}(C_{pri}, C_K, x, H_K(x); r)$

$C'_K = \text{Evaluate}(C_{pri}, C_K, x, K; r')$

C''_{reply}

$C'_{reply} = \text{Randomize}(C_{reply}; r)$

set Memory B = $\text{Randomize}(C'_K; r')$

$Y = \text{Dec}(C'_{reply}; sk_{i+1})$

Output Y

$C''_{reply} = \text{Enc}(H_K(x), pk_{i+1})$

Doesn't change the distribution

Hybrid 2

Memory A: pk_i
Randomness: r_{gen}

Memory B: C_K
Randomness: r, r'

$pk_{i+1}, sk_{i+1} = \text{KeyGen}(r_{gen})$

$C_{pri} = \text{Enc}(pk_i, pk_{i+1})$

Set Memory A = pk_{i+1}

C_{pri}

$C_{reply} = \text{Evaluate}(C_{pri}, C_K, x, H_K(x); r)$

$C'_K = \text{Evaluate}(C_{pri}, C_K, x, K; r')$

C''_{reply}

$C'_{reply} = \text{Randomize}(C_{reply}; r)$

set Memory B = $\text{Randomize}(C'_K; r')$

$Y = \text{Dec}(C'_{reply}; sk_{i+1})$

Output Y

$C''_{reply} = \text{Enc}(H_K(x), pk_{i+1})$

Hybrid 2

Memory A: pk_i
Randomness: r_{gen}

Memory B: C_K
Randomness: r, r'

$pk_{i+1}, sk_{i+1} = \text{KeyGen}(r_{gen})$

$C_{pri} = \text{Enc}(pk_i, pk_{i+1})$

Set Memory A = pk_{i+1}

C_{pri}

$C_{reply} = \text{Evaluate}(C_{pri}, C_K, x, H_K(x); r)$

$C'_K = \text{Evaluate}(C_{pri}, C_K, x, K; r')$

C''_{reply}

$C'_{reply} = \text{Randomize}(C_{reply}; r)$

set Memory B = C''_K

$Y = \text{Dec}(C'_{reply}; sk_{i+1})$

Output Y

$C''_{reply} = \text{Enc}(H_K(x), pk_{i+1})$

$C''_K = \text{Enc}(0\dots 0, pk_{i+1})$

Hybrid 2

Memory A: pk_i
Randomness: r_{gen}

Memory B: C_K
Randomness: r, r'

$pk_{i+1}, sk_{i+1} = \text{KeyGen}(r_{gen})$

$C_{pri} = \text{Enc}(sk_i, pk_{i+1})$

Set Memory A = pk_{i+1}

C_{pri}

$C_{reply} = \text{Evaluate}(C_{pri}, C_K, x, H_K(x); r)$

$C'_K = \text{Evaluate}(C_{pri}, C_K, x, K; r')$

C''_{reply}

$C'_{reply} = \text{Randomize}(C_{reply}; r)$

set Memory B = C''_K

$Y = \text{Dec}(C'_{reply}; sk_{i+1})$

Output Y

$C''_{reply} = \text{Enc}(H_K(x), pk_{i+1})$

$C''_K = \text{Enc}(0\dots 0, pk_{i+1})$

Changes the distribution completely

Why Should This Work?

- Very informally: Ciphertexts are incompressible.
- This means that leakage on B can help only if Adv knows enough about pri
- But Adv sees only leakage on pri which is insufficient to break semantic security

Open Questions

- Can we get rid of the leak-free component?
- Granularity of leakage.

Thank you!