

1. Problem 2.27 - Make a 3 variable K map for  $f_1$  and  $f_2$ ; minimize and implement using NAND only gates.
2. Problem 3.2 (d)
3. Problem 3.9 (d)
4. Problem 3.15 (d)
5. Problem 4.5
6. Problem 4.21
7. Problem 4.26
8. Problem 4.32