

# Is BQP Squeezed Out or In?

UB CSE Theory Group

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<sup>1</sup>Includes joint work with Amlan Chakrabarti, U. Calcutta, and Chaowen Guan, U. Cincinnati

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- ⑤ There is a tight deterministic time hierarchy...but the languages involved are diagonal or are “artificially complete.”

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- *Proved* when  $G$  is a finitely-generated subgroup of a connected Lie group. (J. Tits).

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- More-natural characterizations, or indelibly “Meta”?

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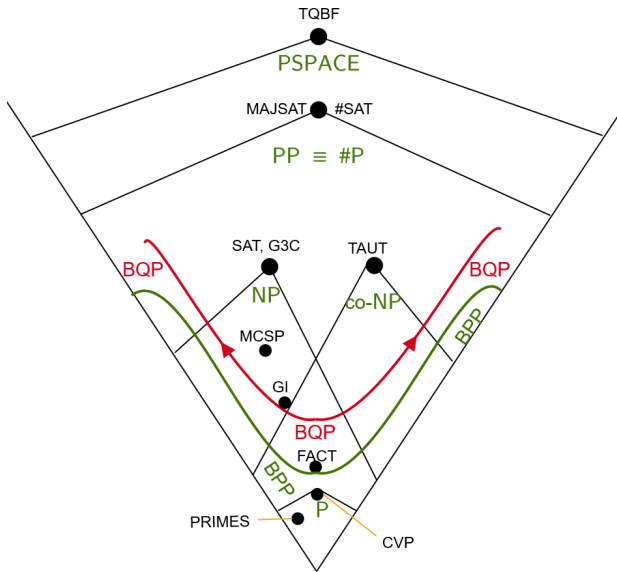
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- PP is the lowest known “simple” upper bound for **BQP**, bounded-error quantum polynomial time. (A technical subclass called **AWPP** contains BQP.)

# Diagram of These Classes and Problems



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- For many other counting problems, seemingly small changes in settings flip the problem between P and #P-hard, with no sign of anything in between.

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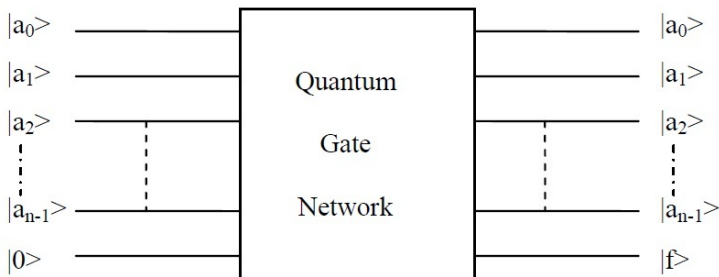
This brings up our main philosophical question:

If there is “nothing natural” between P and #P-complete, where does that leave BQP?

(For this purpose, NP is tantamount to #P.)

# Quantum Circuits

Quantum circuits look more constrained than Boolean circuits:



But Boolean circuits look similar if we do Savage's TM-to-circuit simulation and call each *column* for each tape cell a "cue-bit."

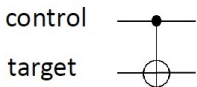
## Quantum Gates—three slides by M. Rötteler

## Quantum gates

single qubit operation:

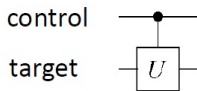


controlled-NOT:



$$\text{unitary matrix} = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{pmatrix}$$

controlled-U:

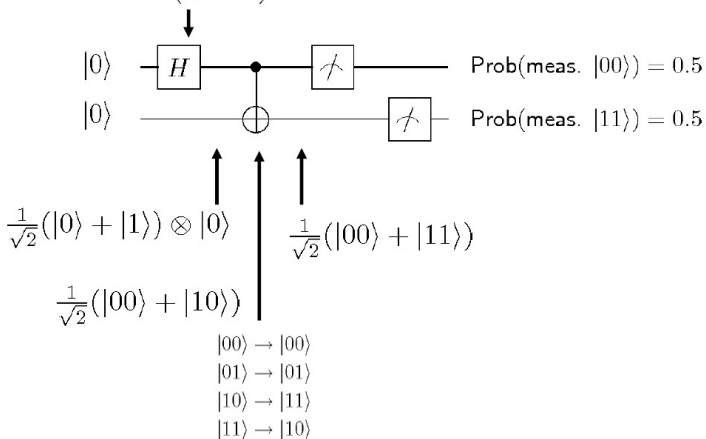


$$\text{unitary matrix} = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & U_{00} & U_{01} \\ 0 & 0 & U_{10} & U_{11} \end{pmatrix}$$

measurement in the  $|0\rangle, |1\rangle$  basis:

# Quantum circuit example

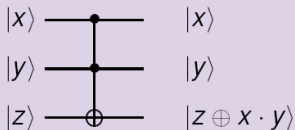
$$H \otimes \mathbf{1}_2 = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 1 \\ 1 & -1 \end{pmatrix} \otimes \mathbf{1}_2$$



# Toffoli Gate

## The Toffoli gate "TOF"

$x$	$y$	$z$	$x'$	$y'$	$z'$
0	0	0	0	0	0
0	0	1	0	0	1
0	1	0	0	1	0
0	1	1	0	1	1
1	0	0	1	0	0
1	0	1	1	0	1
1	1	0	1	1	1
1	1	1	1	1	0



## Theorem (Toffoli, 1981)

Any reversible computation can be realized by using TOF gates and ancilla (auxiliary) bits which are initialized to 0.

Slides by  
Martin  
Rötteler

## Some More Gates

$$X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}, \quad Y = \begin{bmatrix} 0 & -i \\ i & 0 \end{bmatrix}, \quad Z = \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix},$$

$$S = \begin{bmatrix} 1 & 0 \\ 0 & i \end{bmatrix}, \quad T = \begin{bmatrix} 1 & 0 \\ 0 & e^{i\pi/4} \end{bmatrix}, \quad R_8 = \begin{bmatrix} 1 & 0 \\ 0 & e^{i\pi/8} \end{bmatrix},$$

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- The gates H, X, Y, Z, S, CNOT, CZ generate *Clifford circuits*, which are simulable in polynomial time.

## Some More Gates

$$X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}, \quad Y = \begin{bmatrix} 0 & -i \\ i & 0 \end{bmatrix}, \quad Z = \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix},$$

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- The gates H, X, Y, Z, S, CNOT, CZ generate *Clifford circuits*, which are simulable in polynomial time.
- Adding any of T, R<sub>8</sub>, or CS gives the full power of BQP.

# Bounded-error Quantum Poly-Time

A language  $A$  belongs to BQP if there are uniform poly-size quantum circuits  $C_n$  with  $n$  data qubits, plus some number  $\alpha \geq 1$  of “ancilla qubits,” such that for all  $n$  and  $x \in \{0, 1\}^n$ ,

$$\begin{aligned} x \in A &\implies \Pr[C_n \text{ given } \langle x0^\alpha | \text{ measures 1 on line } n+1] > 2/3; \\ x \notin A &\implies \Pr[\dots] < 1/3. \end{aligned}$$

One can pretend  $\alpha = 0$  and/or measure line 1 instead. One can also represent the output as the “triple product”  $\langle b | C | a \rangle$ , with  $a = x0^\alpha$ ,  $b = 0^{n+\alpha}$ .

Two major theorems about BQP are:

- (a)  $C_n$  can be composed of just Hadamard and Toffoli gates [Y. Shi].
- (b) Factoring is in BQP [P. Shor].

## More-general forms of a known relation

- Assume all nonzero entries  $re^{i\theta}$  of gate matrices in quantum circuits  $C$  have equal magnitude  $|r|$  and  $\theta$  an integer multiple of  $2\pi/K$ .
- Suppose  $C$  has  $h$  Hadamard gates as nondeterministic gates.
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Theorem (multiplicative form, case  $G = \mathbb{F}_2$  is Dawson et al. (2004) + ...)

Any QC  $C$  of  $n$  qubits can be quickly transformed into a polynomial  $P_C$  of the form  $\prod_g P_g$  and a constant  $R > 0$  such that for all  $x, z \in \{0, 1\}^n$ :

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Here  $g$  ranges over all gates and outputs of  $C$  and  $y$  ranges over  $\{0, 1\}^h$ .

Degree is  $\Theta(s)$  where  $s$  is the number of gates in  $C$ . 

# Additive Case

Theorem (RCG (2017), RC (2007-9), cf. Bacon-van Dam-Russell (2008))

Given  $C$  and  $K$ , we can efficiently compute a polynomial  $Q_C(x_1, \dots, x_n, y_1, \dots, y_h, z_1, \dots, z_n, w_1, \dots, w_t)$  of *degree  $O(1)$*  over  $\mathbb{Z}_K$  and a constant  $R'$  such that for all  $x, z \in \{0, 1\}^n$ :

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- Hence poly-time simulation by solution counting in  $\mathbb{Z}_4$ .

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- The same idea with only CZ and (optionally, for self-loops) Z gates between the two banks of Hadamards are called **graph-state circuits**, and are equivalent to general Clifford circuits in power.

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- General observations—how wide are the possibilities and prospects?















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- Logic-based full QC simulator, 8,000+ lines of C++. [show demo]

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- Means that the  $n^2$ -vs.- $n^\omega$  weak/strong simulation gap cannot be closed unless matrix rank is in  $O(n^2)$  time over  $\mathbb{F}_2$ .

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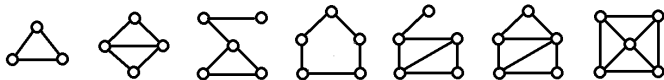
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- Also yields an apparently new class of undirected graphs:





# Boolean Logic Simulation

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- Our C++ simulator outputs DIMACS-compliant files for these solvers. SAT solvers have seen great success in many areas, but maybe not QC...
- Second main purpose of simulator [show] is to enable tinkering with approximative methods.

# Higher Algebra and Applications

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- Singular points of varieties determine (most of?) amplitude under the Principle of Least Action, conjectured by Bacon, van Dam, and Russell [2008 unpublished]

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- Involvement in the general debate over *Quantum Advantage*.