SHAMBHU J. UPADHYAYA

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EDUCATION

PhD	1987	University of Newcastle, Australia (Electrical and Computer Engineering)
ME	1982	Indian Institute of Science, Bangalore, India (Electrical Engineering)
BE	1979	Indian Institute of Science, Bangalore, India (Electrical Engineering)

RESEARCH INTERESTS

Cybersecurity (intrusion detection, alert correlation, insider threat modeling, authentication, simulation, advanced persistent threats); Wireless Networks Security; Web Security; Fault Tolerance and Dependability in Distributed Systems.

EMPLOYMENT HISTORY

2023 – present	Director, SEAS/SOM Cybersecurity MS Program, School of Engineering and Applied Sciences (SEAS), University at Buffalo.
2022 – 2023	Strategic Lead, Cybersecurity Research and Education, School of Engineering and Applied Sciences (SEAS), University at Buffalo.
2017 – 2022	Associate Dean for Research and Graduate Education, School of Engineering and Applied Sciences (SEAS), University at Buffalo.
2015 – 2017	Associate Department Chair, Department of Computer Science and Engineering, University at Buffalo.
2008 – present	Professor, Department of Computer Science and Engineering, University at Buffalo.
2002 – present	Director, Center of Excellence in Information Systems Assurance Research and Education (CEISARE) certified by National Security Agency and Department of Homeland Security http://www.cse.buffalo.edu/caeiae/ >.
1998 – 2007	Associate Professor, Department of Computer Science and Engineering, University at Buffalo.
2001 - 2002	National Research Council Faculty Fellow, AFRL (5/01 – 7/01, 5/02 – 7/02).
2000 - 2002	Consultant, IBM Corporation, Endicott, NY.
1999	Senior Faculty Fellow, Naval Research Laboratory, Washington DC (5/99 – 7/99).
1998 – 1999	Research Associate, AFRL, Rome, NY (7/98 – 9/98, 7/99 – 9/99).
1998	Visiting Faculty, Intel Corporation, Folsom, CA (5/98 – 7/98).

1993 – 1998	Associate Professor, Department of Electrical and Computer Engineering, University at Buffalo; Director of Undergraduate Studies; Adjunct Associate Professor of Computer Science.
1995	Visiting Research Associate Professor, Department of Electrical and Computer Engineering, Univ. of Illinois, Urbana-Champaign, IL $(1/95-7/95)$.
1986 – 1993	Assistant Professor, Department of Electrical and Computer Engineering, University at Buffalo.
1990	Consultant, EG &G Idaho, Inc., Idaho Falls, ID (5/90 – 7/90).
1986	Visiting Lecturer, Department of Electrical and Computer Engineering, University of Iowa, Iowa City, IA $(1/86 - 8/86)$.

HONORS/AWARDS

- Research Initiation Award, NSF, 1989.
- Lilly Endowment Teaching Fellow, 1989.
- Certificate of Recognition of Service to IEEE Transactions on Reliability, 1997.
- IBM Faculty Partner Fellow in recognition of contribution to Fault Tolerance and VLSI Testing, 2000-01.
- Elected Senior Member of IEEE, 2001.
- National Research Council Faculty Fellow, 2001-02.
- Best Paper Award at 2nd IEEE International Swarm Intelligence & Other Forms of Malware Workshop (Malware'07), New Orleans, LA, held in conjunction with IPCCC 2007, April 2007 (with M. Chandrasekaran and S. Vidyaraman). Award sponsored by RSA, The Security Division of EMC.
- Best Paper Award at 11th Annual New York State Cyber Security Conference, Albany, NY, June 2008 (with B. Gilbert, R. Sharman, M. Gupta, H.R. Rao and K. Mortensen).
- Invited to participate in the Cyber Security Research Initiative sponsored by leading industries such as Intel, IBM, Lockheed Martin and Honeywell at Washington DC, April 2012. The purpose was to set the agenda for industry research in cyber security.
- Tan Chin Tuan (TCT) Exchange Fellow, Singapore, 2013.
- Sustained Achievement Award UB Exceptional Scholars, 2013.
- Best Paper Award at 6th International Conference on Secure Knowledge Management in the Big Data Era, Dubai, UAE, December 2014 (with R. Mehresh).
- SEAS Senior Teacher of the Year Award, 2015.
- Best Poster Award at 8th IEEE International Conference on Biometrics: Theory, Applications, and Systems (BTAS), Niagara Falls, NY, September 2016 (with H. Ceker).
- Best Paper Award at 13th International Conference on Design Science Research in Information Systems and Technology (DESRIST), Chennai, India, June 2018 (with R. Valecha, R. Chakraborty and H.R. Rao).
- IEEE Region 1 Technological Innovation in Academic Award, 2018.
- SUNY Chancellor's Award for Scholarship and Creative Activities, 2019.
- Elevated to IEEE Fellow, 2021.
- UB's Excellence in Graduate Student Mentoring Award, 2021-22.

- Invited to serve as a member of the IEEE CS Fellows Evaluation Committee, 2022.
- Invited to participate in the visioning event to explore Engineering R&D Solutions for Unhackable Infrastructure organized by NSF-supported Engineering Research Visioning Alliance (ERVA), MIT, Cambridge, MA, August 2022.
- Most Effective Mentor Award, National Center of Academic Excellence in Cybersecurity Research Designation, Charleston, SC, April 2025.

PROFESSIONAL ASSOCIATIONS

Fellow, IEEE.

Member, IEEE Computer Society.

Member, IEEE Test Technology Technical Council (TTTC).

Member, IEEE Computer Society's Technical Committee on Fault Tolerant Computing.

PROFESSIONAL ACTIVITIES

EDITORIAL SERVICE

- International Journal of Reliability, Quality, and Safety Engineering, World Scientific Publishing Company, Member of the Editorial Board, 1997 present.
- *IEEE Transactions on Computers*, Associate Editor, 2001 2006.
- IEEE Transactions on Computers, Special Issue co-Guest Editor, Feb. 2003.
- *IEEE Transactions on Systems, Man and Cybernetics, Part A*, Special Issue co-Guest Editor, May 2006.
- Information Systems Frontier, Guest Executive Editor, 2007, Coordinating Editor, 2010 .
- *ICST Transactions on Security and Safety*, Founding Member of the Editorial Board, April 2009 December 2011.
- Information Systems Frontier, Special Issue co-Guest Editor, June 2014.
- *IEEE Transactions on Emerging Topics in Computing*, Special Issue co-Guest Editor, July 2016.
- Cybersecurity & Privacy of Frontiers in Big Data, Associate Editor, April 2018 -

ADVISORY BOARD/COMMITTEE MEMBERSHIP

- NSF/NIST Committee on Cyber Security Workforce Needs Assessment and Educational Innovation, Washington DC, August 2003.
- Application Communities Committee to assess research direction at DARPA, Washington DC, October 2004.
- Science Advisory Board, Griffiss Institute for Information Assurance, 2004 2006.
- Technical Working Group for Education and Training in Digital Evidence (TWGETDE), National Institute of Justice, March 2005 January 2006.
- Advisory Panel, eWEEK, 2005 2007.
- 12th Annual Symposium on Information Assurance and Secure Knowledge Management, Albany, NY, June 2012.
- Information Assurance and Homeland Security Academy, Mahindra Special Services Group (MSSG), Mumbai, India, 2012 2014.

CONFERENCE SERVICE

General Chair

- Symposium Chair, 28th IEEE Symposium on Reliable Distributed Systems, Niagara Falls, NY, September 2009.
- General Chair, 2nd International Symposium on Data, Privacy and E-Commerce, Buffalo/Niagara Falls, NY, September 2010.
- General Co-Chair, International Conference on Security in Computer Networks and Distributed Systems (SNDS 2012), Thiruvananthapuram, Kerala, India, October 2012.

Technical Program Chair

- Program Co-Chair, 5th IEEE/ACM Great Lakes Symposium on VLSI, Buffalo, NY, March 1995.
- Program Co-Chair, 19th IEEE Symposium on Reliable Distributed Systems, Nuernberg, Germany, October 2000.
- Program Chair, 1st New York State Cyber Security Symposium, Utica, NY, Feb. 2003.
- Program Vice-Chair, 12th IEEE North Atlantic Test Workshop, Montauk, NY, May 2003.
- Program Co-Chair, International Conference and Indo-US Bilateral Workshop on Cyber Security, Cybercrime and Cyber Forensics, August 19-21, 2009, Cochin, India.
- Program Co-Chair, 27th IEEE International Conference on Computer Communications and Networks (ICCCN 2018), August 2018, Hangzhou, China.

Publicity Chair

- IEEE Computer Performance and Dependability Symposium (IPDS'98), Durham, NC, 1998.
- IEEE North Atlantic Test Workshop, Gloucester, MA, 2001, 2002.

Conference and Workshop Organizer

- Organizing Committee Member, American Association of Artificial Intelligence (AAAI) Workshop on Reasoning about Function, Washington D.C., 1993.
- Organizing Committee Member, 1st Griffiss Institute Academic-Industry Symposium on Cyber Security, New Paltz, NY, November 2003.
- Organizing Committee Member, Secure Knowledge Management, Buffalo, NY, September 23-24, 2004.
- Organizing Committee Member, Joint Workshop on Cyber Security 2006, Buffalo, NY, March 31, 2006.
- Steering Committee Chair, 2nd Workshop on Secure Knowledge Management, Brooklyn, NY, September 21-22, 2006.
- Steering Committee Chair, 3rd Workshop on Secure Knowledge Mgmt., Dallas, TX, 2008.
- Steering Committee Chair, 4th Workshop on Secure Knowledge Management, Rutgers University, NJ, 2010.
- Steering Committee Member, 2nd International Workshop on Security in Cloud Computing (SCC'2010), San Diego, CA, 2010.
- Steering Committee Member, 2nd International Conference on Advances in Computing, Communications and Informatics (ICACCI-2012), Mysore, India, Aug. 2013.
- Steering Committee Member, 6th Conference on Secure Knowledge Management, Dubai, UAE, 2014.

- Steering Committee Member, 2nd Int. Conference on Security in Computer Networks and Distributed Systems (SNDS 2014), Thiruvananthapuram, Kerala, India, March 2014.
- Steering Committee Member, IEEE Symposium on Reliable Distributed Systems, Newport Beach, CA, 2012, Braga, Portugal, 2013, Nara, Japan, 2014, Montreal, Canada, 2015, Budapest, Hungary 2016, Hong Kong, China, 2017, Salvador, Brazil, 2018, Lyon, France, 2019.
- Steering Committee Member, 8th Conference on Secure Knowledge Management in the Artificial Intelligence Era, Goa, India, 2019.
- Organizing Committee Member, International Program on Information Assurance and Management, Buffalo, NY, 2012, 2013, 2014, 2015, 2016, 2017, 2018, 2019.
- Organizing Committee Member, GenCyber Camp for Middle School and High School Students, Buffalo, NY, Summer 2015, Summer 2016, Summer 2018, Summer 2019, Summer 2021, Summer 2022, Summer 2024.
- Organizing Committee Member, Cyber Security Camp for Middle School and High School Students, Buffalo, NY, Summer 2017, Summer 2023.

Panel Organizer

- "Fault Tolerance and Security in Distributed Systems", 19th IEEE Symposium on Reliable Distributed Systems, Neurnberg, Germany, 2000.
- "Key Security Issues for Financial Institutions and What can be done to Mitigate Them?", 4th International Program on Information Assurance and Management, Niagara Falls, NY, Sept. 2015, 5th International Program on Information Assurance and Management, Niagara Falls, NY, Aug. 2016.

Keynote Speaker

- 2nd Annual Symposium on Information Assurance, Albany, NY, June 2007 (jointly with 10th Annual NYS Cyber Security Conference).
- DIMACS/DyDAn Workshop on Mathematical & Computational Methods for Information Security, Texas Southern University, Houston, Texas, December 7, 2007.
- International Symposium on Data, Information & Knowledge Spectrum (ISDIKS 2007), Dec 13 15, 2007, Amrita University, Kerala, India.
- 3rd ACM International Workshop on Security and Privacy Analytics (Co-located with ACM CODASPY 2017), March 24, 2017, Scottsdale, AZ.
- 5th International Conference on Machine Learning, IoT and Big Data (ICMIB)-2025, April 5, 2025, Berhampur, India (remote).

Panelist

- IEEE Symposium on Defect and Fault Tolerance in VLSI Systems, Albuquerque, NM, November 1999.
- 2nd International Workshop on Mathematical Methods, Models and Architectures for Computer Networks Security, St. Petersburg, Russia, September 2003.
- 20th Annual Computer Security Applications Conference (ACSAC), Tucson, AZ, December 2004.
- 6th Annual Western New York Technology Forum Featuring Homeland Security and Bioinformatics, Niagara Falls, NY, October 2006.

- 4th International Workshop on Dependable Network Computing and Mobile Systems (DNCMS), Madrid, Spain, October 2011.
- 1st Workshop on Computer Security Research Institute, Washington DC, April 2012.
- Thought Leaders Panel Discussion on Cybersecurity, hosted by Hodgson Russ LLP, Buffalo NY, July 2022.
- Cybersecurity in Manufacturing at the New York State Innovation Summit, Buffalo, NY, October 2022.

Tutorial/Short Course Presentation

- 3-hour tutorial on "Real-time Intrusion Detection" (with K. Kwiat), IEEE MILCOM 2002.
- Embedded tutorial on "Recent Advances in Internet Security", International Symposium on Data, Information & Knowledge Spectrum, Dec 13-15, 2007, Amrita University, Kerala, India.
- Short courses on "Intrusion Detection" and "Wireless Networks Security", Amrita University, Kerala, India, December 15-16, 2007.
- 3-day course on "Cyber Security", January 17-19, 2012, C.R. Rao Advanced Institute of Mathematics, Statistics and Computer Science (AIMSCS), Hyderabad, India.
- 4-hour Coursera course on "Cyber Security in Manufacturing", June 12, 2017 (total learners: 21,478 and counting).

Program Committee Member

- IASTED Int. Conference on Reliability, Quality Control and Risk Assessment, Nov. 1992.
- 2nd International Conference on Reliability, Maintainability and Safety (ICRMS '94), Beijing, China, June 1994.
- ISSAT International Conference on Reliability and Quality in Design, (1st) Seattle, WA, 1994, (3rd) Anaheim, CA, 1997, (4th) Seattle, WA, 1998.
- IEEE/ACM Annual Simulation Symposium, (27th) La Jolla, CA, April 1994; (28th) Phoenix, AZ, April 1995; (30th) Atlanta, GA, April 1997; (33rd) Washington DC, April 2000; (34th) Seattle, WA, April 2001; (35th) San Diego, CA, April 2002; (36th) Orlando, FL, April 2003; (37th) Washington DC, April 2004; (38th) San Diego, CA, April 2005; (39th) Huntsville, AL, April 2006; (40th) Norfolk, VA, March 2007, (41st) Ottawa, Canada, April 2008.
- IEEE International Symposium on Defect and Fault Tolerance in VLSI Systems, (12th) Paris, France, 1997; (13th) Austin, TX, 1998; (14th) Albuquerque, NM, 1999; (15th) Mt. Fuji, Japan, 2000; (16th) San Francisco, CA, October 2001; (17th) Vancouver, Canada, October 2002; (18th) Boston, MA October 2003; (19th) Cannes, France, October 2004; (21st) Arlington, VA, 2006.
- 27th IEEE International Symposium on Fault Tolerant Computing, Seattle, WA, June 1997; 29th Madison, WI, June 1999.
- IEEE North Atlantic Test Workshop (NATW), (9th) Gloucester, MA, May 2000; (13th) Essex Junction, VT, May 2004; (16th) Boxborough, MA, May 2007; (17th)
- 12th Great Lakes Symposium on VLSI, New York City, April 2002.
- 3rd IEEE International Information Assurance Workshop, Washington DC, March 2005.
- Dependable Computing and Communications Symposium of IEEE DSN-2005.

- 6th and 8th Annual IEEE Information Assurance Workshop, West Point, NY, June 2005; June 2007.
- International Workshop on Mathematical Methods, Models and Architectures for Computer Networks Security (MMM-ACNS-2005), St. Petersburg, Russia, (3rd in Sept. 2005, 4th in Sept. 2007, 5th in Sept. 2010), Warsaw, Poland (7th in August 2017).
- Annual Symposium on Information Assurance, in conjunction with 9th Annual New York State Cyber Security Conference, June 2006; in conjunction with 10th Annual Conference, June 2007, in conjunction with 12th Annual Conference, June 2009, in conjunction with 13th Annual Conference, June 2010.
- IEEE Symposium on Reliable Distributed Systems (SRDS), (17th), West Lafayette, IN, 1998; (18th) Lausanne, Switzerland (1999); (19th) Nuernberg, Germany (2000); (20th) New Orleans, LA, 2001; (25th) Leeds, UK, 2006; (26th) Beijing, China, Oct. 2007; (27th) Napoli, Italy, Oct. 2008, (29th) New Delhi, India, Nov. 2010; (31st) Newport Beach, CA, Oct. 2012; (43rd) Charlotte, NC, Oct. 2024.
- 2nd International Swarm Intelligence & Other Forms of Malware Workshop (Malware 2007), New Orleans, LA, April 2007, (3rd) Fairfax, VA, October 2008, (4th) Montreal, Quebec, October 2009.
- The 2007 IEEE International Symposium on Ubisafe Computing (UbiSafe-07), Niagara Falls, Ontario, Canada, May 2007; UbiSafe-09, Chengdu, China, December 2009.
- IEEE First International Symposium on Data, Privacy, & E-Commerce (ISDPE 2007), Chengdu, China, November 2007.
- Second Workshop on Intelligent Networks: Adaptation, Communication & Reconfiguration (IAMCOM 2008) held in conjunction with 3rd International Conference on COMmunication System softWAre and MiddlewaRE (COMSWARE 2008), Bangalore, India, Jan. 2008.
- International Conference on Emerging Security Information, Systems and Technologies (SECURWARE), (1st) Valencia, Spain, 2007, (2nd) Cap Esterel, France, Aug. 2008, (3rd) Athens, Greece, June 2009, (4th) Venice, Italy, July 2010, (5th) French Riviera, Nice/Saint Laurent du Var, France, August 2011, (6th) Rome, Italy, August 2012.
- 3rd International Conference on Information Security and Assurance (ISA-09), Korea University, Seoul, Korea, June 2009.
- Euromicro International Conference on Parallel, Distributed and Network-based Processing (PDP 2009) Special Session on "Security in Networked and Distributed Systems", Weimar, Germany, February 2009; PDP 2010, Pisa, Italy, February 2010; Ayia Napa, Cyprus, February 2011; Garching, Germany, February 2012; Turku, Finland, March 2015.
- IEEE Symposium on Computational Intelligence in Cyber Security (CICS 2009), Nashville, TN, March 2009.
- The 9th International Symposium on Autonomous Decentralized Systems (ISADS 2009) Athens, Greece, March 2009.
- International Workshop on Managing Insider Security Threats (MIST 2009), West Lafayette, IN, June 2009 (1st), Morioka, Iwate, Japan, June 2010 (2nd), Fukuoka, Japan, Nov. 2012 (4th), Pukyong National University, Busan, Korea, Oct. 2013 (5th), Republic of Korea, Nov. 2014 (6th), Denver, CO, Oct. 2015 (7th), Vienna, Austria, Oct. 2016 (8th).
- International Conference on Information Security and Privacy, sponsored by International Society for Research in Science and Technology (ISRST), Orlando, Florida, July 2009.

- 25th IEEE International Conference on Advanced Information Networking and Applications (AINA), Distributed and Parallel Systems Track, Biopolis, Singapore, March 2011.
- 4th Secure Knowledge Management Workshop, Rutgers University, October 2010.
- IEEE MILCOM 2010, San Jose, CA, October 2010.
- The Colloquium for Information Systems Security Education (E), (13th) Seattle, WA; (14th) Baltimore, MD, June 2010, (15th) Fairborn, OH, June 2011.
- The 8th International Conference on Mobile Web Information Systems (MobiWIS), Niagara Falls, Ontario, Canada, Sept. 2011; (9th) Niagara Falls, Ontario, Canada, Aug. 2012.
- 30th International Performance Computing and Communications Conference (IPCCC 2011), Orlando, Florida, November 2011.
- 7th International Conference on Information Warfare (ICIW 2012), Seattle, WA, March 2012.
- 1st International Conference on Cloud Security Management (ICCSM-2013), Seattle, WA, October 2013.
- 2nd International Conference on Security in Computer Networks and Distributed Systems (SNDS 2014), Thiruvananthapuram, Kerala, India, March 2014.
- 10th International Conference on Cyber Warfare and Security ICCWS-2015, Kruger National Park, South Africa, March 2015.
- 24th International Conference on Computer Communications and Networks (ICCCN), Las Vegas, NV, Aug. 2015.
- 8th ACM Conference on Data and Application Security and Privacy (CODASPY), Tempe, AZ, March 2018.
- NSA CAE-R Research Symposium, Atlanta, GA, June 2022.
- International Conference on Information Systems Security and Privacy (ICISSP), (7th) Online Streaming, February 2021, (8th) Online Streaming, February 2022; (9th) Lisbon, Portugal, February 2023; (10th) Rome, Italy, February 2024; (11th) Porto, Portugal, February 2025.

Session Chair

- 5th ISMM International Conference on Parallel and Distributed Computing and Systems, Pittsburgh, PA, October 1992.
- IASTED International Conference on Reliability, Quality Control and Risk Assessment, November 1992, Boston, MA, October 1993.
- 27th IEEE/ACM Annual Simulation Symposium, La Jolla, CA, April 1994.
- 3rd ISSAT Int. Conf. on Reliability and Quality in Design, Anaheim, CA, March 1997.
- IEEE Computer Performance and Dependability Symposium (IPDS'98), Durham, NC, 1998.
- 1st New York State Cyber Security Symposium, Utica, New York, Feb. 2003.
- 2nd International Workshop on Mathematical Methods, Models and Architectures for Computer Networks Security, St. Petersburg, Russia, September 2003.
- 13th IEEE North Atlantic Test Workshop, Essex Junction, VT, May 2004.
- 5th Annual IEEE Information Assurance Workshop, West Point, NY, June 2004.
- Dependable Computing and Communications Symposium of IEEE DSN-2005, Yokohama, Japan, June 2005.
- IEEE International Conference on Communications, Istanbul, Turkey, June 2006.
- 4th Annual IFIP WG 11.9 International Conference on Digital Forensics, Kyoto, Japan, January 2008.

- 27th IEEE Symposium on Reliable Distributed Systems (SRDS 2008), Naples, Italy, October 2008.
- 3rd Secure Knowledge Management (SKM 2008) Workshop, Dallas, TX, November 2008.
- 15th Annual Colloquium for Information Systems Security Education, Fairborn, OH, June 2011.
- Security Applications and Management Conference (SAM), Las Vegas, NV, July 2011.
- 16th Annual Colloquium for Information Systems Security Education, Orlando, FL June 2012.
- 32nd IEEE Symposium on Reliable Distributed Systems, Braga, Portugal, October 2013.
- 6th International Conference on Information-Warfare & Security, Washington DC, March 2011, 10th at Kruger National Park, South Africa, March 2015.
- IEEE MILCOM 2015, Tampa, FL, October 2015.
- IEEE International Conference on Identity, Security and Behavioral Analysis (ISBA), New Delhi, India, February 2017.
- *IEEE* 27th International Conference on Computer Communications and Networks (ICCCN), Hangzhou, China, July-Aug. 2018.
- 3rd Great Lakes Security Day, Buffalo, NY, September 2019.
- 9th Secure Knowledge Management (SKM 2021) Workshop, San Antonio, TX (virtual), October 2021.
- CAE in Cybersecurity Community Symposium, Atlanta, GA, June 2022.
- 10th Secure Knowledge Management (SKM 2023) Conference, Tempe, AZ (virtual), September 2023.

REFEREEING/EVALUATIONS

Academic Programs

- CIS Program Review at Genesee Community College, NY, 2004.
- External Reviewer, Higher Diploma in Information Security Engineering Technology Program (HDISET), Abu Dhabi Polytechnic, UAE, 2012.
- Panel Chair, Master of Science degree in Information Assurance in Ontario, Canada, offered by Northeastern University, February 2015.
- External Review Team Member, Master of Science degree in Information Security, United Arab Emirates University, Al Ain, UAE, 2017.
- External Review Team Member, Initial Accreditation, BS in Cyber Security, Al Ain University, Al Ain, UAE, 2018.
- External Review Team Member, Initial Accreditation, BS in Cyber Forensics, University of Fujairah, Fujairah, UAE, 2018.
- External Review Team Member, Renewal of Accreditation of the MSc in Computer Engineering, American University of Sharjah (AUS), UAE, Nov. 2018.
- External Review Team Member, Initial Accreditation of BSc program in Cybersecurity Engineering, Abu Dhabi University (ADU), UAE, Nov. 2018.
- External Review Team Member, Renewal of Accreditation of the Applied Bachelor & Higher Diploma in Information Security Engineering Technology (with three Concentrations:

Software Security, Systems/Servers Security Administration, Network and Cypher Security), Abu Dhabi Polytechnic (ADPoly), UAE, Feb. 2019.

- External Review Team Member, Renewal of Accreditation, BS in Computer Engineering, Al Ain University, Al Ain, UAE, September 2019.
- External Review Team Member, Initial Accreditation, BS in Computing Security, Rochester Institute of Technology Dubai, UAE, February 2020.
- External Review Team Member, Initial Accreditation, BS in Cybersecurity, Canadian University of Dubai, UAE, June 2020 (remote).
- External Review Team Member, Initial Accreditation, BS in Information Technology, University of Science and Technology of Fujairah, Fujairah, UAE, August 2020 (remote).
- External Review Team Member, Initial Accreditation, BS in Cybersecurity Engineering, University of Sharjah, UAE, July 2021 (remote)
- External Review Team Member, Renewal of Accreditation, MS in Computer Engineering, University of Sharjah, UAE, November 2021 (remote).
- External Review Team Member, Initial Program Accreditation, BS in Computer Engineering, University of Dubai, UAE, February 2022.
- External Review Team Member, Renewal Accreditation, BS in Computer Science, American University of the Emirates, Dubai, UAE, February 2023.
- External Review Team Member, Renewal Accreditation, Bachelor of Information Technology, University of Fujairah, Fujairah, UAE, March 2023.
- External Review Team Member, Renewal Accreditation, Bachelor of Cybersecurity Engineering, American University of the Emirates, Dubai, UAE, April 2024.
- External Review Team Member, Renewal Accreditation, Applied Bachelor in Information Security Engineering Technology, Abu Dhabi Polytechnic (ADPoly), UAE, Nov. 2024.
- Judge for UB's School of Management PhD Showcase, Feb. 2025.

Proposals

- National Science Foundation, Panel, ad hoc and mail-in reviewer.
- U.S. Civilian Research and Development Foundation for Qatar's National Priorities Research Program (NPRP).
- American Association for the Advancement of Science (AAAS) for King Abdulaziz City for Science and Technology (KACST) at Saudi Arabia.
- Interdisciplinary Research and Creative Activities Funds Program (UB).
- Center for Connected Multimodal Mobility (C2M2), Clemson University.
- National Defense Science and Engineering Graduate (NDSEG) Fellowship Program.
- Research Funding Department, University of Sharjah.

Books

Springer

Prentice Hall

Thomson Delmar Learning

McGraw-Hill

Journals

IEEE Transactions on Dependable and Secure Computing, IEEE Transactions on Computers, IEEE Transactions on Parallel and Distributed Computing, IEEE Transactions on Circuits and

Systems I, II, IEEE Transactions on Software Engineering, IEEE Transactions on Computer Aided Design of ICs and Systems, IEEE Transactions on VLSI Systems, IEEE Transactions on Reliability, Parallel Computing, Software: Practice and Experience, IEEE Computer, Journal of Parallel and Distributed Computing, Simulation Practice and Theory (Elsevier), The Computer Journal, ACM Transactions on Design Automation, ACM Transactions on Computer Education, VLDB Journal, Journal of Electronic Testing: Theory and Applications (JETTA), VLSI Journal, IEE Proceedings on Computer and Digital Techniques, Microelectronics Journal, ETRI Journal (Korea), International Journal of Communication Networks and Distributed Systems (IJCNDS).

Conferences/Workshops

IEEE International Symposium on Fault Tolerant Computing (FTCS), IEEE International Conference on Dependable Systems and Networks (DSN), IEEE Int. Conference of Computer Design (ICCD), IEEE International Computer Performance and Dependability Symposium, IEEE International Symposium on Reliable Distributed Systems (SRDS), IEEE International Test Conference (ITC), IEEE International Symposium on Circuits and Systems (ISCAS), IEEE Computer Software and Applications Conference, IEEE VLSI Design Conference, IEEE Great Lakes Symposium on VLSI, IEEE Asia Test Symposium, IASTED Conference on Reliability, Quality Control and Risk Assessment, International Simulation Symposium, ISMM Conference on Parallel and Distributed Computing Systems, IEEE Symposium on Defect and Fault Tolerance of VLSI Systems, IEEE North Atlantic Test Workshop, IFIP Conference on Digital Forensics, IEEE Malware, IEEE MILCOM, International Workshop on Managing Insider Security Threat (MIST), Parallel, Distributed and Network-Based Processing (PDP), International Conference on Computer Communications and Networks (ICCCN), ACM/IEEE International Conference on Human-Robot Interaction (HRI), International Conference on Information Systems Security and Privacy (ICISSP).

UNIVERSITY SERVICE

UNIVERSITY-WIDE

- Representative of the SUNY Cyber Security Task Force, 2002-06.
- Mentor, SUNY LSAMP Minority Summer Research Internship Program, 2003-04.
- Director, Center of Excellence in Information Assurance Research and Education, 2002 .
- Member, Information Security Risk and Policy Advisory Group (ISRP), 2007.
- Mentor, Ronald E. McNair Post-Baccalaureate Achievement Program, 2011.
- Member, Information Security and Privacy Advisory Committee (ISPAC), 2021-24.

FACULTY-WIDE

- Mentor, Minority Students Mentor Program, 1993 1997.
- Mentor, Presidential Honors Mentor Program, 1993 1997.
- Member, Chair Search Committee, CSE department, 2001-02.
- Mentor, Minority Students Mentor Program, 2003.
- Member, Tenure Committee, 2007-09.
- Associate Dean for Research and Graduate Education, 2017-22.
- Chair, SEAS COVID-19 Research Task Force, 2020.
- Director, SEAS/SOM Cybersecurity MS Program, 2023 .

DEPARTMENT-WIDE

- Member of the Computer Engineering Committee, to make recommendations on equipments and other general needs of the CE Group, ECE Dept., 1988 1997.
- Coordinator of VLSI/Image Processing Laboratory, 1988 1989.
- Member of the Design Credits Evaluation Committee, ECE Dept., 1992.
- Member of the Income Fund Reimbursable (IFR) Committee, ECE Dept., 1992.
- Coordinator for Graduate Colloquium, ECE Dept., Spring 1993.
- Director of Undergraduate Studies, ECE Dept., Fall 1993 Spring 1998.
- Coordinator of ABET preparation during the 1996 visit, ECE Dept.
- Founding Director of Computer Engineering program, Fall 1997 Spring 1999.
- Member of Faculty Recruiting Committee, 1996, 1997, 1999, 2000.
- Member of the Executive Committee, CSE Dept., 1998.
- Director of IBM sponsored Electronic Test Design Automation Lab, 1999 2004.
- Internship Coordinator, CSE Dept., Fall 2002 2005.
- Member of Graduate Affairs Committee, CSE Dept., Fall 2001 2007.
- Member of Graduate Admissions Committee, CSE Dept., 2006 2007.
- Chair, Recruiting Committee, CSE Dept., 2010, 2011, 2013.
- Chair, ACM Dissertation Nomination Committee, 2011.
- Internship Coordinator, CSE Dept., Fall 2009 Summer 2017.
- Associate Department Chair, 2015 2017.
- Member, Search Committee, CSE Dept., 2019, 2020, 2022, 2023, 2024.
- Member, Computer Science and Engineering Strategic Planning Committee, 2021, 2023.
- Chair, CSE Distinguished Speaker Series, 2024-25.

COMMUNITY-WIDE

- Participated (by invitation) in the NSF and the American Association of Community Colleges (AACC) sponsored first workshop "Broadening Impact: NSF-funded Projects at Two-Year Colleges" at Washington DC, 16-17, June 2011.
- Participated (by invitation) in the NSF sponsored Working group meeting on "Information Assurance Education in Two and Four-Year Institutions" during the ACM ITiCSE Conference at Darmstadt, Germany, 24-29, June 2011.
- Participated (by invitation) in the "New York State Cyber Security Roundtable for Healthcare Industry", Roswell Park, NY, August 2015.
- Talk on "Cybersecurity Simplified" North Presbyterian Church, Williamsville, NY, January 2018.

TEACHING

COURSES TAUGHT AT UNDERGRADUATE LEVEL

- 1. ECE 202 Circuit Analysis.
- 2. CSE 341 Computer Organization (revised and restructured): to include both hardware and software aspects. Offered 13 times since fall 1999, last taught in spring 2016.

- 3. CSE 452/552 VLSI Testing (new): offered first time in spring 1992, utilizing the Tektronix LV512 Hardware Chip Tester. The Tektronix tester was later replaced by ELVIS Tester from National Instruments. Facilities include Cadence software tools, last taught in spring 2009.
- 4. ECE 487 Information Structures for Computer Engineers (revised and restructured): this course became one of the most popular electives of the ECE department, drawing students from other Engineering departments as well, last offered in 1996.
- 5. ECE 479: Microprocessors and Applications.

COURSES TAUGHT AT GRADUATE LEVEL

- 1. CSE 561 Performance Analysis of Computer Systems (revised and restructured): this course utilized numerous software tools for performance evaluation. Offered in fall 1998 and Spring 2002.
- 2. CSE 561AM Wireless Communications and Security: this course was offered to students at Amrita University, India as part of the joint MS in Embedded Systems program. Offered in July 2013, December 2013 and June 2016.
- 3. ECE 576 Fault Diagnosis (revised and restructured): this course has been a popular graduate level course, also drawing students from other departments. Last offered in fall 1997.
- 4. CSE 565 Computer Security (new) offered in fall semesters of 2002, 2003, 2004, 2006, 2007, 2008, 2009, 2010, 2011, 2012, 2013, 2014, 2015, 2016 and 2017.
- 5. CSE 566 Wireless Networks Security (new), in spring semesters of 2005, 2006, 2007, 2010, 2013, 2015 and 2019.
- 6. ECE 604 Fault Tolerant Computing (new), offered three times since 1988.
- 7. CSE 713 Topics in Computer System Security offered in spring 2000.
- 8. CSE 704 Hardware-based Approach to Wireless Networks Security in spring 2008.
- 9. CSE 735 Wireless Networks Security in spring 2011.
- 10. CSE 727 –Wireless Networks Security—Principles and Practices in spring 2014.
- 11. CSE 713 Wireless Networks Security—Principles and Practices in spring 2016, spring 2017, spring 2020, spring 2021, spring 2022, spring 2023, fall 2023, fall 2024.

LABORATORY DEVELOPMENT

- A new lab called SPIDER (Security, PrIvacy and DEpendability Research) has been created in 2001 to consolidate the research efforts on fault tolerance, security and Testing. This lab has Sun Workstations and PCs. In mid-2003, this lab was expanded through funds from DARPA and NSA/ARDA. In 2005, a component on wireless networks and security has been added through funds from NSF and Cisco. This lab is now in 338 Bell Hall.
- 2. A separate lab on Information Assurance was created jointly with the School of Management in 2003 to support the teaching of Information Assurance Methods course. This lab was funded by a grant from the Department of Defense.
- 3. The VLSI program, especially the Testing aspects, was initiated in 1989 with funding from Lilly Foundation. More funding was obtained from IEEE/ACM Design Automation in 1996, National Science Foundation in 1996, and IBM Corporation in 1999. The IBM partnership supports a Test Design and Automation Lab. The lab has a RS6000 server and 4 workstations and a variety of tools that include TestBench, BooleDozer and HiaSynth. More than 25 students, both undergrads and grads, receive experience on industry-grade VLSI tools in their course CSE452/552: VLSI Testing. This lab has been expanded to include 10 more workstations and a new server and has moved into a larger space. CircuitBench, a circuit level design and simulation tool has been installed and the existing TestBench tool has been updated. The lab has been used in CSE341:

Computer Organization as well. (The lab has been decommissioned in 2005 due to lack of maintenance from Cadence.)

THESIS AND DISSERTATION SUPERVISION

MS STUDENTS

- 1. Kerr R., "An Expert System for the Selection of an Optimal BIST Design of PLAs", May 1988, first appointment at Eastman Kodak, Rochester, NY.
- 2. Thodiyil J., "Implementation and Analysis of BIST Design of PLAs" Feb. 1990, first appointment at LSI Logic Inc., Milpitas, CA.
- 3. Sehgal A., "Design and Implementation of a Line Monitoring and Control System for Circuit Board Manufacture Using Surface Mount Technology", June 1990, first appointment at Southwestern Bell Communications, Austin, TX.
- 4. Dakwala N., "A Heuristically Aided Probing Strategy for Troubleshooting Electronic Systems", Feb. 1991, first appointment at Motorola, Austin, TX.
- 5. Ranganathan A., "Performance Analysis of Rollback Recovery Techniques", May 1991, Continued into PhD.
- 6. Chen X., "Circuit Partitioning for the Testable Design of Large and Complex Systems", Sept. 1991.
- 7. Francis C.S., "Software Design Using ADA A Data Link Monitor Application", Dec. 1991, appointed at Sierra Research, Buffalo, NY.
- 8. Chen L.C., "On-chip Test Generation for Combinational Circuits by LFSR Modification", May 1993, joined Univ. of Southern California as a PhD student in Fall 1993.
- 9. Vidyaranya, "Parallel Implementation of Viterbi Algorithm for Solving Word Recognition Problems Using Hidden Markov Model", July 1993, M.S., first appointment at Salomon Brothers Inc. NJ.
- 10. Babu K.J., "Detection and Diagnosis of Faults in Analog VLSI Circuits Using Specification-BIST (S-BIST)", July 1994, M.S., first appointment at Ford Motor Company, Detroit, MI.
- 11. Reuse R.J., "A New Approach to Random testing of sequential circuits", January 1997.
- 12. Terrill T., "A Software-Implemented Error Detection Scheme Designed for Run-time Monitoring of Pipelined Superscalar Processors", June 1997, first appointment at Lockheed-Martin, Maryland.
- 13. Lu M., "3-D height occultation algorithm for radar navigation simulation system", February 1998, supported as RA, first appointment at Azerty Inc., Orchard Park, NY, as Programmer/Analyst.
- 14. Nissar A., "Fault Diagnosis of Mixed Signal VLSI Systems Using Neuromorphic Analyzers", July 1998, first appointment at Intel Corporation, Portland, OR.
- 15. Unnikrishnan N., "Reconfiguration of Field Programmable Gate Array (FPGA) Logic Blocks for Testability", September 1998, first appointment at LSI Logic, San Jose, CA.
- 16. Mantha K., "Concurrent Intrusion Detection in Distributed Systems", July 2000, first appointment at Deloitte & Touche Consulting, New York City, NY.
- 17. Nagaraj P., "Defect Analysis and Realistic Fault Model Extensions in Multiport Memories", June 2001, first appointment at Qualcomm, San Diego, CA.

- 18. Jafari R., "Enhancements to Testability Analysis and a Novel Method for Test-Point Insertion in Digital Circuits", January 2002, joined Ph.D. program at University of California at Los Angeles, LA in Spring 2002.
- 19. Gummadidala, R., "A Geographic Leash Based Routing Protocol With Wormhole Defense for Mobile Ad Hoc Networks", January 2004, first appointment at Qualcomm, San Diego, CA.
- 20. Challapalli H., "A New Framework for a Secure Federated Patient Healthcare System", January 2004
- 21. Sundararaman K., "Design for Manufacturability Fault Model Extensions for RF Circuits with an Economic Perspective for Manufacturability, May 2004, first appointment at Qualcomm, San Diego, CA.
- 22. Chandrasekaran M., "Fast Online Reasoning Technique (FORT) Using Sequential Hypothesis Testing for Reasoning about Intrusions", August 2004, continued into Ph.D. at SUNY at Buffalo.
- 23. Muthukrishnan A., "Job Centric User Level Anomaly Detection", August 2004, first appointment at Frontier Science & Technology Research Foundation, Amherst, NY, currently at Bloomberg.
- 24. Tong M., "Twitter Structure and Formation Process from Information Propagation and Security Perspectives", December 2013, first appointment at Amazon Inc.

PHD STUDENTS

- 1. Chen Y.Y., "Fault Tolerant Design of Large Area Array Processors by Multiple Level Redundancy", August 1991; first employed a faculty member in Chung-Hua Polytechnic Institute, Hsinchu, Taiwan.
- 2. Ranganathan A., "Techniques and Models for Rollback Recovery in Distributed Systems", December 1993; first employed by Sun Microsystems, Mountain View, CA.
- 3. Kumar A., "A Framework for Function in Model Based Diagnosis", March 1994; currently a Professor at Ramapo College, NJ.
- 4. Demjanenko M., "Yield Enhancement by Inherent Component Redundancy", January 1995; first employed by VoCal Technology, Buffalo, NY.
- 5. Spina R., "A Framework for the Testing and Fault Diagnosis of Analog and Mixed-Signal Circuits Using Artificial Neural Networks", Aug. 1996; first employed by NBX Corporation, Boston, MA.
- 6. Goldberg S., "Toward a Practical Realization of Fault-Tolerant Processing Arrays"; January 1997, currently a Professor at Buffalo State College.
- 7. Ramamurthy B., "Hardware-Assisted Rollback Recovery in Distributed Systems", January 1997; currently working as a Teaching Professor in CSE Department, SUNY at Buffalo.
- 8. Zarrineh K., "Architecture and Design of a Memory Test Synthesis Framework", May 1999; currently working at Advanced Micro Devices (AMD), Boston, MA.
- 9. Wu H., "Commonality Architecture: A New Interactive Video-on-Demand Systems Paradigm", September 2001; currently a faculty member in Taiwan.
- 10. Tien T.C., "Adaptive Routing in Dynamic Wireless Mobile Ad Hoc Networks", January 2003.
- 11. Zhao D., "An Integrated Framework for Concurrent Test and Wireless Control in Complex SoCs", December 2003; currently an Associate Professor in CS Department, Old Dominion University, Norfolk, VA.
- 12. Chinchani R., "Job-Centric User-Level Intrusion Detection", May 2005; First job at Cisco Systems as Software Engineer.

- 13. Garg A., "A Modeling and Simulation Framework for Evaluation of Intrusion Detection Systems", August 2006; ArcSight Inc. as Senior Software Engineer.
- 14. Pramanik S., "Adaptive End-to-End Dependability for Generic Applications in a Network Environment", August 2007; ArcSight Inc. as Senior Software Engineer.
- 15. Vidyaraman S., "GUST: Game theoretic User-centered Security design Techniques", February 2008; Microsoft as Program Manager.
- 16. Virendra M., "Secure, Robust and Trusted Communications in Wireless Networks", May 2008; Google, Product Head.
- 17. Jadliwala M., "Security and Robustness of Localization Techniques for Emergency Sensor Networks", May 2008; currently an Associate Professor in CS Department, University of Texas at San Antonio, San Antonio, TX.
- 18. Chadrasekaran M., "An Introspective Behavior Based Methodology to Mitigate Internet Attacks", May 2009; Amazon Inc., Engineer.
- 19. Mathew S., "Techniques for Cyber-Attack Comprehension through Analysis of Application Level Data", July 2009; Amazon Inc., Engineer.
- 20. Mehresh R., "Schemes for Surviving Advanced Persistent Threats", September 2013, EMC Corporation, Software Engineer.
- 21. Sanzgiri A., "A Comprehensive Threat Assessment Framework for Securing Emerging Technologies", December 2013, Intel Corporation, Santa Clara, CA.
- 22. Ceker H., "Keystroke Dynamics for Enhanced User Recognition in Active Authentication", May 2017, Amazon Inc., Engineer.
- 23. Sun Y., "Shared Keystroke Data for Continuous Authentication Generation and Analysis", January 2018, Bloomberg, Engineer.
- 24. Kul G., "Insider Threat on Databases: Modeling and Mitigation Techniques", June 2018, University of Massachusetts Dartmouth, Assistant Professor.
- 25. Baksi R., "Framework and Strategies to Mitigate Advanced Persistent Threats (APT)", July 2022, Illinois State University, Assistant Professor.

HONORS TO PHD STUDENTS

- Kumar A., Presidential Fellowship, State University of New York at Buffalo, 1987-90.
- Tien T.C., Best Student Paper Award in the 1st IEEE IWoRC Conference, Buffalo, NY, April 2000.
- Zhao D., Best Student Paper Award, 12th IEEE North Atlantic Test Workshop, Montauk, NY, May 2003.
- Zhao D., NSF Career Award, Aug. 2009.
- Nataraj P. and Nagarajaiah H., Best Student Paper Award in the 4th Secure Knowledge Management Workshop, Rutgers University, October 2010.
- Jadliwala, M., NSF Career Award, May 2020.
- Zhao D., *NSF Program Director*, CCF (CISE Division), 2022 .

GRANTS AND CONTRACTS

1987-89 RADC & Co-PI, Versatile Maintenance Expert System, with S. \$756,992
AFOSR Shapiro (PI) and S. Srihari (co-PI) (joined as co-PI in the ongoing project to investigate fault diagnosis)

1987 – 99	NYS/UUP	Travel to various Conferences	\$4,730
1988-89	Lilly Foundation	PI, Development of an Interactive Laboratory for Undergraduate Instruction in Electrical & Computer Eng., with R. Acharya (co-PI) and R. Sridhar (co-PI)	\$8,000
1988-91	NSF	PI, Error Detection and Recovery Issues in Real-time Computer Systems (Research Initiation Award + REU Supplement)	\$66,610
1990	ACM SIGDA	Travel to IEEE Design Automation Conference	\$500
1991-92	IEEE/ACM	Honorable Mention Scholarship Grant (SIGDA)	\$1,000
1992-98	NSF	Fabrication of VLSI Testing Project Chips at MOSIS	\$38,420
1996-99	NSF	Co-PI, Instrumentation and Laboratory Improvement Program (ILI), with R. Sridhar (PI) (\$41,000 in matching funds from UB)	\$39,000
6/96- 10/96	BCG, Buffalo	PI, Global Marine Distress Safety System Radio Telex Modem Simulator Development (L. Warnock, co-PI)	\$12,651
1/97- 10/97	BCG, Buffalo	PI, Marine Radar/Navigation Simulator Development (with L. Warnock, co-PI)	\$58,260
1997-98	IEEE/ACM	PI, A Design for Test Perspective on Memory Synthesis	\$12,000
1998-99	Kristal Systems	PI, Gaitway Instrumented Treadmill Systems Software Development (with S. White and T. Mattulke, co-PIs)	\$52,071
1999	IBM Corporation	Electronic Test Design Automation Laboratory (on loan basis, worth more than \$0.5 million)	-
1999-00	AFOSR	PI, A Distributed Concurrent Intrusion Detection and Recovery Scheme Based on Assertions	\$25,000
2000	Motorola Foundation	Computer Engineering Education	\$2,500
2000-01	IBM Corporation	PI, Built-In Self-Test of Sequential Circuits Using the Hold Method (Faculty Partnership Award)	\$82,285
2000-04	AFRL/AFOSR	PI, A Comprehensive Reasoning Framework for Information Survivability (\$36,907 subcontracted to Colorado State Univ.)	\$305,000
2001	UB EdTech	PI, Upgrade of the Test Design Automation Laboratory	\$8,000
3/01-9/01	Telcordia Technologies	PI, Prevention of Attacks in Computer Networks (administered as a gift to UB)	\$20,000
2001-02	IBM Corp.	PI, Enhancements to TestBench	\$32,165
2002-04	NYSTAR	PI, A New Embedded Built-In Self-Test Module for Future Systems-on-Chip (subcontract from Univ. of Rochester)	\$45,000
2002-04	DoD/NSA	PI, Information Assurance Scholarship Program and Capacity Building in IA, with H.R. Rao (co-PI)	\$271,579
2003	UB VP for Research	PI, Securing the Cyberspace – A Planning Proposal, with H.R. Rao (co-PI)	\$10,000
2003-04	Vocal	A New Embedded Built-In Self-Test Module for Future	\$2,500

	Technologies	Systems-on-Chip	
2003-06	Semiconductor Research Corporation	PI, Non-intrusive Signal Analysis of Embedded Multi-GHz RF Circuits (subcontract from Univ. of Rochester with M. Margala, \$300,000; the other subcontractor is P.R. Mukund, RIT)	\$80,000
2003-04	DARPA	PI, User Intent Encapsulation and Reasoning About Intrusion: Implementation and Performance Assessment	\$315,000
2003-05	NSA/ARDA	PI, Protecting Documents from Insider Threat – A Multi-Phase Approach	\$226,620
2003-04	DoD/NSA	Information Assurance Scholarship Program and Capacity Building in IA, with H.R. Rao (co-PI)	\$243,284
2003-04	NSA/ARDA	PI, Event Correlation for Cyber Attack Recognition System, with J. Llinas, co-PI (subcontract from Alion Science and Technology, administered through Calspan- UB Research Center)	\$157,000
2004	UB EdTech	Co-PI, HIPAA Compliant Medical Data Repository for Teaching (with R. Sharman, PI and H. Rao, co-PI)	\$8,000
2004-05	NSF	PI, Secure Knowledge Management, A Workshop, with H.R. Rao (co-PI)	\$8,000
2004-06	DARPA	PI, Mitigating the Insider Threat using High Dimensional Search and Modeling, with Hung Ngo, co-PI (subcontract from Telcordia Technologies, with E. Van Den Berg, \$1,292,295; the other subcontractor is R. Maxion, CMU)	\$255,862
2004-06	NSF	PI, Federal Cyber Service: Expanding Capacity in Information Assurance Curriculum, with H.R. Rao (co-PI) (\$50,000 subcontracted to Genesee Community College)	\$299,699
2004-06	NSF	Co-PI, Women and Cyber Security: Gendered Tasks and Inequitable Outcomes, with H.R. Rao (PI) and S. Bagchi-Sen (co-PI)	\$297,021
2004-05	AFRL	PI, Secure Knowledge Management – A Workshop, with H.R. Rao (co-PI)	\$5,000
2004-07	NSF	PI, Information Systems Security Curriculum Development, with H.R. Rao (co-PI) (subcontract from Erie Community College, with Donna Kaputa, \$450,000, under the ATE Projects)	\$157,926
2004-05	DoD/NSA	PI, Information Assurance Scholarship Program and Research on Intrusion Detection and Response, with H.R. Rao (co-PI)	\$185,191
2005-06	DoD/NSA	PI, Information Assurance Scholarship Program, with H.R. Rao (co-PI)	\$100,925
2005-06	Cisco	PI, Equipment Donation (Computer Security Lab), with H.R. Rao (co-PI), Chunming Qiao (co-PI), Xin Wang (co-PI), R. Sharman (co-PI), David Murray (co-PI), Mathew Stock (co-PI)	\$131,333
2006-07	UB VP for	Co-PI, Analyzing Emergency Response Management	\$22,082

	Research	Systems in the Context of the Katrina and Rita Disasters – A First Responder Focus, with H.R. Rao (PI), R. Sharman (co-PI), C. Cook-Cottone (co-PI)	
2006-07	ITT Industries	PI, Inferring the Loss of Service Quality in a Disadvantaged Network – A Game Theoretic Perspective	\$35,000
2006-07	NSF	Co-PI, SGER: The October 2006 Federal Disaster in Buffalo, NY: An Investigation of First and Second Responder Operations, with H.R. Rao (PI), R. Sharman (co-PI), C. Cook-Cottone (co-PI)	\$29,977
2006-07	ITT Industries	PI, A Framework for Trusted and Reliable Cyber Interactions in Wireless Networks	\$50,000
2007-09	NSF	PI, SFS (Capacity Building Track): Faculty Development to Promote Computer Forensics in the IA Curriculum, with H.R. Rao (co-PI), (\$37,500 subcontract to Hilbert College)	\$150,000
2007-08	DoD/NSA	PI, Information Assurance Scholarship Program and Capacity Building in Information Security, with H.R. Rao (co-PI)	\$128,583
2008-11	NSF	PI, Computer Security and Investigations: An Integrative Approach to Curriculum Development in Digital Forensics, with H.R. Rao (co-PI) (subcontract from Erie Community College, with Donna Kaputa, \$449,662, under the ATE Projects)	\$149,631
2008-09	Intel Corp.	Accelerating Techniques for Rapid Mitigation of Phishing and Spam Emails	\$29,000
2008-09	DoD/NSA	PI, Information Assurance Scholarship Program, with H.R. Rao (co-PI)	\$69,152
2008-12	NSF	PI, SFS (Scholarship Track): An Interdisciplinary Information Assurance Curriculum, with H.R. Rao (co-PI), T. Cusick (co-PI) and M. Bartholomew (co-PI)	\$868,037
2009-10	ITT Industries	PI, Secure Proactive Recovery	\$77,267
2009-10	NSF	Co-PI, U.SIndia Workshop on Security and Privacy in Global Information Systems, with H.R. Rao (PI)	\$15,246
2009-10	Intel Corp.	PI, Accelerating Techniques for Rapid Mitigation of Phishing and Spam Emails – A Research Extension	\$25,000
2009-10	Intel Corp.	Co-PI, Two Media/Content Processing Applications on Atom: Mobile 3D Graphics and Event Correlation for	\$29,000
2009-12	NSF	Cyber Attack Recognition, with Chang Wen Chen (PI) Co-PI, TC: Small: Online Privacy and Senior Citizens: A Socio-Technical Multi-Perspective Framework for Trustworthy Operations, with H.R. Rao (PI), S. Bagchi- Sen (Co-PI)	\$454,771
2010-10	Harris Comm.	Formal Methods-Based Common Criteria Certification Framework for a Separation Kernel, Phase 1	\$34,566
2010-11	DoD/NSA	PI, Information Assurance Scholarship Program, with H.R. Rao (co-PI)	\$77,552
2010-11	Harris Comm.	Formal Methods-Based Common Criteria Certification Framework for a Separation Kernel, Phase 2	\$85,000
2011-12	DoD/NSA	PI, Information Assurance Scholarship Program, with	\$89,353

		H.R. Rao (co-PI)	
2012-13	DoD/NSA	PI, Information Assurance Scholarship Program, with H.R. Rao (co-PI)	\$44,442
2012-17	NSF	PI, SFS: An Interdisciplinary Information Assurance Curriculum, with H.R. Rao (co-PI), T. Cusick (co-PI) and M. Bartholomew (co-PI)	\$1,762,559
2013-16	NSF	Co-PI, TWC: Medium: Collaborative: Long-term Active User Authentication Using Multi-modal Profiles, with V. Govindaraju (PI), Ifeoma Nwogu (co-PI)	\$849,718
2014-18	NSF	Co-PI, TWC: Medium: Collaborative: Data is Social: Exploiting Data Relationships to Detect Insider Attacks, with H. Ngo (PI), O. Kennedy (Co-PI), V. Chandola (Co-PI)	\$959,999
2015-16	NSF	PI, GenCyber: Educating and Preparing Young Adults for a Cyber-driven World, with H.R. Rao (Co-PI)	\$86,393
2015-17	NSF	Co-PI, EDU: Collaborative: Cybersecurity for Middle School Students at Museums: An Informal Learning Approach, with H.R. Rao (PI)	\$199,759
2016-17	NSA	PI, Buffalo GenCyber Camp 2016, with H.R. Rao (Co-PI), D.J. Murray (Co-PI), L. Amo (Co-PI)	\$99,169
2016-17	DMDII, UI Labs, Chicago	Co-PI, Digital Manufacturing & Design Specialization, K. Lewis (PI), Grant through UB TCIE	\$385,489
2016-17	Peeva LLC, Buffalo	PI, Pet Tracking Software Development, with W. Xu (Co-PI), Grant through UB TCIE	\$54,725
2017-20	NSF	Co-PI, SaTC: CORE: Small: Collaborative: Cardiac Password: Exploring a Non-Contact and Continuous Approach to Secure User Authentication, with W. Xu (PI)	\$294,582
2017	NSF	Co-PI, NSF Student Travel Grant for 2017 Secure Knowledge Management Workshop (SKM), with H.R. Rao (PI), R. Santanam (Co-PI), M. Agrawal (Co-PI), R. Krishnan (Co-PI)	\$12,142
2018	NSA	PI, CLEAR-Cybersecurity Learning, Education, Awareness and Readiness: Buffalo GenCyber Camp 2018, with L. Amo (Co-PI), D.J. Murray (Co-PI)	\$94,519
2018-23	NSF	PI, CyberCorps(R): SFS (Scholarship Track): Renewal: An Interdisciplinary Cyber Security Curriculum, with L. Sanders (Co-PI), T. Cusick (Co-PI), M. Bartholomew (Co-PI)	\$2,396,371
2019	NSA	PI, CLEAR-Cybersecurity Learning, Education, Awareness and Readiness: Buffalo GenCyber Camp 2019, with L. Amo (Co-PI), D.J. Murray (Co-PI)	\$94,029
2019	Tiggee LLC	PI, High-speed Inline Encryption, Grant through UB TCIE	\$42,395
2020	NSA	PI, CLEAR-Cybersecurity Learning, Education, Awareness and Readiness: Buffalo GenCyber Camp 2019, with L. Amo (Co-PI), D.J. Murray (Co-PI)	\$97,128
2020	NSF	Senior Personnel, REU Site: Frontier Technologies for Biometrics and Authentication, with Wenyao Xu (PI), Zhanpeng Jin (Co-PI)	\$393,599

2021	NSA	PI, CLEAR-Cybersecurity Learning, Education, Awareness and Readiness: Buffalo GenCyber Camp	\$126,577
		2022, with L. Amo (Co-PI), D.J. Murray (Co-PI)	
2022-24	NSA	Co-PI, Building High Assurance Containers Using FPGA, with Z. Zhao (PI), H. Hu (Co-PI)	\$490,879
2023-26	NSF	Co-PI, CyberTraining: Implementation: Small: Infrastructure Cybersecurity Curriculum Development and Training for Advanced Manufacturing Research Workforce, with W. Xu (PI), C. Zhou (Co-PI), and H. Sun (Co-PI)	\$499,989
2023-25	NSA	PI, CLEAR-Cybersecurity Learning, Education, Awareness and Readiness: Buffalo GenCyber Camp 2024, with L. Amo (Co-PI), D.J. Murray (Co-PI)	\$124,313
2023-28	NSF	PI, CyberCorps(R): SFS (Scholarship Track): Renewal: An Interdisciplinary Cyber Security Curriculum with Technical and Managerial Paths, with L. Sanders (Co-PI), T. Cusick (Co-PI), M. Bartholomew (Co-PI)	\$3,418,604
2024-27	NSF	Co-PI, Explorations: Refugees & Underprivileged Students Experiential Learning in Emerging STEM Technologies, with S. Das Bhattacharjee (PI), W. Xu (Co-PI), W. Kim (Co-PI)	\$1,000,000

Notes: 1) NYSTAR is New York State Science and Technology for Academic Research Agency.

2) NYS/UUP is the New York State United University Professionals Committee which offers competitive travel grants based on proposal submissions.

PATENTS

- "Smart Device Enabled Secure Access to Multiple Entities", Invention Disclosure filed on May 4, 2012.
- "Method for Rendering Usable a Defective Raw Programmable Logic Array", U.S. Patent No. 4,920,497, April 1990.

PUBLICATIONS

Total number of refereed publications: 308; Google Scholar Citations: 9,031; h-index: 44

EDITED BOOKS

- 1. Upadhyaya S., K. Kwiat, A. Chaudhuri and M. Weiser (eds.), Mobile Computing: Implementing Pervasive Information and Communication Technologies, *Kluwer Academic Publishers Book Series on Interfaces in Operations Research and Computer Science*, June 2002.
- 2. Rao H.R., M. Gupta and S. Upadhyaya (eds), Managing Information Assurance in Financial Services, *Idea Group Inc.*, May 2007.
- 3. Rao H.R. and S. Upadhyaya (eds), Annals of Emerging Research in Information Assurance, Security and Privacy Services, *Elsevier*, 2009.

EDITED JOURNAL ISSUES

1. Upadhyaya S. and A. Bondavalli, Guest Editors, Special Issue on Reliable Distributed Systems, *IEEE Transactions on Computers*, Feb. 2003.

- 2. Rao, H.R. and S. Upadhyaya, Guest Editors, Part 1: Special Issue on Secure Knowledge Management, *IEEE Transactions on Systems, Man and Cybernetics, Part A*, May 2006.
- 3. Park, I., J.K. Lee, S. Upadhyaya and H.R. Rao, Part 2: Emerging Issues for Secure Knowledge Management Results of a Delphi Study, *IEEE Transactions on Systems, Man and Cybernetics, Part A*, May 2006.
- 4. Kermani M., E. Savaş and S. Upadhyaya, Guest Editors, Special Issue on Emerging Security Trends for Deeply-Embedded Computing Systems, *IEEE Transactions on Emerging Topics in Computing*, Vol. 4, No. 3, July 2016, pp. 318-320.
- 5. Sahay S.K., N. Goel, M. Jadliwala and S. Upadhyaya, Guest Editors, Special Issue on Advances in Secure Knowledge Management in the Artificial Intelligence Era, *Information Systems Frontiers*, Springer, Vol. 23, No. 4, August 2021.

REFEREED JOURNAL ARTICLES

- 1. Goel P.S., V.K. Agarwal, A. Krishnan and S.J. Upadhyaya, "Auto reconfiguration of reaction wheels in IRS", *IEEE Transactions on Aerospace and Electronics Systems*, Vol. AES-21, No. 1, pp. 160-163, Jan. 1985.
- 2. Upadhyaya S.J. and K.K. Saluja, "A watchdog processor based general rollback technique with multiple retries", *IEEE Transactions on Software Engineering* Special Issue on Software Reliability (Amrit Goel, Guest Editor), Vol. SE-12, No. 1, pp. 87-95, Jan. 1986.
- 3. Upadhyaya S.J. and K.K. Saluja, "A new approach to the design of built-in self testing PLA's for high fault coverage", *IEEE Transactions on Computer Aided Design of Integrated Circuits and Systems*, Special Issue on Testable and Maintainable Design (E.J. McCluskey, Guest Editor), Vol. 7, No. 1, pp. 60-67, Jan. 1988¹.
- 4. Upadhyaya S.J. and K.K. Saluja, "An experimental study to determine task size for rollback recovery systems", *IEEE Transactions on Computers*, Vol. 37, No. 7, pp. 872-877, July 1988.
- 5. Pham H. and S.J. Upadhyaya, "The efficiency of computing the reliability of k-out-of-n systems", *IEEE Transactions on Reliability*, Vol. 37, No. 5, pp. 521-523, Dec. 1988.
- 6. Pham H. and S.J. Upadhyaya, "Reliability analysis of a class of fault tolerant systems", *IEEE Transactions on Reliability*, Vol. 38, No. 3, pp. 333-337, Aug. 1989.
- 7. Demjanenko M. and S.J. Upadhyaya, "Yield enhancement of field programmable logic arrays by inherent component redundancy", *IEEE Transactions on Computer Aided Design of ICs and Systems*, Vol. 9, No. 8, pp. 876-884, Aug. 1990.
- 8. Pham H. and S.J. Upadhyaya, "Optimal design of RAFT based fault tolerant distributed systems", *IEEE Transactions on Reliability*, Vol. 40, No. 3, pp. 375-379, Aug. 1991.
- 9. Kumar A. and S.J. Upadhyaya, "Focusing candidate generation", *Artificial Intelligence in Engineering*, Vol. 6, No. 3, pp. 156-159, July 1991.
- 10. Upadhyaya S.J. and S. Chakravarty, "Analysis of a fault-tolerant scheme for processor ensembles", *IEEE Transactions on Reliability*, Vol. 41, No. 2, pp. 294-302, June 1992.
- 11. Upadhyaya S.J. and H. Pham, "Optimal design of *k-to-l-out-of-n* systems", *International Journal of Modelling and Simulation*, Vol. 12, No. 3, pp. 69-72, Sept. 1992.

¹ The technique included as a section in the textbook on "Digital Systems Testing and Testable Design", by M. Abramovici, M. Breuer and A.D. Friedman, Computer Science Press, 1990.

- 12. Upadhyaya S.J. and H. Pham, "Analysis of noncoherent systems and an architecture for the computation of the system reliability", *IEEE Transactions on Computers*, Vol. 42, No. 4, pp. 484-493, Apr. 1993.
- 13. Chen Y.Y. and S.J. Upadhyaya, "Reliability, reconfiguration and spare allocation issues in binary tree architecture based on multiple level redundancy", *IEEE Transactions on Computers*, Vol. 42, No. 6, pp. 713-723, June 1993.
- 14. Ranganathan A and S.J. Upadhyaya, "Performance evaluation of rollback recovery techniques in computer programs", *IEEE Transactions on Reliability*, Special Issue on Software Fault Tolerance (J.F. Meyer and H. Pham, Guest Editors), Vol.42, No. 2, pp. 220-226, June 1993.
- 15. Chen Y.Y. and S.J. Upadhyaya, "Yield analysis of reconfigurable array processors based on hierarchical redundancy", *IEEE Transactions on Computers*, Vol. 42, No. 9, pp. 1136-1141, Sept. 1993.
- 16. Upadhyaya S.J. and B. Ramamurthy, "Concurrent process monitoring with no reference signatures", *IEEE Transactions on Computers*, Vol. 43, No. 4, pp. 475-480, Apr. 1994.
- 17. Chen Y.Y. and S.J. Upadhyaya, "A new approach to modeling the performance of a class of fault tolerant VLSI/WSI systems based on multiple-level redundancy", *IEEE Transactions on Computers*, Vol. 43, No. 6, pp. 737-748, June 1994.
- 18. Kumar A. and S.J. Upadhyaya, "Function based candidate discrimination during model-based diagnosis", *Applied Artificial Intelligence, An International Journal*, Vol. 9, No. 1, pp. 65-80, Jan. 1995.
- 19. Upadhyaya S.J. and I.S. Hwang, "Design of a multi-level fault-tolerant mesh (MFTM) for high reliability applications", *International Journal of Reliability, Quality, and Safety Engineering, World Scientific*, Vol. 2, No. 4, pp. 419-429, December 1995.
- 20. Spina R. and S.J. Upadhyaya, "Linear circuit fault diagnosis using neuromorphic analyzers", *IEEE Transactions on Circuits and Systems-II*, Vol. 43, No. 3, pp. 188-196, March 1997.
- 21. Chen Y.Y., S.J. Upadhyaya and C.W. Cheng, "A comprehensive reconfiguration scheme for fault-tolerant VLSI/WSI array processors", *IEEE Transactions on Computers*, Vol. 46, No. 12, pp. 1363-1370, December 1997.
- 22. Kumar A. and S.J. Upadhyaya, "Component ontological representation of function for reasoning about devices", *Artificial Intelligence in Engineering Journal*, Vol. 12, No. 4, pp. 399-415, June 1998.
- 23. Nachman L., K.K. Saluja, S.J. Upadhyaya and R. Reuse, "A novel approach to random pattern testing of sequential circuits", *IEEE Transactions on Computers*, Special Issue on Dependability of Computing Systems (K. Kanoun and I. Pomerantz, Guest Editors), Vol. 47, No. 1, pp. 129-134, Jan. 1998.
- 24. Goldberg S. and S.J. Upadhyaya, "Implementing degradable processing arrays", *IEEE Micro*, Vol. 18, No. 1, pp. 64-74, Jan/Feb, 1998.
- 25. Ramamurthy B., S.J. Upadhyaya and B. Bhargava, "Design and analysis of an integrated checkpointing and recovery scheme for distributed applications", *IEEE Transactions on Knowledge and Data Engineering*, Special Issue on Reliable Distributed Systems, Vol. 12, No. 2, pp. 174-186, March/April 2000.
- 26. Zarrineh, K., S.J. Upadhyaya and V. Chickermane, "System-on-Chip testability using LSSD scan structures", *IEEE Design and Test of Computers*, Vol. 18, No. 3, pp. 83-97, May-June 2001.
- 27. Agarwal M., R. Kishore, H.R. Rao and S. Upadhyaya, "Towards a test bed for modelling application service provider (ASP)", *Vision, The Journal of Business Perspective*, Vol. 5, No. 1, pp. 13-23, January-June, 2001.

- 28. Zarrineh K., S.J. Upadhyaya and S. Chakravarty, "Automatic Generation and Compaction of March Tests for Memory Arrays", *IEEE Transactions on VLSI Systems*, Vol. 9, No. 6, pp. 845-857, December 2001.
- 29. Zhao D. and S. Upadhyaya, "A Generic Resource Distribution and Test Scheduling Scheme for Embedded Core-Based SoCs", *IEEE Transactions on Instrumentation and Measurement*, Vol. 53, No. 2, pp. 318-329, April 2004.
- 30. Gupta M., H.R. Rao and S. Upadhyaya, "Electronic Banking and Information Assurance Issues: Survey and Synthesis", *Journal of Organizational and End User Computing*, Special Issue on Information Assurance and Security, IDEA Group Publishing, Vol. 16, No. 3, pp. 1-21, July-September 2004.
- 31. Goldberg S., S. Upadhyaya and W.K. Fuchs, "Recovery Schemes for Mesh Arrays Utilizing Dedicated Spares", *IEEE Transactions on Reliability*, Vol. 53, No. 4, pp. 445-451, Dec. 2004.
- 32. Tanna G., M. Gupta, H.R. Rao and S. Upadhyaya, "Transaction and Workflow Analysis of Electronic Bill Payment and Presentation Systems An Information Assurance Perspective", *Journal of Decision Support Systems*, Elsevier, Vol. 41, No. 1, pp. 242-261, November 2005.
- 33. Chinchani R., Duc Ha, Anusha Iyer, Hung Q. Ngo, and Shambhu Upadhyaya, "On the Hardness of Approximating the MIN-HACK Problem", *Journal of Combinatorial Optimization*, Springer, Vol. 9, No. 3, pp. 295-311, May 2005.
- 34. Zhao D. and S. Upadhyaya, "Dynamically Partitioned Test Scheduling with Adaptive TAM Configuration for Power-Constrained SoC Testing", *IEEE Transactions on Computer-Aided Design of ICs and Systems*, Vol. 24, No. 6, pp. 956-965, June 2005.
- 35. Lee J.K., S. Upadhyaya, H.R. Rao and R. Sharman, "Secure Knowledge Management and the Semantic Web", *Communications of the ACM*, Special Section on Semantic E-Business Vision, Vol. 48, No. 12, pp. 48-54, December 2005.
- 36. Chai S., S. Bagchi-Sen, C. Morrell, H. Raghav Rao, and S. Upadhyaya, "Role of Perceived Importance of Information Security: An Exploratory Study of Middle School Children's Information Security Behavior", *The Journal of Issues in Informing Science and Information Technology*, Information Science Institute, Vol. 3, pp. 127-136, 2006.
- 37. Zhao D., S. Upadhyaya and M. Margala, "Design of a Wireless Test Control Network with Radio-on-Chip Technology for Nanometer System-on-Chip", *IEEE Transactions on Computer-Aided Design of ICs and System (TCAD)*, pp. 1411-1418, Vol.25, No.7, July 2006.
- 38. Ghosh D., R. Sharman, H.R. Rao and S. Upadhyaya, "Self-Healing Systems Survey and Synthesis", *Journal of Decision Support Systems*, Elsevier, 42 (January 2007) 2164-2185.
- 39. Jadliwala M., Qi Duan, Jinhui Xu and Shambhu Upadhyaya. "On Extracting Consistent Graphs in Wireless Sensor Networks", *International Journal of Sensor Networks (IJSNET):* Special Issue on Theoretical and Algorithmic Aspects in Sensor Networks, Vol. 2, Nos 3/4, pp. 149-162, 2007.
- 40. Park I., R. Sharman, H.R. Rao and S. Upadhyaya, "Short Term and Total Life Impact Analysis of Email Worms in Computer Systems", *Decision Support Systems*, 43 (April 2007) 827-841.
- 41. Chen R., R. Sharman, H.R. Rao and S. Upadhyaya, "Design Principles for Emergency Response Systems", *Special Issue on Security Informatics, Information Systems and e-Business Management*, Springer, Vol. 5, No. 3, June 2007.
- 42. Park I., R. Sharman, H.R. Rao and S. Upadhyaya, "The Effect of SPAM and Privacy Concerns on email Users' Behavior", *Journal of Information System Security*, Vol. 3, No. 1, pp. 37-63, 2007.
- 43. Kim J.K., R. Sharman, H. Raghav Rao and S. Upadhyaya, "Efficiency of Critical Incident Management Systems: Instrument Development and Validation", *Decision Support Systems*, 44(1), pp. 235-250, November 2007.

- 44. Cody E., R. Sharman, H.R. Rao and S. Upadhyaya, "Security in Grid Computing: A Review and Synthesis", *Journal of Decision Support Systems, Elsevier*, 44 (March 2008), 749-764.
- 45. Chen R., N. Chakravarty, H.R. Rao, R. Sharman, S. Upadhyaya, "Emergency Response Information System Interoperability: Development of Chemical Incident Response Data Model", Special Issue on Cultivating and Securing the Information Supply Chain, *Journal of the Association for Information Systems*, 9(3), March 2008.
- 46. Chen R., R. Sharman, H.R. Rao, and S. Upadhyaya, "Coordination in Emergency Response Management", *Communications of ACM*, 51(5), pp. 66-73, May 2008.
- 47. Liu L., P. Nagaraj, S. Upadhyaya and R. Sridhar, "Defect Analysis and Fault Tolerant Design for Multi-port SRAMs", *Journal of Electronic Testing: Theory and Applications*, Special Issue on DFTISS (Nur Touba, Guest Editor), Vol. 24, No. 1-3, pp. 165-179, June 2008.
- 48. Upadhyaya S., N. Venugopal, N. Shastry, S. Gopalakrishnan, B. Kuppuswamy, R. Bhowmik and P. Mayor, "Design Considerations for High Performance RF Cores Using Process Variation Study", *Journal of Electronic Testing: Theory and Applications*, Vol. 24, No. 1-3, Special Issue on DFTISS (Nur Touba, Guest Editor), pp. 143-155, June 2008.
- 49. Chai S., S. Bagchi-Sen, C. Morrell, H.R. Rao and S. Upadhyaya, "Internet and Online Privacy: An Exploratory Study of Pre and Early Teens Behavior", *IEEE Transactions on Professional Communications*, Vol. 53, No. 2, June 2009, pp. 167-182.
- 50. Ryu C., R. Sharman, H.R. Rao and S. Upadhyaya, "Security Protection Design for Deception and Real System Regimes: A Model and Analysis", *European Journal of Operations Research*, Vol. 201, No. 2, pp. 545–556, March 2010.
- 51. Bagchi-Sen S., H.R. Rao, S. Upadhyaya and S. Chai, "Women in Cybersecurity: A Study of Career Advancement", *IEEE IT Professional*, Vol. 12, No. 1, January/February 2010, pp. 24-31.
- 52. Lee J.K., S. Bagchi-Sen, H.R. Rao and S. Upadhyaya, "The Anatomy of Information Security Workforce", *IEEE IT Professional*, Vol. 12, No. 1, January/February 2010, pp. 14-23.
- 53. Jadliwala M., S. Zhong, S. Upadhyaya and C. Qiao and Jean-Pierre Hubaux, "Secure Distance-based Localization in the Presence of Cheating Beacon Nodes", *IEEE Transactions on Mobile Computing*, Vol. 9, No. 6, June 2010, pp. 810-823.
- 54. Chen R., H.R. Rao, R. Sharman, S. Upadhyaya and J.K. Kim, "An Empirical Examination of IT-Enabled Emergency Response: The Cases of Hurricane Katrina and Hurricane Rita", *Communications of the Association for Information Systems*, Vol. 26, No. 8, January 2010, pp. 141-156.
- 55. Kim M., R. Sharman, C.P. Cook-Cottone, H.R. Rao and S. Upadhyaya, "Assessing Roles of People, Technology and Structure in Emergency Management Systems: A Public Sector Perspective", *Journal of Behaviour & Information Technology*, Wiley, Vol. 30, No. 2, February 2011.
- 56. Mehresh R., S. Upadhyaya and K. Kwiat, "Secure Proactive Recovery a Hardware Based Mission Assurance Scheme", Journal of Network Forensics, Springer, Vol. 3, Issue 2, Spring 2011, pp. 32-48.
- 57. Husain M., M. Chandrasekaran, S. Upadhyaya and R. Sridhar, "Cross-layer Soft Security Framework for Wireless Embedded Systems", *International Journal of Wireless Communications and Networking*, 3(2) Dec. 2011.
- 58. Chen R., R. Sharman, H.R. Rao and S. Upadhyaya, "Data Model Development for Fire Related Extreme Events: An Activity Theory Approach", *MIS Quarterly*, (37: 1) pp.125-147, 2013.

- 59. Valecha R., H.R. Rao, R. Sharman and S. Upadhyaya, "A Dispatch-Mediated Communication Model for Emergency Response Systems," *IEEE Transactions on Management Information Systems*, Vol. 4, No. 1, pp. 1-25, April 2013.
- 60. Duan Q., M. Virendra, S. Upadhyaya and A. Sanzgiri, "Minimum Cost Blocking Problem in Multi-path Wireless Routing Protocols", *IEEE Transactions on Computers*, Vol. 63, No. 7, pp. 1765-1777, July 2014.
- 61. Mehresh R. and S. Upadhyaya, "Surviving Advanced Persistent Threats in a Distributed Environment Architecture and Analysis", *Information Systems Frontiers*, Volume 17, Issue 5, pp. 987-995, October 2015.
- 62. Sun Y. and S. Upadhyaya, "Secure and Privacy Preserving Data Processing Support for Active Authentication", *Information Systems Frontiers*, Volume 17, Issue 5, pp. 1007-1015, October 2015.
- 63. Yuan S., H.R. Rao and S. Upadhyaya, "Emerging Issues for Education in E-discovery for Electronic Health Records", *Security Informatics*, December 2015, 4:3.
- 64. Kul G. and S. Upadhyaya, "Towards a Cyber Ontology for Insider Threats in the Financial Sector", *Journal of Wireless Mobile Networks, Ubiquitous Computing, and Dependable Applications (JoWUA)*, Volume 6, No. 4, pp. 64-85, December 2015.
- 65. Chakraborty R., S. Bagchi-Sen, J. Lee, H.R. Rao and S. Upadhyaya, "An Examination of Online Shopping Intention in the Context of the Data Breach in Online Retail Stores", *Decision Support Systems*, Volume 83, pp. 47-56, March 2016.
- 66. Kul G., D. Luong, T. Xie, V. Chandola, O. Kennedy and S. Upadhyaya, "Similarity Measures for SQL Query Clustering", *IEEE Transactions on Knowledge and Data Engineering*, Vol. 30, No. 12, December 2018.
- 67. Valecha R., H.R. Rao and S. Upadhyaya, "An Activity Theory Approach to Modeling Dispatch-Mediated Emergency Response", *Journal of the Association for Information Systems*, Vol. 20, No. 1, January 2019.
- 68. Amo L., R. Liao, E. Frank, H.R. Rao and S. Upadhyaya, "Cybersecurity Interventions for Teens: Two Time-Based Approaches", *IEEE Transactions on Education*, vol. 62, No. 2, pp.134-140, May 2019.
- 69. Sanders G.L., S. Upadhyaya and X. Wang, "Inside the Insider", *IEEE Engineering Management Review*, Vol. 47, No. 2, 2nd Quarter, pp. 84-91, June 2019.
- 70. Kul G., S. Upadhyaya and A. Hughes, "An Analysis of Complexity of Insider Attacks to Databases", *ACM Transactions on Management Information Systems*, Vol. 12, No. 1, pp. 1-18, March 2021.
- 71. Baksi R. and S. Upadhyaya, "Deception: a Theoretical Framework to Counter Advanced Persistent Threats", *Information Systems Frontiers*, Vol. 23, No. 4, August 2021, pp. 897-913.
- 72. Gaia J., G. Sanders, S. Sanders, S. Upadhyaya, X. Wang and C. Yoo, "Dark Traits and Hacking Potential", *Journal of Organizational Psychology*, Vol. 21(3), pp. 23-44, September 2021.
- 73. Amo L., J. Gaia, D. Murray, G.L. Sanders, S. Sanders, S. Upadhyaya, and X. Wang "Primary and Secondary Control as Antecedents to the Dark Traits in Predicting Attraction to Hacking Behavior", *Journal of Organizational Psychology*, Vol. 23(3), 2023.
- 74. Ma Z., X. Tan, L. Ziarek, N. Zhang, S. Upadhyaya, H. Hu and Z. Zhao, "MICROFT: Exploring and Mitigating Cross-state Control-flow Hijacking Attacks on ARM Cortex-M TrustZone", *IEEE Transactions on Information Forensics & Security*, 2025. (accepted)

REFEREED BOOK CHAPTERS

- 1. Upadhyaya S.J. and K.K. Saluja, "Signature techniques in fault detection and location", in M. Karpovski, Ed., *Spectral Techniques and Fault Detection*, Academic Press, pp. 421-476, 1985².
- 2. Upadhyaya S.J. and B. Ramamurthy, "A new efficient signature technique for process monitoring in critical systems", in R. Schlichting and J. Meyer, Eds., *Dependable Computing for Critical Applications* 2, Springer-Verlag, pp. 421-437, 1992.
- 3. Pham H. and S.J. Upadhyaya, "Reliability analysis of a class of fault tolerant systems", in H. Pham, Ed., *Fault Tolerant Software Systems: Techniques and Applications*, IEEE Computer Society Press, pp. 119-123, 1992.
- 4. Upadhyaya S.J. and H. Pham, "Computing reliability of noncoherent systems", in W. Kuo, Ed., *Quality Through Engineering Design*, Elsevier Science Publishers, pp. 285-306, 1993.
- 5. Upadhyaya S.J. and A. Ranganathan, "Rollback recovery in real-time concurrent systems", in T. Casavant and M. Singhal, Eds., *Readings in Distributed Computing Systems*, IEEE Computer Society Press, pp. 249-266, 1994.
- 6. Ramamurthy B. and S.J. Upadhyaya, "Hardware-assisted fast recovery in distributed systems", in R.K. Iyer, M. Morganti, W.K. Fuchs, V. Gilgor, Eds., *Dependable Computing for Critical Applications 5*, Comp. Society Press, pp. 223-241, 1998.
- 7. Upadhyaya, S.J., "Noise Generators", in John G. Webster, Ed., *Encyclopedia of Electrical and Electronics Engineering*, John Wiley Sons, Inc., Vol. 14, pp. 384-392, 1999. (revised in 2007)
- 8. Tien T.C. and S.J. Upadhyaya, "A local/global strategy based on signal strength for message routing in wireless mobile ad-hoc networks", in S. Upadhyaya, K. Kwiat, A. Chaudhuri and M. Weiser, Eds., *Mobile Computing: Implementing Pervasive Information and Communications Technologies*, Interfaces in Operations Research and Computer Science, Kluwer Academic Publishers Book Series, pp. 191-212, June 2002.
- 9. Upadhyaya S., K. Kwiat, R. Chinchani and K. Mantha, "Encapsulation of Owner's Intent A New Proactive Intrusion Assessment Paradigm", in Vipin Kumar, Jaideep Srivastava and Aleksandar Lazarevic, Eds., *Managing Cyber Threats: Issues, Approaches and Challenges*, Springer, pp. 221-245, 2005.
- 10. Upadhyaya S., H.R. Rao and G. Padmanabhan, "Secure Knowledge Management", in *David Swartz, Ed., Encyclopedia of Knowledge Management, IDEA Group*, pp. 795-801, 2005. (also reprinted in a 6-volume reference collection "Knowledge Management: Concepts, Methodologies, Tools, and Applications" by Murray E. Jennex, San Diego State University, 2008)
- 11. Sharman R., K. Pramod Krishna, H.R. Rao and S. Upadhyaya, "Malware and Antivirus Deployment for Enterprise Security", in *Enterprise Information Systems Assurance and Systems Security*, M. Warkentin and R. Vaughn (Eds.), Idea Group Publishing, Hershey, PA, 2006.
- 12. Ha D., S. Upadhyaya, H.Q. Ngo, S. Pramanik, R. Chinchani and S. Mathew, 2007, "Insider Threat Analysis Using Information-Centric Modeling", in *IFIP International Federation for Information Processing, Volume 242, Advances in Digital Forensics III: eds.* P. Craiger and S. Shenoi, (Boston: Springer), pp. 55-73. (invited paper at conference)
- 13. Chandrasekaran M., S. Vidyaraman and S. Upadhyaya, 2008, "Inferring Source of Information Leak in Document Management Systems", in *IFIP International Federation for Information Processing, Advances in Digital Forensics III: eds.* I. Ray and S. Shenoi, (Boston: Springer), pp. 291-306.
- 14. Herath T., H.R. Rao and S. Upadhyaya, "Internet Crime: How Vulnerable Are You? Do Gender, Social Influence and Education play a Role in Vulnerability?" in M. Gupta and R. Sharman, Eds.,

_

² Rated excellent in the Book Review section of IEEE Computer, June 1987.

- Handbook of Research on Social and Organizational Liabilities in Information Securities, IGI Global, pp. 433-445, 2009.
- 15. Chandrasekaran, M. and S. Upadhyaya, "A Multistage Framework to Defend Against Phishing Attacks", in M. Gupta and R. Sharman, Eds., *Handbook of Research on Social and Organizational Liabilities in Information Securities*, IGI Global, pp. 175-192, 2009.
- 16. Chen R, R. Sharman, H.R. Rao, S. Upadhyaya and C.P. Cook-Cottone, "Coordination of Emergency Response: An Examination of the Roles of People, Process, and Information Technology", in Bartel Van de Walle, Murray Turoff and Starr Roxanne Hiltz, eds., *Volume on Information Systems for Emergency Management*, Advances in Management Information Systems Monograph Series by Sharpe, 2010, pages 150-174.
- 17. Vidyaraman S., S. Upadhyaya and K. Kwiat, "Human Centered Security", in H.R. Rao and S. Upadhyaya, Eds., *Annals of Emerging Research in Information Assurance, Security and Privacy Services*, Elsevier, 2009.
- 18. Bhatnagar S., T. Herath, R. Sharman, H.R. Rao and S. Upadhyaya, "Web 2.0: Investigation of Issues for the Design of Social Networks", in *Web 2.0: The Business Models, Springer*, 2009.
- 19. Gupta, M., H.R. Rao and S. Upadhyaya (2009), "Security of Alternative Delivery Channels in Banking: Issues and Countermeasures", *Socioeconomic and Legal Implications of Electronic Intrusion*, Eds: Dionysios Politis, Phaedon Kozyris and Ioannis Iglezakis, IGI Publishing, Hershey, PA, USA.
- 20. Chinchani R., D. Ha, A. Iyer, H. Ngo and S. Upadhyaya, "Insider Threat Assessment: Model, Analysis and Tool", in *Network Security*, Scott Huang, David MacCallum, and Ding Zhu Du (Eds.), Springer, 2010, pp. 143-174.
- 21. Upadhyaya S., "Mandatory Access Control", in *Encyclopedia of Cryptography and Security*, Henk C.A. van Tilborg and Sushil Jajodia (eds.), Springer Science and Business Media LLC, 2011.
- 22. Garg A., S. Upadhyaya and K. Kwiat, "A User Behavior Monitoring and Profiling Scheme for Masquerade Detection", in *Handbook of Statistics Machine Learning, Vol. 31*, V. Govindaraju and C.R. Rao (eds.), Elsevier, 2013.
- 23. Mehresh R. and S. Upadhyaya, "Deception-Based Survivability", in *Secure System Design and Trustable Computing*, C.-H. Chang, M. Potkonjak (eds.), Springer International Publishing Switzerland, pp. 521-537, 2015.
- 24. Shu J., J. Rosenberg, S. Upadhyaya and H.R. Rao, "The Internet of Things (IoT) and IT Auditing", in *Internet of Things Concepts, Technologies, Applications, and Implementations*, Q. Hassan, A.R. Khan and S. Madani (eds.), CRC Press, 2018.
- 25. Baksi R. and S. Upadhyaya, A Game Theoretic Approach to the Design of Mitigation Strategies for Generic Ransomware, *in Information Systems Security and Privacy*, Gabriele Lenzini, Paolo Mori, and Steven Furnell, (eds.), Springer, 2023.

REFEREED CONFERENCES (full-length papers; acceptance rate indicated where available)

- 1. Upadhyaya S.J. and K.K. Saluja, "A hardware supported general rollback technique", *14th IEEE International Symposium on Fault Tolerant Computing*, Florida, pp. 409-414, June 1984. (accept. rate: 24%)
- 2. Saluja K.K. and S.J. Upadhyaya, "A divide and conquer strategy for testable design of PLAs, *4th Australian Microelectronics Conference*, Sydney, pp. 121-127, May 1985.
- 3. Saluja K.K. and S.J. Upadhyaya, "A Built-In Self Testing PLA design with high fault coverage", *IEEE International Conference on Computer Design*, Port Chester, NY, pp. 596-599, Oct. 1986.
- 4. Liu C.Y., K.K. Saluja and S.J. Upadhyaya, BIST-PLA: A built-in self-test design of programmable logic array, 24th ACM/IEEE Design Automation Conference, Miami Beach, Florida, pp. 385-391, June 1987.

- 5. Upadhyaya S.J. and M. Demjanenko, "On the repair of defective FPLAs to increase yield", *IEEE International Conference on Computer-Aided Design*, Santa Clara, CA, pp. 234-237, Nov. 1987. (accept. rate: 30%)
- 6. Chakravarty S. and S.J. Upadhyaya, "A unified approach to designing fault tolerant processor ensembles", *17th Annual International Conference on Parallel Processing*, St. Charles, Ill, pp. 339-342, Aug. 16-18, 1988.
- 7. Demjanenko M. and S.J. Upadhyaya, "Dynamic techniques for yield enhancement of field programmable logic arrays, *IEEE International Test Conference*, Washington D.C., pp. 485-491, Sept. 12-14, 1988. (accept. rate: 30%)
- 8. Upadhyaya S.J. and H. Pham, "Optimal design of k-to-l-out-of-n systems", *IASTED Int. Conf. on Quality Control and Reliability*, Los Angeles, CA, pp. 10-13, Dec. 1988.
- 9. Pham H. and S.J. Upadhyaya, "Reliability analysis of a class of fault tolerant systems", *IEEE Reliability and Maintainability Symposium*, Atlanta, GA, pp. 114-118, Jan. 1989.
- 10. Upadhyaya S.J., H. Pham and K.K. Saluja, "Reliability enhancement by submodule redundancy", 1990 Annual IEEE Reliability and Maintainability Symposium, Los Angeles, CA, pp. 127-132, Jan. 23-25, 1990.
- 11. Upadhyaya S.J. and S. Chakravarty, "An analysis of a fault tolerance scheme for processor ensembles", 23rd Annual Hawaii Conference on Systems and Sciences, Kona, Hawaii, pp. 101-110, Jan. 1990.
- 12. Chen Y.Y. and S.J. Upadhyaya, "An analysis of a reconfigurable binary tree architecture based on multiple-level redundancy", *20th Annual IEEE Int. Symposium on Fault Tolerant Computing*, Newcastle, England, pp. 192-199, June 1990. (accept. rate: 25%)
- 13. Upadhyaya S.J. and J.A. Thodiyil, "BIST PLAs, pass or fail A case study", *IEEE/ACM 27th Design Automation Conference*, Florida, pp. 724-727, June 1990.
- 14. Upadhyaya S.J., "Rollback recovery in real-time systems with dynamic constraints", *14th Annual IEEE International Computer Software Applications Conference*, Chicago, Ill., pp. 524-529, Nov. 1990.
- 15. Upadhyaya S.J. and Y.Y. Chen, "Yield and layout issues in fault tolerant VLSI architectures", *4th CSI/IEEE International Symposium on VLSI Design*, New Delhi, India, pp. 255-260, Jan. 1991.
- 16. Upadhyaya S.J. and B. Ramamurthy, "A new efficient signature technique for process monitoring in critical systems", 2nd IEEE/IFIP Working Conference on Dependable Computing for Critical Applications, Tucson, Arizona, pp. 178-185, Feb. 1991.
- 17. Kumar A. and S.J. Upadhyaya, "A framework for function-based diagnosis", *Proc. of the 1992 Golden West Int. Conf. on Intelligent Systems*, Reno, Nevada, pp. 73-78, June 1992.
- 18. Spina R. and S.J. Upadhyaya, "Fault diagnosis of analog circuits using artificial neural networks as signature analyzers", *Fifth Annual IEEE International ASIC '92 Conference*, Rochester NY, pp. 355-358, Sept. 1992.
- 19. Ranganathan A. and S.J. Upadhyaya, "Performance analysis of rollback recovery in process control systems", *3rd IEEE/IFIP Dependable Computing for Critical Applications Conference*, Mondello, Sicily, Sept. 1992. (accept. rate: 30%)
- 20. Ranganathan A., P.W. Dowd and S.J. Upadhyaya, "Effect of network delays on rollback recovery", 5th ISMM International Conference on Parallel and Distributed Computing and Systems, Pittsburgh, PA, pp. 193-198, Oct. 1992.
- 21. Ranganathan A. and S.J. Upadhyaya, "Performance evaluation of rollback recovery techniques in computer programs", *IASTED International Conference on Reliability, Quality Control and Risk Assessment*, Washington DC, pp. 239-242, Nov. 1992.

- 22. Upadhyaya S.J. and H. Pham, "Computing reliability of noncoherent systems", *Quality in Engineering Design Conference*, Bangalore, India, Jan. 1993.
- 23. Vidyaranya, S.J. Upadhyaya and A. Kundu, "A parallel VLSI implementation of Viterbi algorithm for accelerated word recognition", 3rd IEEE Great Lakes Symposium on VLSI, Kalamazoo, MI, pp. 37-41, Mar. 1993.
- 24. Ramamurthy B. and S.J. Upadhyaya, "Concurrent process monitoring in RISC based computer systems", *IASTED Conference on Reliability, Quality and Risk Assessment*, Boston, MA, pp. 117-120, Oct. 1993.
- 25. Upadhyaya S.J. and L.C. Chen, "On-chip test generation for combinational circuits by LFSR modification", *IEEE/ACM Int. Conference on Computer-Aided Design*, Santa Clara, CA, pp. 84-87, Nov. 1993. (accept. rate: 33%)
- 26. Ranganathan A. and S.J. Upadhyaya, "Simulation analysis of a dynamic strategy for checkpointing real-time systems", *27th IEEE Annual Simulation Symposium*, La Jolla, CA, pp. 181-187, April 1994.
- 27. Ranganathan A. and S.J. Upadhyaya, "An efficient communication support for watchdog processor assisted checkpointing in distributed applications", *ISSAT International Conference on Reliability and Quality in Design*, Seattle, WA, March 1994.
- 28. Kumar A. and S.J. Upadhyaya, "Component ontological representation of function for diagnosis", *10th IEEE Conference on Artificial Intelligence for Applications*, San Antonio, TX, pp. 448-454, March 1994.
- 29. Kumar A. and S.J. Upadhyaya, "Function Based Candidate Discrimination", *Proc. Seventh Florida AI Research Symposium*, Pensacola Beach, FL, pp. 245-249, May 1994.
- 30. Demjanenko M. and S.J. Upadhyaya, "Efficient reconfiguration techniques in processor arrays to enhance production yield", 2nd International Conference on Reliability, Maintainability and Safety, Beijing, China, pp. 301-305, June 1994.
- 31. Upadhyaya S.J. and I.S. Hwang, "A multi-level fault-tolerant mesh architecture with efficient spare utilization", 2nd International Conference on Reliability, Maintainability and Safety, Beijing, China, pp. 429-434, June 1994.
- 32. Ramamurthy B. and S.J. Upadhyaya, "Watchdog processor-assisted fast recovery in distributed systems", *Fifth IEEE/IFIP Working Conference on Dependable Computing for Critical Applications*, Urbana-Champaign, IL, pp. 125-134, Sept. 1995. (accept. rate: 30%)
- 33. Nachman L., K.K. Saluja, S.J. Upadhyaya and R. Reuse, "Random pattern testing of sequential circuits revisited", *IEEE Fault Tolerant Computing Symposium*, Sendai, Japan, June 1996. (accept. rate: 25%)
- 34. Goldberg S., S.J. Upadhyaya, and W.K. Fuchs, "Recovery schemes for mesh arrays utilizing dedicated spares", *IEEE Defect and Fault Tolerance Symposium*, Boston, MA, pp. 318-326, Nov. 1996.
- 35. Tsai T.K., S.J. Upadhyaya, H. Zhao, M.C. Hsueh and R.K. Iyer, "Path-Based Fault Injection", Invited Paper, *3rd ISSAT Conference on Reliability and Quality in Design*, Anaheim, CA, pp. 121-125, March 1997.
- 36. Ramamurthy B., S. Upadhyaya and R.K. Iyer, "An object-oriented test bed for the evaluation of checkpointing and recovery systems" *IEEE Int. Symposium on Fault Tolerant Computing*, Seattle, WA, pp. 194-203, June 1997. (accept. rate: 25%)
- 37. Zarrineh K., S. Upadhyaya, and P. Shephard III, "Automatic insertion of scan structures to enhance testability of embedded memories, cores and chips", *IEEE VLSI Test Symposium*, Monterey, CA, pp. 98-103, May 1998. (accept. rate: 25%)

- 38. Zarrineh K., S.J. Upadhyaya and S. Chakravarty, "A new framework for generation of optimal march tests for memory arrays", *IEEE International Test Conference*, Washington DC., pp. 73-82, October 1998. (accept. rate: 35%)
- 39. Ramamurthy B., S.J. Upadhyaya and B. Bhargava, "Design and analysis of a hardware-assisted checkpointing and recovery scheme for distributed applications", *IEEE 1998 Symposium on Reliable Distributed Systems*, West Lafayette, IN, pp. 84-90, October 1998. (accept. rate: 25%)
- 40. Zarrineh K. and S. Upadhyaya, "A design for test perspective on memory array synthesis", *IEEE International Symposium on Circuits and Systems*, Orlando, FL, May 1999.
- 41. Zarrineh K. and S. Upadhyaya, "On programmable memory built-in self test architectures", *IEEE Design Automation and Test in Europe 99*, Munich, Germany, pp. 708-713, March 1999. (accept. rate: 35%)
- 42. Nissar A. and S.J. Upadhyaya, "Fault diagnosis of mixed signal VLSI systems using artificial neural Networks", *IEEE Southwest Symposium on Mixed-Signal Design*, Tucson, AZ, pp. 93-98, April 1999.
- 43. Zarrineh K. and S.J. Upadhyaya, "A new framework for automatic generation, insertion and verification of memory Built-In Self Test units", *IEEE VLSI Test Symposium*, Dana Point, CA, pp. 391-396, April 1999. (accept. rate: 25%)
- 44. Zarrineh K. and S. Upadhyaya, "Programmable memory BIST and a new synthesis framework", *IEEE Fault Tolerant Computing Symposium*, Madison, WI, pp. 352-355, June 1999. (accept. rate: 23%)
- 45. Upadhyaya S.J., and K. Kwiat, "A distributed concurrent intrusion detection scheme based on Assertions", 1999 SCS Symposium on Performance Evaluation of Computer and Telecommunication Systems, Chicago, IL, pp. 369-376, July 1999.
- 46. Wu H. and S.J. Upadhyaya, "A chaining scheme to facilitate interactive functions and failure recovery in Video-on-Demand systems", *5th ISSAT International Conference on Reliability and Quality in Design*, Las Vegas, NE, pp. 32-36, August 1999.
- 47. Tien T.C. and S.J. Upadhyaya, "A local/global strategy based on signal strength for message routing in wireless mobile ad-hoc networks", *IEEE Academic Industry Working Conference on Research Challenges*, Buffalo, NY, April 2000. (Best student paper award)
- 48. Mantha K., R. Chinchani, S.J. Upadhyaya and K. Kwiat, "Simulation of intrusion detection in distributed systems", *SCS Summer Simulation Conference*, Vancouver, Canada, July 2000.
- 49. Hardekopf B., K. Kwiat and S. Upadhyaya, "Secure and fault-tolerant voting in distributed systems", 2001 IEEE Aerospace Conference, Big Sky, Montana, March 2001.
- 50. Xu M. and S. Upadhyaya, "An approach to secure communication in PCS", *IEEE Vehicular Technology Conference*, Rhodes Island, Greece, May 2001.
- 51. Hardekopf B., K. Kwiat and S. Upadhyaya, "Specification and verification of a secure distributed voting protocol", SCS Symposium on Performance Evaluation of Computer and Telecommunication Systems, July 2001.
- 52. Hardekopf B., K. Kwiat and S. Upadhyaya, "A decentralized voting algorithm for increasing dependability in distributed systems", *Joint Meeting of the 5th World Multiconference on Systemics Cybernetics and Informatics (SCI 2001)* and the 7th International Conference on Information Systems Analysis and Synthesis (ISAS 2001), Orlando, FL, July, 2001.
- 53. Upadhyaya S., R. Chinchani and K. Kwiat, "An analytical framework for reasoning about intrusions", *IEEE Symposium on Reliable Distributed Systems*, New Orleans, LA, pp. 99-108, October 2001. (accept. rate: 24%)

- 54. Nagaraj P., S. Upadhyaya, K. Zarrineh and D. Adams, "Defect analysis and realistic fault model extensions for multi-port SRAMs", *IEEE Defect and Fault Tolerance Symposium*, San Francisco, CA, October 2001 (Also appeared in 10th *IEEE North Atlantic Test Workshop*, Gloucester, MA, pp. 35-44, May 2001).
- 55. Zhao D., S. Upadhyaya and M. Margala, "Minimizing concurrent test time in SoCs by balancing resource usage", *Proceedings of the 12th ACM Great Lakes Symposium on VLSI*, pp. 77-82. April 2002. (accept. rate: 30%)
- 56. Tien T. and S. Upadhyaya, "Mobility modeling in dynamic mobile networks", *IEEE MILCOM* 2002, Anaheim, CA, October 2002.
- 57. Pramanik S. and S. Upadhyaya, "A new architectural approach for self-verification and fault tolerance of enterprise servers", *Parallel and Distributed Computing Systems*, Louisville, KY, September 2002.
- 58. Chinchani R., S. Upadhyaya and K. Kwiat, "Towards the scalable implementation of a user level anomaly detection system", *IEEE MILCOM 2002*, Anaheim, CA, October 2002.
- 59. Zhao D. and S. Upadhyaya, "Adaptive Test Scheduling in SoCs by Dynamic Partitioning", *IEEE Int'l Symposium on Defect and Fault Tolerance in VLSI Systems (DFT'02)*, pp. 334-342, November 2002.
- 60. Upadhyaya S., J.M. Lee and P. Nair, "Time Slot Specification Based Approach to Analog Fault Diagnosis Using Built-in Current Sensors and Test Point Insertion", *IEEE Asian Test Symposium*, Guam, pp. 429-434, Nov. 2002.
- 61. Zhao D. and S. Upadhyaya, "Power Constrained Test Scheduling with Dynamically Varied TAM", *IEEE VLSI Test Symposium*, pp. 267-272, April 2003. (accept. rate: 25%)
- 62. Zhao D. and S. Upadhyaya, "A Resource Balancing Approach to SoC Test Scheduling", *IEEE International Symposium on Circuits and Systems*, Bangkok, Thailand, May 2003.
- 63. Garg A. Shambhu Upadhyaya, Ramkumar Chinchani, Kevin Kwiat, SIMS: A Modeling and Simulation Platform for Intrusion Monitoring/Detection Systems", *Summer Computer Simulation Conference* 2003, Montreal, Canada, July 2003.
- 64. Pramanik S. and S. Upadhyaya, "VPSS: A Verifiable Proactive Secret Sharing Scheme in Distributed Systems", *IEEE MILCOM 2003*, Boston, MA, October 2003.
- 65. Zhao D., S. Upadhyaya, M. Margala, "Control Constrained Resource Partitioning for Complex SoCs", 18th IEEE International Symposium on Defect and Fault Tolerance in VLSI Systems, Cambridge, MA, November 2003.
- 66. Sharman R., H.R. Rao, S.J. Upadhyaya, P.P. Khot, S. Manocha and S. Ganguly, "Functionality defense by heterogeneity: a new paradigm for securing systems", *37th Hawaii International Conference on System Sciences (HICSS-37)*, January 2004.
- 67. Vikram A. S. Chennuru, H.R. Rao and S. Upadhyaya, "Comprehensive Account Activity Monitoring and Analysis Tool Using Neural Networks A Solution Architecture for Financial Institutions to Handle Illegal Activities", 37th Hawaii International Conference on System Sciences (HICSS-37), January 2004.
- 68. Sundararaman K., S. Upadhyaya and M. Margala, "Cost Model Analysis of DFT Based Fault Tolerant SOC Designs", *IEEE International Symposium on Quality Electronics Design*, March 2004. (accept. rate: 30%)
- 69. Pramanik S. and S. Upadhyaya, "RABIT: A New Framework for Runtime Emulation and Binary Translation", 37th IEEE Annual Simulation Symposium, Washington DC, April 2004.

- 70. Chinchani R., A. Iyer, B. Jayaraman and S. Upadhyaya, "ARCHERR: Architecture Driven Program Safety", 9th European Symposium on Research in Computer Security (ESORICS 2004), Institut Eurécom, Sophia Antipolis, French Riviera, France, September 2004. (accept. rate: 16%)
- 71. Sharman R., H.R. Rao and S. Upadhyaya, "Metrics for Information Security A Literature Review", *Proceedings of the 10th AIS Americas Conference on Information Systems*, New York City, NY, August 2004.
- 72. Virendra M. and S. Upadhyaya, "SWAN: A Secure Wireless LAN Architecture" 29th IEEE Conference on Local Computer Networks, Tampa, FL, November 2004. (accept. rate: 30%)
- 73. Pramanik S., S. Vidyaraman and S. Upadhyaya, "Security Policies to Mitigate Insider Threat in the Document Control Domain", 20th Annual Computer Security Applications Conference, Tucson, AZ, December, 2004. (accept. rate: 25%)
- 74. Chinchani R., A. Muthukrishnan, M. Chandrasekaran and S. Upadhyaya, "RACOON: Rapidly Generating User Command Data for Anomaly Detection from Customizable Templates", 20th Annual Computer Security Applications Conference, Tucson, AZ, December 2004. (accept. rate: 25%)
- 75. Virendra M., M. Jadliwala, M. Chandrasekaran and S. Upadhyaya, "Quantifying Trust in Mobile Ad-Hoc Networks", *IEEE International Conference on Integration of Knowledge Intensive Multi-Agent Systems (KIMAS 2005)*, Boston, MA, pp. 65-70, April 2005.
- 76. Zhao D., S. Upadhyaya, M. Margala, "A New SoC Test Architecture with RF/Wireless Connectivity", *European Test Symposium*, Tallin, Estonia, May 2005. (accept. rate: 30%)
- 77. Chen R., R. Sharman, H. R. Rao and S. Upadhyaya, "Design Principles of Coordinated Multi-Incident Emergency Response Systems", *IEEE Symposium on Intelligence and Security Informatics*, Atlanta, GA, May 2005.
- 78. Chinchani R., A. Iyer, H. Ngo and S. Upadhyaya, "Towards a Theory of Insider Threat Assessment", *IEEE International Conference on Dependable Systems and Networks (DSN 2005)*, Yokohama, Japan, June 2005. (accept. rate: 25%)
- 79. Kim J.K., R. Sharman, H.R. Rao and S. Upadhyaya, "Risk Assessment in the Context of Emergency Response Systems", *The 2nd Symposium on Risk-Management and Cyber-Informatics*, Orlando, FL, July 2005.
- 80. Kim J.K., R. Sharman, H.R. Rao and S. Upadhyaya, "An Investigation of Risk Management Issues in the Context of Emergency Response Systems", *American Conference on Information Systems (AMCIS 2005)*, Omaha, NE, pp. 3273-3282, Aug. 2005.
- 81. Bagchi-Sen S., J.K. Lee, H.R. Rao, and S. Upadhyaya, "A Framework for Examining Skill Specialization, Gender Inequity, and Career Advancement in the Information Security Field", *Ist International Conference on Information Systems Security*, Kolkata, India, Dec. 2005.
- 82. Kim J.K., R. Sharman, H.R. Rao and S. Upadhyaya, "Framework for Analyzing Critical Incident Management Systems", 39th Hawaii International Conference on System Sciences (HICSS-39), January 2006.
- 83. Garg A., Vidyaraman S., S. Upadhyaya and K. Kwiat, "USim: A User Behavior Simulation Framework for Training and Testing IDSes in GUI Based Systems", 39th Annual Simulation Symposium, Huntsville, AL, April 2006.
- 84. Vidyaraman S. and S. Upadhyaya, "A Trust Assignment Model based on Alternate Actions Payoff", 4th International Conference on Trust Management, Pisa, Italy, May 2006. (accept. rate: 30%)

- 85. Vidyaraman S., S. Pramanik and S. Upadhyaya, "Detecting Masquerading Users in a Document Management System", 2006 IEEE International Conference on Communications (ICC 2006), Istanbul, Turkey, June 2006. (accept. rate: 30%)
- 86. Chandrasekaran M., K. Narayanan and S. Upadhyaya, "Phishing E-mail Detection Based On Structural Properties", 9th Annual New York State Cyber Security Conference, Albany, NY, June 2006.
- 87. Sharman R., Jin Ki Kim, H. Raghav Rao and Shambhu Upadhyaya, "Delphi Study on Priorities Lessons Learned from the Gulf Coast", Invited Paper, 9th Annual New York State Cyber Security Conference, Albany, NY, June 2006.
- 88. Chai, S., S. Bagchi-Sen, R. Goel, H.R. Rao and S. Upadhyaya, "A Framework for Understanding Minority Students' Cyber Security Career Interests", 12th Americas Conference on Information Systems (AMCIS), August 2006.
- 89. Chakravarti N., R. Chen, R. Sharman, H.R. Rao and S. Upadhyaya, "Common Operating Vocabulary Extensions for Emergency Management: Case of a Chemical Spill Incident", *The 4th Annual Center for Advancing Business Through Information Technology (CABIT) Symposium*, Tempe, AZ, Sept. 2006.
- 90. Venugopal N., N. Shastry and S. Upadhyaya, "Effect of Process Variation on the Performance of Phase Frequency Detector", *IEEE Defect and Fault Tolerance Symposium*, Oct. 2006, pp. 525-534.
- 91. Liu L., R. Sridhar and S. Upadhyaya, "A 3-port Register File Design for Improved Fault Tolerance on Resistive Defects in Core-Cells", *IEEE Defect and Fault Tolerance Symposium*, Oct. 2006, pp. 545-553.
- 92. Vidyaraman S., S. Upadhyaya and K. Kwiat, "QoS-LI: QoS Loss Inference in Disadvantaged Networks", 2007 IEEE International Symposium on Ubisafe Computing (UbiSafe-07), Niagara Falls, Canada, May 2007.
- 93. Chandrasekaran M., M. Baig, and S. Upadhyaya, "AEGIS: A Proactive Methodology to Shield against Zero-day Exploits", 2007 IEEE International Symposium on Ubisafe Computing (UbiSafe-07), Niagara Falls, Canada, May 2007.
- 94. Chai S., R. Sharman, S. Patil, S. Satam, H.R. Rao and S. Upadhyaya, "Surface Transportation and Cyber Infrastructure, An Exploratory Study", *Intelligence and Security Informatics (ISI)*, New Brunswick, NJ, May 2007.
- 95. Duan Q., M. Virendra and S. Upadhyaya, "On the Hardness of Minimum Cost Blocking Attacks on Multi-path Wireless Routing Protocols", *IEEE International Conference on Communications* (*ICC 2007*), Glasgow, UK, June 2007. (accept. rate: 31%)
- 96. Vidyaraman S., M. Chandrasekaran and S. Upadhyaya, "Towards Modeling Trust Based Decisions: A Game Theoretic Approach", 12th European Symposium on Research in Computer Security (ESORICS 2007), Dresden, Germany, Sept. 2007. (accept. rate: 23%)
- 97. Jadliwala M., S. Upadhyaya and M. Taneja, "ASFALT: A Simple Fault-Tolerant Signature-based Localization Technique for Emergency Sensor Networks", *IEEE Symposium on Reliable Distributed Systems*, Beijing, China, October 2007 (accept. rate: 15%)
- 98. Zhong S., M. Jadliwala, S. Upadhyaya, C. Qiao, "Towards a Theory of Robust Localization against Malicious Beacon Nodes", *IEEE INFOCOM 2008*. (accept. rate: 21%)
- 99. Sharman R., R. Kasturi, S. Upadhyaya, I. Park, D. Velu and H. R. Rao, "Critical Risk Factors Affecting the Effectiveness of Public Health Information Systems Infrastructure", *INFORMS Annual Meeting*, Seattle, WA, November 2007.

- 100. Vidyaraman S., S. Upadhyaya and K. Kwiat, "QoS-LI: QoS Loss Inference in Disadvantaged Networks Part II", 11th Communications and Networking Simulation Symposium (CNS'08), Ottawa, Canada, April 2008.
- 101. Chandrasekaran M., S. Vidyaraman and S. Upadhyaya, "CUSP: Customizable and Usable Spam Filters for Detecting Phishing Emails", 11th Annual New York State Cyber Security Conference, Albany, NY, June 2008, pp. 10-16.
- 102. Gilbert B., R. Sharman, M. Gupta, H.R. Rao, S. Upadhyaya and K. Mortensen, "Content-sensitive, Temporally Adaptive Metadata", 11th Annual New York State Cyber Security Conference, Albany, NY, June 2008, pp. 47-52. (Best Paper Award)
- 103. Mathew S., S. Upadhyaya, D. Ha and H. Ngo, "Insider Abuse Comprehension through Capability Acquisition Graphs", 11th International Conference on Information Fusion, Cologne, Germany, July 2008.
- 104. Chakraborty R., S. Vidyaraman, H.R. Rao and S. Upadhyaya, "Mediated Internet Experience for Senior Citizens", *14th Americas Conference on Information Systems*, Toronto, Canada, August 2008.
- 105. Chen R., R. Sharman, H.R. Rao and S. Upadhyaya, "Information Theoretic Approach to Design of Emergency Response Systems", *Proceedings of the Fourteenth Americas Conference on Information Systems*, Toronto, ON, Canada August 14th-17th 2008.
- 106. Chai S., S. Bagchi-Sen, H.R. Rao and S. Upadhyaya, "Information Ethics and Privacy: An Explorative Study on Wired Senior Citizens", 10th ETHICOMP International Conference on the Social and Ethical Impacts of Information and Communication Technology, Mantua, Italy, September 2008.
- 107. Chen R., R. Sharman, H.R. Rao and S. Upadhyaya, "Data Model Development for Fire Related Extreme Events An Activity Theory and Semiotics Approach", *International Conference on Information Systems*, Paris, France, Dec. 2008.
- 108. Jadliwala M., Q. Duan, S. Upadhyaya and J. Xu, "Towards a Theory for Securing Time Synchronization in Wireless Sensor Networks", 2nd ACM Conference on Wireless Network Security (WiSec 2009), Zurich, Switzerland, March 2009.
- 109. Chandrasekaran M. and S. Upadhyaya, "AEGIS: A Pedagogical Tool for Vulnerability and Patch Management", *13th Colloquium for Information Systems Security Education*, Seattle, WA, June 2009.
- 110. Oh O., R. Chakraborty, H.R. Rao and S. Upadhyaya, "Exposure to Vulnerabilities through Google Hacking: An Exploratory Study Comparing U.S. and Indian Academic Institution", *Indo-US Conference on Cyber Security, Cybercrime and Cyber Forensics*, Kochi, India, August 2009.
- 111. Mathew S. and S. Upadhyaya, "Attack Scenario Recognition through Heterogeneous Event Stream Analysis", *IEEE MILCOM 2009*, Boston, MA, October 2009.
- 112. Oh O., R. Chakraborty, H.R. Rao and S. Upadhyaya, "An Exploration of Unintended Online Privacy Information Disclosure in Educational Institutions across Four Countries", *IEEE eCrime Researchers Summit*, Tacoma, WA, October 2009.
- 113. Katerinsky A., H.R. Rao and S. Upadhyaya, "Harsh Realities 101- Augmenting Information Assurance with Legal Curricula", 14th Colloquium for Information Systems Security Education, Baltimore, MD, June 2010.
- 114. Mathew S., M. Petropoulos, H.Q. Ngo and S. Upadhyaya, "A Data-Centric Approach to Insider Attack Detection in Database Systems", 13th International Symposium on Recent Advances in Intrusion Detection, Ottawa, Canada, September 2010.

- 115. Mathew S., S. Upadhyaya, M. Sudit and A. Stotz, "Situation Awareness of Multistage Cyber Attacks by Semantic Event Fusion", *IEEE MILCOM 2010*, October 2010.
- 116. Mehresh R., S. Upadhyaya and K. Kwiat, "Secure Proactive Recovery A Hardware Based Mission Assurance Scheme", *The 6th International Conference on Information-Warfare & Security*, Washington DC, March 2011.
- 117. Sanzgiri A. and S. Upadhyaya, "Feasibility of Attacks: What is Possible in the Real World A Framework for Threat Modeling", *International Conference on Security and Management (SAM)*, Las Vegas, NV, July 2011 (accept. rate: 23%).
- 118. Mehresh R., J. Rao, S. Upadhyaya, S. Natarajan and K. Kwiat, "Tamper-resistant Monitoring for Securing Multi-core Environments, *International Conference on Security and Management (SAM)*, Las Vegas, NV, July 2011 (accept. rate: 23%).
- 119. Salerno S., A. Sanzgiri and S. Upadhyaya, "Exploration of Attacks on Current Generation Smartphones", 8th International Conference on Mobile Web Information Systems (MobiWIS), Niagara Falls, ON, Canada, September 2011.
- 120. Valecha R., R. Sharman, H.R. Rao and S. Upadhyaya, "Emergency Response System Design: An Examination of Emergency Communication Messages", *Seventh International Conference on Design Science Research in Information Systems and Technology*, Lecture Notes in Computer Science, Volume 7286, 2012, pp. 139-146 May 2012.
- 121. Valecha R., R. Sharman, H.R. Rao and S. Upadhyaya, "Messaging Model for Emergency Communication", 7th Annual Midwest Association for Information Systems Conference, Green Bay, WI, May 2012.
- 122. Mehresh R. and S. Upadhyaya, "A Deception Framework for Survivability Against Next Generation Cyber Attacks", *International Conference on Security and Management (SAM'12)*, Las Vegas, NV, July 2012.
- 123. Joyce J., A. Sanzgiri and S. Upadhyaya, "The Early (tweet-ing) Bird Spreads the Worm: An Assessment of Twitter for Malware Propagation", 9th International Conference on Mobile Web Information Systems (MobiWIS), Niagara Falls, Ontario, Canada, August 2012.
- 124. Sanzgiri A., A. Nandugudi, S. Upadhyaya and C. Qiao, "SESAME: Smartphone Enabled Secure Access to Multiple Entities", *International Conference on Computing, Networking and Communications, Internet Services and Applications*, San Diego, CA, January 2013.
- 125. Valecha R., H.R. Rao, R. Sharman and S. Upadhyaya, "Collaboration Patterns in Local Emergency Communications: A "Gated" Language Action Perspective", *Eighth International Conference on Design Science Research in Information Systems and Technology*, Helsinki, Finland, June 2013.
- 126. Chakraborthy R., H.R. Rao, S. Upadhyaya, and S. Bagchi-Sen, "A Conceptual Examination of Distrusting Beliefs in Older Adults about the Internet", *19th Americas Conference on Information Systems*, Chicago, IL, Aug. 2013.
- 127. Sanzgiri A., A. Hughes, S. Upadhyaya, "Analysis of Malware Propagation in Twitter", 32nd IEEE International Symposium on Reliable Distributed Systems, Braga, Portugal, October 2013. (acceptance rate: 24%)
- 128. Valecha, R., M. Kashyap, S. Rajeev, H.R. Rao, and S. Upadhyaya, "An Activity Theory Approach to Specification of Access Control Policies in Transitive Health Workflows", *Proceedings of International Conference on Information Systems (ICIS)*, Auckland, New Zealand, December 2014.

- 129. Sun Y. and S. Upadhyaya, "Secure and Privacy Preserving Data Processing Support for Active Authentication", 6th International Conference of Secure Knowledge Management in Bigdata Era, Dubai, UAE, December 2014.
- 130. Mehresh R. and S. Upadhyaya, "A Framework for Surviving Advanced Persistent Threats in a Distributed Environment", 6th International Conference of Secure Knowledge Management in Big-data Era, Dubai, UAE, December 2014 (best paper award).
- 131. Mehresh R. and S. Upadhyaya, "Surviving Advanced Persistent Threats A Framework and Analysis", PhD Colloquium, 10th International Conference on Cyber Warfare and Security (ICCWS), Kruger National Park, South Africa, March 2015.
- 132. Ceker H. and S. Upadhyaya, "Enhanced Recognition of Keystroke Dynamics Using Gaussian Mixture Models", *IEEE MILCOM 2015*, Tampa, Florida, October 2015.
- 133. Kul, G., D. Luong, T. Xie, P. Coonan, V. Chandola, O. Kennedy and S. Upadhyaya, ""Ettu: Analyzing Query Intents in Corporate Databases", *Proceedings of the 25th International Conference Companion on World Wide Web*, April 2016, pp. 463-466.
- 134. Biswas T., A. Sanzgiri and S. Upadhyaya, "Building Long Term Trust in Vehicular Networks", *IEEE 83rd Vehicular Technology Conference (VTC)*, Nanjing, China, May 2016.
- 135. Spaulding J., A. Mohaisen and S. Upadhyaya, "The Landscape of Domain Name Typosquatting: Techniques and Countermeasures", 11th International Conference on Availability, Reliability and Security (ARES), Salzburg, Austria, Aug. 2016. (accept. rate: 30%)
- 136. Ceker H. and S. Upadhyaya, "User Authentication with Keystroke Dynamics in Long-Text Data," 8th IEEE International Conference on Biometrics: Theory, Applications, and Systems (BTAS), Niagara Falls, NY, September 2016. (accept. rate: 40%)
- 137. Ceker H. and S. Upadhyaya, "Adaptive Techniques for Intra-User Variability in Keystroke Dynamics", 8th IEEE International Conference on Biometrics: Theory, Applications, and Systems (BTAS), Niagara Falls, NY, September 2016. (accept. rate: 40%)
- 138. Ceker H., J. Zhuang, S. Upadhyaya, Q.D. La and B.H. Soong, "Deception-based Game Theoretical Approach to Mitigate DoS Attacks", *Conference on Decision and Game Theory for Security, GameSec 2016*, New York City, NY, November 2016.
- 139. Ceker H. and S. Upadhyaya, "Transfer Learning in Long-Text Keystroke Dynamics", *IEEE International Conference on Identity, Security and Behavior Analysis (ISBA)*, New Delhi, India, February 2017.
- 140. Sun Y., H. Ceker and S. Upadhyaya, "Anatomy of Secondary Features in Keystroke Dynamics Achieving More with Less", *IEEE International Conference on Identity, Security and Behavior Analysis (ISBA)*, New Delhi, India, February 2017.
- 141. Valecha, R., R. Chakraborty, H.R. Rao, and S. Upadhyaya, "A Prediction Model of Privacy Control for Online Social Networking Users", 13th International Conference on Design Science Research in Information Systems and Technology (DESRIST 2018), Chennai, India, June 2018. (Best paper award)
- 142. Sun Y. and S. Upadhyaya, "Synthetic Forgery Attack against Continuous Keystroke Authentication Systems", *IEEE 27th International Conference on Computer Communications and Networks*, Hangzhou, China, July-Aug. 2018. (accept. rate: 29%)
- 143. Baksi R. and S. Upadhyaya, "A Comprehensive Model for Elucidating Advanced Persistent Threats (APT)", *Int'l Conf. on Security & Management (SAM'18)*, Las Vegas, NV, July-Aug. 2018.
- 144. Kul G., S. Upadhyaya and V. Chandola, "Detecting Data Leakage from Databases on Android Apps with Concept Drift", *Proceedings of the 17th IEEE International Conference On*

- Trust, Security and Privacy in Computing and Communications (IEEE TrustCom 18), New York, NY, August 2018.
- 145. Baksi R. and S. Upadhyaya, "Deception: A Hidden Markov Model Approach to Counter Advanced Persistent Threats", 8th International Conference on Secure Knowledge Management in the Artificial Intelligence Era, Goa, India, December 2019.
- 146. Gaia J., B. Ramamurthy, G. Sanders, S. Sanders, S. Upadhyaya, X. Wang and C. Yoo, "Psychological Profiling of Hacking Potential", *Hawaii International Conference on System Sciences (HICSS-53)*, Grand Wailea, HI, January 2020.
- 147. Gaia J., D. Murray, G. Sanders, S. Sanders, S. Upadhyaya, X. Wang and C. Yoo, "The Interaction of Dark Traits with the Perceptions of Apprehension", *Hawaii International Conference on System Sciences (HICSS-55)*, Virtual, January 2022.
- 148. Baksi R. and S. Upadhyaya, "Game Theoretic Analysis of Ransomware: A Preliminary Study", 8th International Conference on Information Systems Security and Privacy (ICISSP), Virtual, pages 242-251, February 2022.
- 149. Baksi R., V. Nalka and S. Upadhyaya, Apt Detection of Ransomware An Approach to Detect Advanced Persistent Threats Using System Call Information, IEEE 22nd International Conference on Trust, Security and Privacy in Computing and Communications (TrustCom), Exeter, UK, November 2023.
- 150. Amo L., J. Gaia, D. Murray, G. Sanders, S. Sanders, R. Singh and S. Upadhyaya, "Economic Incentives and Perceptions as Critical Factors for Understanding Insider Hacking" Hawaii International Conference on System Sciences (HICSS-57), January 2024.
- 151. Zhang Y., L. Amo, D. Cichocki, J. Gaia, D. Murray, S. Sanders, S. Upadhyaya and G. Sanders, "Calm, Focused, and Potentially Dangerous- Unpacking the Role of Mindfulness in Cyber Hacking", *Decision Science Institute* 56th Annual Conference, Orlando, FL, November 2025.

REFEREED WORKSHOPS (full-length papers)

- 1. Upadhyaya S.J. and K.K. Saluja, "Is fault location possible using linear feedback shift registers", *An International Workshop on Fault Detection and Spectral Techniques*, Boston, pp. 2-1 to 2-9, Oct. 1983.
- 2. Kumar A. and S.J. Upadhyaya, "Automating representation", *AAAI Workshop on Model-based Reasoning*, Boston, MA, pp. 124-130, July 1990.
- 3. Chen Y.Y. and S.J. Upadhyaya, "A new approach to modeling the performance of a class of fault tolerant VLSI/WSI systems based on multiple-level redundancy", *IEEE Int'l Workshop on Defect/Fault Tolerance in VLSI Systems*, Hidden Valley, PA, pp. 157-160, Nov. 1991.
- 4. Kumar A. and S.J. Upadhyaya, "Abstraction and approximation in diagnostic reasoning", *AAAI Workshop on Abstraction and Approximation*, San Jose, CA, pp. 124-131, July 1992.
- 5. Goldberg S. and S.J. Upadhyaya, "Implementation of a gracefully degradable binary tree in programmable multi-chip modules", *IEEE Int. Workshop on Defect and Fault Tolerance in VLSI Systems*, Montreal, Canada, pp. 28-36, October 1994.
- 6. Goldberg S. and S.J. Upadhyaya, "Utilizing spares in multichip modules for the dual function of fault coverage and fault diagnosis", *IEEE Defect and Fault Tolerance Workshop*, Lafayette, LA, pp. 234-242, Nov. 1995.
- 7. Spina R. and S.J. Upadhyaya, "Concurrent test versus design for test resources in mixed-signal circuits", 2nd IEEE International Mixed-Signal Testing Workshop, Quebec City, Canada, pp. 173-180, May 1996.

- 8. Zarrineh, K. and S. Upadhyaya, "A new approach to programmable memory built-in self-test scheme", *IEEE International Workshop on Embedded Fault-Tolerant Systems*, Boston, pp. 128-133, May 1998.
- 9. Upadhyaya S., "A generalized hold method for random pattern testing of sequential circuits and its BIST implications", 9th IEEE North Atlantic Test Workshop, Gloucester, MA, May 2000.
- 10. Nagaraj P., S. Upadhyaya, K. Zarrineh and D. Adams, "Defect analysis and realistic fault model extensions for multi-port SRAMs", *10th IEEE North Atlantic Test Workshop*, Gloucester, MA, pp. 35-44, May 2001.
- 11. Upadhyaya S., R. Chinchani and K. Kwiat, "A comprehensive reasoning framework for information survivability", 2nd Annual IEEE Systems, Man, and Cybernetics Information Assurance Workshop, West Point, NY, pp. 148-155, June 2001.
- 12. S. Upadhyaya and J.M. Lee, "Fault diagnosis of analog VLSI circuits using time slot specification based built-in sensors A case study", 11th IEEE North Atlantic Test Workshop, Montauk, NY, May 2002.
- 13. Zhao D. and S. Upadhyaya, "Dynamically partitioned test scheduling for SoCs under power constraints", 11th IEEE North Atlantic Test Workshop, Montauk, NY, May 2002.
- 14. Chinchani R., S. Upadhyaya and K. Kwiat, "A Tamper-Resistant Framework for Unambiguous Detection of Attacks in User Space Using Process Monitors", *IEEE International Workshop on Information Assurance*, Darmstadt, Germany, pp. 25-34, March 2003. (accept. rate: 20%)
- 15. Zhao D., S. Upadhyaya and M. Margala, "A New Distributed Test Control Architecture with Multihop Wireless Test Connectivity and Communication for GigaHertz Systems-on-Chip", *12th IEEE North Atlantic Test Workshop*, Montauk, NY, May 2003. (Best student paper award)
- 16. Gupta M., S. Banerjee, H.R. Rao and S. Upadhyaya, "Intrusion Countermeasures Security Model Based on Prioritization Scheme for Intranet Access", *4th Annual IEEE Information Assurance Workshop*, West Point, NY, June 2003.
- 17. Iyer A., R. Chinchani, B. Jayaraman and S. Upadhyaya, "Insecure Programming: How Culpable is a Language's Syntax?", *4th Annual IEEE Information Assurance Workshop*, West Point, NY, June 2003.
- 18. Sundararaman K., S. Narayanan and S. Upadhyaya, "Defect Analysis and Fault Model Extensions for RF Circuits", 13th IEEE North Atlantic Test Workshop, Essex Junction, VT, pp. 64-70, May 2004.
- 19. Kadiyala A. and S. Upadhyaya, "Revisiting LFSRs and its Role in BIST Applications Two New Results", 13th IEEE North Atlantic Test Workshop, Essex Junction, VT, pp. 103-108, May 2004
- 20. Garg A., S. Pramanik, S. Vidyaraman and S. Upadhyaya, "Dynamic Document Reclassification for Preventing Insider Abuse", *Proc. of the 2004 IEEE Workshop on Information Assurance*, West Point, NY, pp. 218-225, June 2004.
- 21. Gummadidala R., C. Qiao and S. Upadhyaya, "WDP: A Wormhole Discovery Protocol for Mobile Ad Hoc Networks", *International Workshop on Mobile and Wireless Ad Hoc Networking*, Las Vegas, NV, June 2004.
- 22. Virendra M. and S. Upadhyaya, "Securing Information Through Trust Management in Wireless Networks", *NSF/ARFL Sponsored Workshop on Secure Knowledge Management*, Buffalo, NY, pp. 201-206, September 2004.
- 23. Virendra M., S. Upadhyaya, V. Kumar and V. Anand, "A Survivable Architecture for Wireless LANs", *IEEE Int. Workshop on Information Assurance*, Washington DC, pp. 71-82, March 2005. (accept. rate: 33%)

- 24. Mathew S., C. Shah and S. Upadhyaya, "An Alert Fusion Framework for Situation Awareness of Multistage Coordinated Attacks", *IEEE International Workshop on Information Assurance*, Washington DC, March 2005. (accept. rate: 33%)
- 25. Mathew S., D. Britt, R. Giomundo, S. Upadhyaya, M. Sudit and A. Stotz, "Real-time Multistage Attack Awareness Through Enhanced Intrusion Alert Clustering", *SIMA 2005*, in conjunction with IEEE MILCOM 2005, Atlantic City, NJ, October 2005.
- 26. Jadliwala, M., S. Upadhyaya, H.R. Rao and R. Sharman, "Security and Dependability Issues in Location Estimation for Emergency Sensor Networks", 4th Workshop on e-Business (WeB 2005), Las Vegas, NV, December 2005.
- 27. Chandrasekaran M., M. Baig, and S. Upadhyaya, "AVARE: Aggregated Vulnerability Assessment and Response against Zero-day Exploits, *Ist International Swarm Intelligence & Other Forms of Malware Workshop (Malware'06)*, Phoenix, AZ, April 2006.
- 28. Garg A., Ragini Rahalkar, Shambhu Upadhyaya and Kevin Kwiat, "Profiling Users in GUI Based Systems Masquerade Detection", 7th IEEE Information Assurance Workshop, West point, NY, June 2006.
- 29. Ramaswamy S. and S. Upadhyaya, "Smart Handling of Colluding Black Hole Attacks in MANETs and Wireless Sensor Networks using Multipath Routing", 7th IEEE Information Assurance Workshop, West point, NY, June 2006.
- 30. Chandrasekaran M., R. Chinchani and S. Upadhyaya, "Phoney: Mimicking User Response to Detect Phishing Attacks", 2nd International Workshop on Trust, Security and Privacy in Ubiquitous Computing (TSPUC 2006), Niagara Falls, NY, June 26, 2006.
- 31. Mathew S., R. Giomundo, S. Upadhyaya, M. Sudit and A. Stotz, "Understanding Multistage Attacks by Attack-Track Based Visualization of Heterogeneous Event Streams", *ACM VizSec Workshop* in conjunction with ACM CCS, Nov. 2006. (accept. rate: 33%)
- 32. Garg A., S. Upadhyaya and K. Kwiat, "Attack Simulation for Measuring Detection Model Effectiveness", 2nd Workshop on Secure Knowledge Management, Brooklyn, NY, Sept. 2006.
- 33. Meka K.D., M. Virendra and S. Upadhyaya, "Trust Based Routing Decisions in Mobile Ad-Hoc Networks", 2nd Workshop on Secure Knowledge Management, Brooklyn, NY, Sept. 2006.
- 34. Chandrasekaran M., S. Vidyaraman and S. Upadhyaya, "SpyCon: Emulating User Activities to Detect Evasive Spyware", 2nd International Swarm Intelligence & Other Forms of Malware Workshop (Malware'07), New Orleans, LA, April 2007. (Best paper award)
- 35. Virendra M., A. Krishnamurthy, K. Narayanan, S. Upadhyaya and K. Kwiat, "Environment-Aware Trusted Data Delivery in Multipath Wireless Protocols", *International Workshop on Mathematical Methods, Models and Architectures for Computer Networks Security (MMM-ACNS-2007)*, St. Petersburg, Russia, pp. Sept. 2007.
- 36. Vidyaraman S., M. Chandrasekaran and S. Upadhyaya, "The User is the Enemy", 14th New Security Paradigms Workshop, White Mountain Hotel and Resort, New Hampshire, September 2007.
- 37. Husain M., S. Upadhyaya and M. Chandrasekaran, "A Novel Approach for Security and Robustness in Wireless Embedded Systems", 6th IFIP Workshop on Software Technologies for Future Embedded and Ubiquitous Systems (SEUS), Capri Island, Italy, October 2008.
- 38. Chai S., S. Bagchi-Sen, H.R. Rao and S. Upadhyaya, "An Exploratory Study of Women Information Security Professionals: Motivation, Skills and Barriers", 3rd Secure Knowledge Management Workshop, Dallas, TX, pp. 25-28, Nov. 2008.

- 39. Park I., R. Sharman, H.R. Rao and S. Upadhyaya, "The Mediating Effect of Information Assurance on Information Infrastructure Performance", 3rd Secure Knowledge Management Workshop, Dallas, TX, pp. 79-85, Nov. 2008.
- 40. Gupta P, A. Nagrale and S. Upadhyaya, "Accelerating Techniques for Rapid Mitigation of Phishing and Spam Emails", 1st Workshop on Embedded Systems and Communications Security, in conjunction with IEEE SRDS 2009, Niagara Falls, NY, Sept. 2009.
- 41. Parameswaran A., M. Husain and S. Upadhyaya, "Is RSSI a Reliable Parameter in Sensor Localization Algorithms An Experimental Study", *1st Workshop on Field Failure Data Analysis, in conjunction with IEEE SRDS 2009*, Niagara Falls, NY, Sept. 2009.
- 42. Chakraborty R., H.R. Rao and S. Upadhyaya, "BANDES: An Adaptive Decision Support System for Protecting Online Privacy for Senior Citizen Centers", *Fourth Pre-ICIS Workshop on Information Security and Privacy (WISP)*, WISP 2009, Phoenix, AZ, December 2009.
- 43. Vidyaraman S., S. Upadhyaya and K. Kwiat, "QoS-T: QoS Throttling to Elicit User Cooperation in Computer Systems", *International Workshop on Mathematical Methods, Models and Architectures for Computer Networks Security (MMM-ACNS-2010)*, St. Petersburg, Russia, Sept. 2010.
- 44. Nataraj P., H.P. Nagarajaiah and S. Upadhyaya, "Informed Route Selection in Ad-Hoc Wireless Networks Based on Trust and Behavior", 4th Workshop on Secure Knowledge Management, Rutgers University, NJ, October 2010 (Best Student Paper Award).
- 45. Chakraborty R., H.R. Rao and S. Upadhyaya, "Evaluation of Effectiveness of Cyber security Intervention Programs for Older Adults", 4th Workshop on Secure Knowledge Management, Rutgers University, NJ, October 2010.
- 46. Mehresh R., S. Upadhyaya and K. Kwiat, "A Multi-Step Simulation Approach Toward Secure Fault Tolerant System Evaluation", 3rdInternational Workshop on Dependable Network Computing and Mobile Systems (DNCMS 2010), in conjunction with IEEE SRDS 2010, New Delhi, India, October 2010.
- 47. Keshavamurthy V., S. Upadhyaya and V. Gopal, "Accelerated Processing of Secure Email by Exploiting Built-in Security Features on the Intel® EP80579 Integrated Processor with Intel® QuickAssist Technology", 4th International Workshop on Dependable Network Computing and Mobile Systems (DNCMS 2011), in conjunction with IEEE SRDS 2011, Madrid, Spain, October 2011.
- 48. Nagarajaiah H., S. Upadhyaya and V. Gopal, "Data De-duplication and Event Processing for Security Applications on an Embedded Processor", 5th International Workshop on Dependable Network Computing and Mobile Systems (DNCMS 2012), in conjunction with IEEE SRDS 2012, Irvine, CA, October 2012.
- 49. Valecha R., S. Upadhyaya, H.R. Rao and A. Keepanasseril, "An Activity Theory Approach to Leak Detection and Mitigation in Personal Health Information (PHI)", AIS Special Interest Group on Information Security and Privacy (SIGSEC) Workshop on Information Security and Privacy (WISP), Orlando, FL, Dec. 2012.
- 50. Chakraborty R., S. Bagchi-Sen, H.R. Rao and S. Upadhyaya, "An Exploration of Security and Privacy Behavior of Elders on the Internet and comparison with Younger Adults", AIS Special Interest Group on Information Security and Privacy (SIGSEC) Workshop on Information Security and Privacy (WISP), Orlando, FL, Dec. 2012.
- 51. Kisekka V., R. Sharman, H.R. Rao, S. Upadhyaya and N. Gerber, "Investigating the Antecedents of Organizational Resilience in Hospitals", 5th Annual Workshop on Health IT and Economics (WHITE), Alexandria, VA, October 2014.

- 52. Kul G. and S. Upadhyaya, "A Preliminary Cyber Ontology for Insider Threats in the Financial Sector", 7th ACM CCS International Workshop on Managing Insider Security Threats (In Conjunction with ACM CCS 2015), Denver, CO, Oct. 2015.
- 53. Tong M., A. Sanzgiri, S. Upadhyaya and D. Koutsonikolas, "Twitter Structure as a Composition of Two Distinct Networks", *International Conference on Computing, Networking and Communications (ICNC), Workshop on Computing, Networking and Communications (CNC)*, Kauai, Hawaii, USA, Feb. 2016.
- 54. Sun Y., H. Ceker and S. Upadhyaya, "Shared Keystroke Dataset for Continuous Authentication", 8th IEEE International Workshop on Information Forensics and Security (WIFS 2016), Abu Dhabi, UAE, December 2016.
- 55. Kul G., S. Upadhyaya and A. Hughes, "Complexity of Insider Attacks to Databases", 9th ACM CCS International Workshop on Managing Insider Security (MIST 2017), Dallas, TX, October 2017.
- 56. Baksi R. and S. Upadhyaya, "Kidemonas: The Silent Guardian", 7th Workshop on Secure Knowledge Management, Tampa, FL, October 2017.
- 57. Amo L., R. Lio, D. Murray, K. Cleary, E. Frank, A. Mazzei, S. Upadhyaya and H.R. Rao, "Cybersecurity Interventions for Young Adults", 7th Workshop on Secure Knowledge Management, Tampa, FL, October 2017.
- 58. Ceker H. and S. Upadhyaya "Sensitivity Analysis in Keystroke Dynamics using Convolutional Neural Networks", *IEEE Workshop on Information Forensics and Security (WIFS) 2017*, Rennes, France, December 2017.

POSTERS (Refereed and non-refereed)

- 1. Ramamurthy B. and S.J. Upadhyaya, "A controllable signature checkpointing scheme for transient error detection", *Phoenix Conference on Computers and Communications*, Scottsdale, Arizona, pp. 899-900, Mar. 1990 (refereed).
- 2. Upadhyaya S., "Attack recognition in distributed systems by assertion checking", poster presentation, *Information Institute-SAB reception*, Rome Labs, Rome, NY, Dec. 6, 1999.
- 3. Upadhyaya S., "Attack recognition and shielding in distributed information systems", poster presentation, Information *Institute-SAB reception*, Rome Labs, Rome, NY, Nov. 5, 2001.
- 4. Upadhyaya S., "A Tamper-Resistant Framework for Unambiguous Detection of Attacks in User Space Using Process Monitors", *1st New York State Cyber Security Conference*, Utica, NY, Feb. 2003.
- 5. Upadhyaya S., R. Chinchani and K. Kwiat, "New methods for attack detection", *AFRL Information Institute Workshop*, Rome, NY, June 2003.
- 6. Virendra M., S. Upadhyaya and X. Wang, "GSWLAN: A New Architecture Model for a Generic and Secure Wireless LAN System", *Proc. of the 2004 IEEE Workshop on Information Assurance*, West Point, NY, pp. 434-435, June 2004 (refereed).
- 7. Sharman, R., H. Challapalli, H.R. Rao and S. Upadhyaya, "A Framework for a Secure Federated Patient Healthcare System," *Proceedings of Symposium on Intelligence and Security Informatics*, Springer-Verlag Lecture Notes in *Computer Science*, June 2004 (refereed).
- 8. Virendra M. and S. Upadhyaya, "Ensuring Secure Information Management in 802.11 Networks after a Central Authority Failure", *NSF/ARFL Sponsored Workshop on Secure Knowledge Management*, Buffalo, NY, pp. 255-256, September 2004 (refereed).
- 9. Ha, D., S. Pramanik, H. Ngo and S. Upadhyaya, "A System for Assessment, Detection and Analysis of Insider Threats", *Insider Threat Seminar, United States Secret Services*, Center for the Arts, University at Buffalo, March 2006.

- 10. Kumaraguru P., S. Upadhyaya and H.R. Rao, "Protecting Senior Citizens from Cyber Security Attacks in the e-Health Scenario: An International Perspective", 6th Annual Cyber Security and Information Intelligence Research Workshop (CSIIRW), Oakridge, TN, April 2010 (refereed).
- 11. Chen R., H.R. Rao, R. Sharman, S. Upadhyaya and C. Cook-Cottone, "Examination of Emergency Response from Knowledge and Psychology Perspectives", 7th International Conference on Information Systems for Crisis Response and Management, Seattle, WA, May 2010 (refereed).
- 12. Keshavamurthy V., S. Upadhyaya and V. Gopal, "Accelerated Processing of Secure Email by Exploiting Built-in Security Features on the Intel EP80579 Integrated Processor with Intel QuickAssist Technology", 2011 Intel Embedded Research & Education Summit, Chandler, AZ, Feb. 2011.
- 13. Crawford C. and S. Upadhyaya, "Implementing a Black Hole Attack in Open 802.11s A Computer Security Project Experience", 15th Colloquium for Information Systems Security Education, Fairborn, OH, June 2011 (refereed).
- 14. Kaputa D., S. Upadhyaya and H.R. Rao, "Computer Security and Investigations: An Integrative Approach to Curriculum Development in Digital Forensics", *Broadening Impact: NSF-funded Projects at Two-Year Colleges Conference*, Washington, D.C., June 16-17, 2011.
- 15. Chandola V., O. Kennedy, H.Q. Ngo and S. Upadhyaya, "Data is Social: Exploiting Data Relationships to Detect Insider Attacks", *Secure and Trustworthy Cyberspace PI Meeting*, Washington DC, January 2015.
- 16. Kisekka V., R. Sharman, H.R. Rao, S. Upadhyaya and N. Gerber, "Investigating the Antecedents of Healthcare Workers' Perceptions of Organizational Resilience in Hospitals", *International Conference on Information Systems (ICIS)*, Fort Worth, TX, December 2015 (refereed).
- 17. Spaulding J., S. Upadhyaya and A. Mohaisen, "You've Been Tricked! A User Study of the Effectiveness of Typosquatting Techniques", 37th IEEE International Conference on Distributed Computing Systems (ICDCS 2017), Atlanta, GA, June 2017 (refereed).
- 18. Amo L., J. Gaia, S. Aluri, J. Higgins, D. Murray, K. Cleary and S. Upadhyaya, "Addressing the Cybersecurity Talent Gap through a Serious Games Approach with Young Adults", 28th Workshop on Information Technologies and Systems (WITS), Demo/prototype, San Francisco, CA, December 2018 (refereed).
- 19. Baksi R. and S. Upadhyaya, "A Strategic Framework for Mitigating Advanced Persistent Threats: A Hidden Markov Model Approach", The 2nd Conference on Risk Analysis, Decision Analysis and Security, Niagara Falls, NY, July 2019 (refereed).
- 20. Amo, L., J. Gaia, D. Murray, G.L. Sanders, S. Sanders, S. Upadhyaya, and X. Wang, "Primary and Secondary Control as Antecedents to the Dark Traits in Predicting Attraction to Hacking Behavior", 32nd Workshop on Information Technologies and Systems (WITS 2022), December 2022 (refereed).

TECHNICAL REPORTS AND EDUCATIONAL CONFERENCES

- 1. Upadhyaya S.J., R. Sridhar and R. Acharya, "Development of an interactive facility for VLSI and image processing instruction", *Proceedings of the Conference on Innovations in Instructional Computing*, Saratoga Springs, NY, pp. 107-111, Apr. 1989.
- 2. Chen J., J. Choi, J. Geller, A. Kumar, M.R. Taie, S.C. Shapiro, S.N. Srihari and S.J. Upadhyaya, "VMES: A Versatile Maintenance Expert System", *Technical Report 90-06*, Dept. of Computer Science, SUNY/Buffalo, April 1990.
- 3. Kumar A. and S.J. Upadhyaya, "Framework for Function Based Diagnosis", Technical *Report* 92-19, Dept. of Computer Science, SUNY/Buffalo, August 1992.

- 4. Upadhyaya S. and R. Reuse, "A New Test Generation Methodology for Built-In Self-Test of Sequential Circuits", *Workshop on Academic Electronics in New York State*, Syracuse, New York, pp. 185-190, June 13-14, 1996.
- 5. Goldberg S. and S. Upadhyaya, "Reconfiguration Scheme for Fault-Tolerant Processing Arrays Packaged in Multichip Modules", *Workshop on Academic Electronics in New York State*, Syracuse, New York, pp. 265-269, June 13-14, 1996.

INVITED TALKS

AT CONFERENCES AND RESEARCH MEETINGS

- 1. "Artificial Intelligence Techniques in Electronics Systems Diagnosis", *NAIC Meeting*, University of Massachusetts, Amherst, Massachusetts, November 1989.
- 2. "Research on Concurrent Intrusion Detection in Distributed Systems", *Information Institute Workshop* sponsored by U.S. Air Force Research Laboratory, Blue Mountain Lake, NY, June 2000.
- 3. "Encapsulation of Owner's Intent for Intrusion Detection", *Air Force Research Laboratory*, Rome, NY, May 2002.
- 4. "Test Scheduling in SoCs", 1st Annual New York State Conference on Microelectronics Design, Columbia University, January 2002.
- 5. "Recent Results in Scheduling of Tests in SoCs", 2nd Annual New York State Conference on Microelectronics Design, University of Rochester, January 2003.
- 6. "Overview of Security Research at UB", 1st New York State Cyber Security Symposium, Utica, NY, Feb. 2003.
- 7. "Real-time Intrusion Detection with Emphasis on Insider Attacks", 2nd International Workshop on Mathematical Methods, Models and Architectures for Computer Networks Security, St. Petersburg, Russia, September 2003.
- 8. "Distributed Test Controller Architecture Using Radio-on-Chip Technology", 3rd Annual Microelectronics Design Conference, Columbia University, January 2004.
- 9. "Wireless Security Initiative at UB", *Joint Workshop on Cyber Security* 2006, Buffalo, NY, March 2006.
- 10. "Assessment, Detection and Analysis of Insider Threats", Information Security Meeting, Stony Brook Manhattan Facility, April 2006.
- 11. "ICMAP: An Information centric Modeling Tool for Insider Threat Analysis", 3rd Annual IFIP WG 11.9 International Conference in Digital Forensics, Orlando, FL, Jan. 2007.
- 12. "Understanding Multistage Attacks in the Cyberspace to Address the Grand Challenges in Security", Keynote Talk, 2nd Annual Symposium on Information Assurance, jointly with the 10th Anniversary New York State Cyber Security Conference, Albany, NY, June 2007.
- 13. "Fighting the Scourge on the Internet: Addressing Security Threats to Contemporary Users", *DARPA Tech* 2007, Sidebar Presentation, Anaheim, CA, August 2007.
- 14. "Security Research at UB", *Information Assurance Scholarship Program, NSA Principal's Meeting*, Albuquerque, NM, October 2007.
- 15. "Information Assurance Activities at UB", *Information Assurance Scholarship Program, NSA Principal's Meeting*, Phoenix, AZ, October 2008.
- 16. "Secure and Privacy-Preserving Document Management", *Data and Applications Security, NSF Workshop*, Washington DC, February 2009.

- 17. "Challenges in Mitigating Phishing and Spam e-mails", *Indo-US Workshop on Cyber Security*, *Cyber Crime and Cyber Forensics*, Kochi, India, August 2009.
- 18. "Challenges in Mitigating Phishing and Spam e-mails", *Graduate Education and Research in Information Security*", Binghamton University, October 2009.
- 19. "Cyber Attack Scenario Detection and Statistical Signature Generation Through Heterogeneous Event Stream Analysis", *Indo-US Workshop on Infrastructure Security*, Bangalore, India, Jan. 2010.
- 20. "Tamper-resistant Monitoring of User Space Components", Featured presentation in the Secured/Protected Networks and Systems session, *MILCOM 2011*, Baltimore, MD, Nov. 2011.
- 21. "Insider Threat in Financial Institutions An Analysis and Countermeasures," distinguished invited talk in the *1st International Symposium on Cyber Security*, Nanyang Technological University, Singapore, January 2013.
- 22. "Insider Threat Analysis and Countermeasures," *Institute for Infocomm Research (I2R)*, Singapore, Aug. 2013.
- 23. "Insider Threat Analysis and Countermeasures," *DIMACS/RUCIA Workshop on Information Assurance in the Era of Big Data*, Rutgers University, February 2014.
- 24. "Injecting Cyber Security through Certificate Programs", NSA Centers of Academic Excellence (CAE) Community Meeting, Columbia, MD, November 2014.
- 25. "Surviving Advanced Persistent Threats: A Framework and Analysis", *Regional Symposium on Graduate Education and Research in Information Security (GERIS 16)*, Binghamton University, Binghamton, NY, March 2016.
- 26. "Key Security Issues for Financial Institutions and What can be done to Mitigate Them?", 5th International Program on Information Assurance and Management, Niagara Falls, NY, Aug. 2016.
- 27. "Continuous Authentication Using Behavioral Biometrics", 3rd ACM International Workshop on Security and Privacy Analytics (Co-located with ACM CODASPY 2017), Scottsdale, AZ, March 2017.
- 28. "Security Trends", 3rd CS4ALL Conference, Buffalo State College, Buffalo, NY, October 2019.
- 29. "University at Buffalo, Know Your Fellow CAE-Rs", CAE Community of Practice-Webinar Presentation, February 2024.
- 30. "Detecting Ransomware with Machine Learning and Natural Language Processing Models", 5th *International Conference on Machine Learning, IoT and Big Data (ICMIB)-2025*, Berhampur, India, April 2025 (virtual).

UNIVERSITY COLLOQUIA AND INDUSTRY TALKS

- 1. "Error Detection and Recovery Issues in Real-time Systems", *Department of Electrical Computer Engineering, University of Wisconsin at Madison*, WI, November 1990.
- 2. "Rollback Recovery Techniques in Computer Systems", *Microprocessor Applications Laboratory*, *Indian Institute of Science*, Bangalore, India, May 1991.
- 3. "Fault-tolerant Design of Large Area Array Processors", *Indian Space Research Organization, Bangalore*, India, May 1991.
- 4. "Watchdog Processor Assisted Checkpointing in Distributed Computer Systems", *Coordinated Sciences Laboratory, University of Illinois at Urbana-Champaign*, Urbana, IL, April 1995.
- 5. "Watchdog Processor Assisted Checkpointing in Distributed Computer Systems", *Department of Electrical Computer Engineering, University of Wisconsin at Madison*, WI, April 1995.

- 6. "Test Generation in BIST Environment", IEEE Computer Society Talk, Dept. of Electrical Engineering, Washington University, St. Louis, MO, July 1995.
- 7. "Fault Tolerance and Fault Testing in Multiprocessor Systems A New Perspective", Dept. of Electrical Engineering, University of Texas at San Antonio, May 1996.
- 8. "A Novel Approach to Testing Sequential Blocks", Microprocessor Products Group, *Intel Corporation*, Folsom, CA, July 1998.
- 9. "Overview of VLSI Testing Research at SUNY-Buffalo", *IBM Corporation*, Endicott, NY, Jan. 1999.
- 10. "Concurrent Intrusion Detection in Distributed Systems", *Naval Research Laboratory*, Washington, DC, June 1999.
- 11. "University at Buffalo IBM Partnership Overview", *IBM Corporation*, Endicott, NY, September 2000.
- 12. "Towards an Integrated Real-Time Intrusion Assessment and Recovery Framework for Network Management", *Department of Mathematics, SUNY at Geneseo*, October 2000.
- 13. "A Tamper-resistant Monitoring Framework for Anomaly Detection in Computer Systems", *Brock University*, Ontario, Canada, Feb. 2003.
- 14. "Real-time Intrusion Detection with Emphasis on Insider Attacks", *Polytechnic University*, Brooklyn, NY, October 2003.
- 15. "Information Assurance and Computer Security", *Communication-Electronics Research Development & Engineering Center (CERDEC)*, U.S. Army, Ft. Monmouth, NJ, June 2006.
- 16. "How to Deal with Insider Threats?", Rutgers University, Piscataway, NJ, March 2008.
- 17. "Accelerating Techniques for Rapid Mitigation of Phishing and Spam Emails", *Embedded Systems and Communications Academic Summit, Intel Corporation*, Chandler, AZ, Feb. 2009.
- 18. "Trends and Directions in Computer Security at UB", *Alumni Symposium, University at Buffalo*, Buffalo, NY, April 2009.
- 19. "Higher Education in Computer Security", KVG College of Engineering, Sullia, India, August 2009.
- 20. "Trends in Cyber Security at UB", Amrita University, Amritapuri, India, August 2009.
- 21. "Cyber Security: Challenges for the Future", Western New York Science and Technology Forum, University at Buffalo, October 2009.
- 22. "Cyber-Crime Data Center Issues", *Indraprastha Institute of Information Technology (IIIT)* Delhi, India, July 2010.
- 23. "Data De-Duplication for Computer Security Applications", *IBM Research Lab*, Delhi, India, July 2010.
- 24. "Data De-Duplication for Computer Security Applications", *Amrita University*, Ettimadai, India, July 2010.
- 25. "Inferring Source of Information Leak in Document Management Systems", *Adobe India Research Lab*, Bangalore, India, July 2010.
- 26. "Data De-Duplication for Computer Security Applications", *Department of Industrial and Systems Engineering, Rutgers University*, October 2010.
- 27. "Cyber Security: Fundamentals and Challenges for the Future", *C.R.Rao Advanced Institute of Mathematics, Statistics and Computer Science (AIMSCS)*, Hyderabad, India, January 2012.

- 28. "Mitigating Insider Threats and Information Leak in High Value Systems", *C.R.Rao Advanced Institute of Mathematics, Statistics and Computer Science (AIMSCS)*, Hyderabad, India, January 2012.
- 29. "Protecting Security Systems from Subversion Attacks on the Internet", *C.R.Rao Advanced Institute of Mathematics, Statistics and Computer Science (AIMSCS)*, Hyderabad, India, January 2012.
- 30. "Cyber Security: Fundamentals and Challenges for the Future", *Indian Institute of Technology, Bhubaneswar, India,* January 2012.
- 31. "Mitigating Insider Threats and Information Leak in High Value Systems", *Indian Institute of Technology*, *Bhubaneswar*, *India*, January 2012.
- 32. "Data De-Duplication for Computer Security Applications", *Indian Institute of Technology, Bhubaneswar, India,* January 2012.
- 33. "Software Security and Secure Programming Practices An Overview", *Indian Institute of Technology, Bhubaneswar, India,* January 2012.
- 34. "Insider Threats in Financial Organizations An Analysis and Countermeasures", *Institute for Development and Research in Banking Technology*, Hyderabad, India, May 2012. (Skype Presentation)
- 35. "Insider Threat Analysis and Countermeasures," *Infocomm Centre of Excellence*, Nanyang Technological University, Singapore, July 2013.
- 36. "Situation Awareness and Cyber Early Warning A Look into Attack Semantics and Data Dimensionality," *Infocomm Center of Excellence, Nanyang Technological University*, Singapore, Aug. 2013 (Co-organized by IEEE Communications Singapore Chapter).
- 37. "Insider Threat Analysis and Countermeasures," *Birla Institute of Technology and Science*, Pilani, Rajasthan, India, December 2013.
- 38. "Protecting Operational Technologies and Intellectual Property of Cyber-Physical Systems from Ransomware Attacks", *Research Experiences for Undergraduates Forum*, University at Buffalo, July 2022.
- 39. "Engineering Science MS with Course Focus on Cybersecurity", Webinar for Prospective Students, February 2023.
- 40. "Cybersecurity at UB", Presentation to Buffalo Renaissance Board, Buffalo, NY, April 2023.

RESEARCH IN THE NEWS

NewScientist	October 2002	"Software Predicts User Behavior to Stop Attacks"
Scientific American	December 2002	"Keyboard Cops", News Scan Brief on Computer Security.
Washington Post	January 2003	"Mission: Find Intruders Instantly", Associated Press article on 1/23/2003, page E09.
USA Today	January 2003	"Profiling Software Provides New Security Against Hackers", 1/20/2003.
San Francisco Chronicle	January 2003	"Anti-hacking Software", 1/21/2003.
Times Union,	January	"Anti-hacker Profiling Shows Promise", 1/20/2003.

Albany	2003	
Yahoo.com, MSNBC.com	January 2003	"Profiling the Hackers", 1/20/2003.
Computer Crime Research Center	January 2003	"Profiling Software Ferrets out Hackers", 1/20/2003.
CBSNEWS.com	January 2003	"Getting a Handle on Hack Attacks", 1/21/2003.
Wired News	January 2003	"How to Foil Data Thieves, Hackers", 1/20/2003.
SecurityFOCUS	January 2003	"New Software Aims to Snare Computer Intruders in Real-time", 1/20/2003.
AFMC Public Affairs	October 2003	"AFRL Computer Security Research Improves Insider Attack Detection."
Buffalo News	October 2012	"Training cyber defenders," October 28, 2012.
Infosecurity Magazine	March, 2013	"Hack to the Future," March 5, 2013.
NBC News Channel 2	November 2013	"Countermeasures to Power Facility Cyber Attacks," November 14, 2013.
NBC News Channel 2	November 2013	"Training Cyber Warriors," November 23, 2013.
CBS News Channel 4	April 2017	"Cyber Attacks on Nuclear Facilities," April 27, 2017.
Fox News	May 2017	"WannaCry Ransomware Attack: How Average Computer Users Contributed to Global Spread of Malware," May 15, 2017.
Buffalo Business First	June 2017	"University at Buffalo Expert on Changing World of Cybersecurity," June 2, 2017.
Buffalo Business First	March 2018	"Thought Leaders – Data Breaches and Cyber Risks," Industry Roundtable, March 15, 2018.
CBS News Channel 4	Sept. 2018	"UB Trains Future 'Cyber Sleuths' with \$2.39 Million Award," Sept. 5, 2018.
Washington Examiner	April 15, 2021	"US Cybersecurity and Infrastructure Security Agency gets a meager increase in Biden's budget" https://www.washingtonexaminer.com/policy/technology/cybersecurity-infrastructure-security-agency-gets-meager-increase-biden-budget
Buffalo Business First	July 2022	"Thought Leaders – Cybersecurity," Industry Roundtable, July 21, 2022.
Research.com	August 2024	Interview with Cybersecurity Experts: Answering Students' Questions About Cybersecurity Trend, August 8, 2024.